

NBSIR 73-235

**FIELD SERVICE TEST MODEL:
COMPUTER-CONTROLLED U SYSTEM.
OPERATIONS MANUAL AND SOFTWARE
OPERATING SYSTEM APPENDICES FOR
THE AUTOMATED SYSTEM. VOLUME II.**

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Instruction Manual

Prepared for
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FIELD SERVICE TEST MODEL: COMPUTER-CONTROLLED U SYSTEM

OPERATIONS MANUAL AND SOFTWARE OPERATING
SYSTEM APPENDICES FOR THE AUTOMATED SYSTEM

VOLUME II

INTRODUCTION

Volume II of the "Automated U System" describes the software listings and the operator's manual. This volume is intended to instruct the station technician in the procedure to load programs and operate, under computer control, the automated U system. Other manuals describe the system and the technical design and are contained in the following manuals:

Volume I. Field Service Test Model: Computer-Controlled U System. Operations Manual and Software Operating System Appendices for the Automated System.

Field Service Test Model: Computer-Controlled U System. Manual for Receiver and ADACS Interface Chassis.

Field Service Test Model: Computer-Controlled U System. Manual for Test Unit.

Field Service Test Model: Computer-Controlled U System. Manual for Computer Subsystem.

Field Service Test Model: Computer-Controlled U System. Manual for System.

Certain commercial equipment and materials are identified in this paper in order to adequately specify the components used. In no case does such identification imply recommendation or endorsement by the National Bureau of Standards, nor does it imply that the material or equipment identified is necessarily the best available for the purpose.

APPENDIX I

INT/EXT CROSS REFERENCE TABLE

AL 2

	E\$INT	E\$SKD	E\$T\$SI	E\$CLK	E\$UIN	E\$SUB	E\$OMI	E\$T28	E\$T35	E\$MAG	U\$RTA	U\$LSQ	U\$TGR	U\$WFA	U\$DAT	U\$REP	U\$RED	U\$PST	M\$ESC	M\$LEV	M\$STD	E\$LOG	E\$BDC	E\$FRT	E\$DIF	E\$FME	E\$SRT	F\$FMT	F\$DMP
B:IOR			I																								X		
B:ORT		X			I																						X		
B:RET		I	X																										
B:RS1		I	X																										
B:RS2		I	X																										
B:STT		I				X	X			X																	X		
E:28F						X	X																				X		
E:280																											X		
E:35I																											X		
E:350																											X		
E:35S																													
E:3IC																													
E:ABR																													
E:ANF																													
E:ANL																													
E:BAD																													
E:BDC																													
E:BDF																													
E:BSG																													
E:BSK																													
E:COT																													
E:DIF																													
E:FAM																													

NOTE: I denotes internal reference, X denotes external reference.

	E:INT	E:SKD	E:TST	E:CLK	E:UIN	E:SUB	E:OMI	E:T28	E:T35	E:MAG	U\$RTA	U\$LSQ	U\$TGR	U\$WFA	U\$DAT	U\$REP	U\$RED	U\$PST	M\$ESC	M\$LEV	M\$STD	E\$LOG	E\$BDC	E\$FRT	E\$DIF	E\$FMF	E\$SRT	F\$MT	F\$DMP
E:FCT																													
E:FMF											X																		
E:FRT	X					X	X				X			X		X	X			X	X								
E:I28	X							I																					
E:I35	X								I																				
E:IOF	X		X			X	X				X			X															
E:LOG		X		X						I	X								X										
E:MAG		X								I																			
E:MGS			X							I																			
E:MIN	X																												
E:MIT	X																												
E:MTI	X																												
E:O28	X																												
E:O35	X		X																										
E:OMI																													
E:PAR	X																												
E:PWD	X																												
E:SEC	X																												
E:SF1	X																												
E:SKD	X																												
E:SRT																													
E:T01																													
E:T02																													

	E\$INT	E\$SKD	E\$TSI	E\$CLK	E\$UNIN	E\$SUB	E\$OMI	E\$T28	E\$T35	E\$MAG	U\$RTA	U\$LSQ	U\$TGR	U\$WFA	U\$DAT	U\$REP	U\$RED	U\$PST	M\$ESC	M\$LEV	M\$STD	E\$LOG	E\$BDC	E\$FRT	E\$DIF	E\$FMF	E\$SRT	F\$FMT	F\$DMP
E.T03		I			X		X																						
E.T04		I	X																										
E.T05		I					X																						
E.T06		I						X																					
E.T07		I																											
E.T08	X	I		X			X																						
E.T09		I																											
E.T10		I				X																							
E.T11		I							X																				
E.T12		I						X																					
E.T13		I					X																						
E.T14		I		X																									
E.T15		I	X				X			X																			
E:T28		X						I																					
E:T35		X							I																				
E.TM1											X					X													
E.TM2																													
E.TM3																													
E.TSF	X	I	X																										
E:TSI	X		I																										
E.TSK		I																											
E.UBF																													
E.UBU	X																												

	E\$INT	E\$SKD	E\$TSI	E\$CLK	E\$UIN	E\$SUB	E\$OMI	E\$T28	E\$T35	E\$MAG	U\$RTA	U\$LSQ	U\$TGR	U\$WFA	U\$DAT	U\$REP	U\$RED	U\$PST	M\$ESC	M\$LEV	M\$STD	E\$LOG	E\$BDC	E\$FRT	E\$DIF	E\$FMF	E\$SRT	F\$FMT	F\$DMP
E:UIN					I																								
F:ANL		X																											
F:BBT		X																											
F:CHK																													
F:CVT																													
F:DBF																													
F:DDD																													
F:DMP																													
F:FMT																													
F:FST																													
F:MTP																													
F:TIM																													
F:TRD																													
F:USE																													
M:ESC																													
M:GNT																													
M:INT																													
M:LEV																													
M:STA																													
M:STD																													
M:TM1																													
M:TRT																													
M:UPT																													

	E\$INT	E\$SKD	E\$TSI	E\$CLK	E\$UNIN	E\$SUB	E\$OMI	E\$T28	E\$T35	E\$MAG	U\$RTA	U\$LSQ	U\$TGR	U\$WFA	U\$DAT	U\$REP	U\$RED	U\$PST	M\$ESC	M\$LEV	M\$STD	E\$LOG	E\$BDC	E\$FRT	E\$DIF	E\$FME	E\$SRT	F\$FMT	F\$DMP
U.AMP											X																		
U.AST	X										X		X	X	I		X				X								
U.BGF														X	I														
U.BLL	X				X						X				I							X							
U.CCL														I															
U.CCN	X										X		X		I														
U.CDM													X	I															
U.CDT													X	I															
U.CLS													X	I															
U.CMP													X	I															
U.CNI											X		X		I														
U.CPC	X										X		X		I														
U.CPP											X		X		I														
U.CST	X										X		X		I														
U.CTM											X		X		I														
U.CYC											X		X		I														
U.DIU	X										X		X		I														
U.DFM											X		X		I														
U.END											X		X		I														
U.FAC	X										X		X		I														
U.FRQ											X		X		I														
U.GAN	X										X		X		I														
U:GET													X		I														

	E\$INT	E\$SKD	E\$T\$SI	E\$CLK	E\$UN	E\$SUB	E\$OMI	E\$T28	E\$T35	E\$MAG	U\$RTA	U\$LSQ	U\$TGR	U\$WFA	U\$DAT	U\$REP	U\$RED	U\$PST	M\$ESC	M\$LEV	M\$STD	E\$LOG	E\$BDC	E\$FRT	E\$DIF	E\$FME	E\$SRT	F\$FMT	F\$DMP
U. IHB	X										X																		
U. LFZ																													
U. LSP											X																		
U:LSQ											X																		
U. LST											X																		
U. LTF											X																		
U. LTM											X																		
U. OAM											X																		
U. ODI											X																		
U. ODR											X																		
U. OFZ											X																		
U. OSG											X																		
U. OTB											X																		
U. OTM											X																		
U: PST																													
U: RED																													
U: RP1																													
U: RP2																													
U: RP3																													
U: RTA																													
U. RTY																													
U. SIG																													
U. SLP																													

	E\$INT	E\$SKD	E\$T\$1	E\$CLK	E\$UN	E\$SUB	E\$OMI	E\$T28	E\$T35	E\$MAG	U\$RTA	U\$LSQ	U\$TGR	U\$WFA	U\$DAT	U\$REP	U\$RED	U\$PST	M\$ESC	M\$LEV	M\$STD	E\$LOG	E\$BDC	E\$FRT	E\$DIF	E\$FMF	E\$SRT	E\$FMT	E\$DMP
U:TGR												X	I																
U.TM1	X			I																									
U.TM3	X										I																		
U.TM4	X										I																		
U.TRF	X										X																		
U.TRK	X										X																		
U.TRL											X																		
U.UDR																													
U:WFA													X																
U.WRK														I															
W.LAM											X																		
W.LPK																													
W.LTM																													
W.ONR																													
W.OST																													
W.PAM																													
W.PFZ																													
W.PTM																													
W.RAM																													
W.RFZ																													
W.RTM																													
W.VAM																													
W.VPK																													

E\$INT	
E\$SKD	
E\$TSI	
E\$CLK	
E\$UNIN	
E\$SUB	
E\$OMI	
E\$T28	
E\$T35	
E\$MAG	
U\$RTA	
U\$LSQ	
U\$TGR	
U\$WFA	X
U\$DAT	1
U\$REP	X
U\$RED	
U\$PST	
M\$ESC	
M\$LEV	
M\$STD	
E\$LOG	
E\$BDC	
E\$FRT	
E\$DIF	
E\$FME	
E\$SRT	
E\$FMT	
E\$IMP	

M. VTM

APPENDIX II
NUSTAD MEMORY MAP

AII. 1

XXAP		
P	ESINT	0000
	F:ANL	0169
	E:ANF	0100
P	ESSKD	01A3
	E.T15	01BF
	E.T14	01C0
	E.T13	01C1
	E.T12	01C2
	E.T11	01C3
	E.T10	01C4
	E.T09	01C5
	E.T08	01C6
	E.T07	01C7
	E.T06	01C8
	E.T05	01C9
	E.T04	01CA
	E.T03	01CB
	E.T02	01CC
	E.T01	01CD
	B.STT	01EA
	B.RS2	01F3
	B.RS1	01EC
	E.TSX	01BE
	E.TSF	01EB
	E:BSX	01CF
	E:SKD	01A3
	B.RET	01F9
P	ESSUB	01FA
	E:PWD	01FA
	B:OKT	025D
	E:ABK	025A
	E:PAK	0224
P	ESCLK	02A4
	M:TRT	032F
	M:UPT	042B
	U:TM1	0448
	M:GNT	03BB
	E:TM3	0449
	E:MIN	02D2
	E:SEC	02A4
P	ESTSI	0468
	E:TSI	0468
	B:IOK	047C
P	USRKD	049F
	U:RED	049F
P	USRTA	0569
	U:RTA	0569

	U. TM4	093A
	U. TM3	0939
	U: CNI	07F5
	U. SIG	08D2
P	USLSQ	094B
	U:LSQ	094B
P	ISTGR	09BE
	U. TRL	0CD3
	U. TRF	0B05
	U: TGR	09BE
	U: GET	0AA9
P	USWFA	0D00
	U. LTM	1037
	U. LFZ	1029
	U: WFA	0D00
	U. CDT	1053
	U. CDM	1061
	U. CCL	10B8
P	ESJMI	10DE
	E: JMI	10DE
P	USREP	1246
	U: RP3	1397
	U: RP2	133F
	U: RP1	1246
	U. UDR	1865
P	ESLJG	1AC1
	U. FRU	1B91
	E: LJG	1AC1
P	ESUIN	1BAD
	E: ANL	1C2D
	E: UIN	1BAD
	E: MIT	1C30
	E. SF1	1E2C
	E. UBU	1C48
	E. UBF	1C72
P	MSESC	1E2D
	M. STA	1EDB
	M: ESC	1E36
	M. TM1	1EDA
	M: INT	1E2D
P	MSSTD	1EF7
	M: STD	1EF7
	E: FAM	1FF4
P	MSLEV	209A
	M: LEV	209A
P	USPST	21D0
	U: PST	21D0
P	ESFRT	22E1
	E: FRT	22E5
	E. TM2	2329
	E. TM1	2323
	E: FCT	22E1
P	ESBDC	232A

	E.BSG	2358
	E.BDF	2359
	E.BDC	232A
	E.BAD	232D
P	E\$FMF	235E
	E:FMF	235E
P	E\$DIF	23A1
	E:CJT	23A1
	E:DIF	23A7
P	E\$SRT	23FB
	E:SRT	23FB
P	E\$MAG	243A
	E:MTI	243A
	E:MGS	246F
	E:MAG	243D
P	E\$T35	24FC
	E.350	261E
	E.35I	262F
	E:35S	260B
	E:T35	24FF
	E:J35	25C4
	E:I35	24FC
	E.IDF	2792
	E.3IC	267F
P	E\$T28	2793
	E.280	3073
	E.28F	3077
	E:T28	2796
	E:J28	27C3
	E:I28	2793
P	USDAT	3080
	U.CYC	3507
	W.VTM	3691
	W.KTM	36C9
	W.PTM	3605
	W.LTM	3659
	W.JST	362F
	U.RTY	3531
	U.DTM	3577
	U.LTF	3088
	U.DIM	3593
	U.CTM	3345
	U.JTB	3595
	W.VPK	3633
	W.LPK	364B
	U.LST	30C8
	U.LSP	3154
	U.CST	353F
	U.JSG	35E9
	U.AST	354D
	U.WRK	3163
	U.TRK	3060
	W.JNR	363D
	U.CPP	34EB

U. CPC	34F9
U. END	3569
U. CMP	3347
U. AMP	36F3
U. SLP	3155
U. CLS	355B
U. BLL	309C
W. KFZ	36BB
W. PFZ	35F7
U. DIU	30AA
U. IHB	3523
U. BGF	30B9
U. JFZ	35BF
W. VAM	36AD
W. RAM	36E5
W. PAM	3621
W. LAM	3675
U. JDR	35CD
U. JDI	35DB
U. CCN	3515
U. JAM	35B1
U. GAN	308E
U. FAC	30BA
P F\$FMT	3701
F: CVT	378E
F: MTP	381A
F: USE	381B
F: TRU	3811
F: FMT	3701
F: CHK	371D
F: DBF	3842
P F\$DMP	3865
F: FST	3942
F: DMP	3865
F: TIM	3A1D
F: DDD	3889
F: BBT	3AD3
*HIGH	3AFC
*XFER	0100
*BIAS	0000

APPENDIX III

PROGRAM USER'S MANUAL

SECTION I. OPERATING PROCEDURES.

I. 1 SYSTEM START-UP.

- a. Turn on rack power in all three processor racks, Teletype machines, ADACS, receiver, etc.
- b. Put the ASR 35 and the ASR 28 in the on line mode of operation.
- c. Put the ADACS in the auto/normal mode.
- d. Mount the SAL (MODCOMP Stand Alone Linking Loader, MODCOMP No. 600000-012) program paper tape in the PTR.
- e. Turn on the power to the PTR, depress the sync button for approximately 2 seconds. Ensure that no part of the data has passed over the read head.
- f. Set the data entry switch to a HEX A (Swt 12 and 14 set to a one).
- g. Depress Master Clear.
- h. Depress Fill.
- i. HALT/RUN to RUN.
- j. The SAL tape will now load. The ASR 35 will type out the following message:

SAL @ 787D, CSW = BIAS, Type "I" for commands
- k. Set all data entry switches to zero.

l. Turn power off to the PTR, remove and rewind the SAL tape.

m. Mount "NUSTAD" part I tape, turn power on to the PTR and sync the tape as in e above.

n. Type the characters PR and carriage return on the ASR 35. This will cause the NUSTAD tape to begin loading into the processor. When the tape halts at the end, turn power off to the PTR, mount the NUSTAD part 2 tape, restore power and sync the PTR. Depress the carriage return key on the ASR 35 and the tape will continue to load. If the load was successful, the ASR 35 will type out "SATISFIED." If during the loading procedure any other message is typed out or if no message is typed at the end of NUSTAD part 2, attempt to reload the system. If it fails to load again begin maintenance on the processor.

o. Type "GO" and a carriage return on the ASR 35 and the system will begin operation. This will cause a "System Restart" (See Figure I. 1) to be typed on the ASR 28. This will be followed by a status display message on the ASR 28.

p. The system is now in automatic operation, refer to Section II for operator options.

q. If any of the receiver channels do not respond to the initial parameter values established by the program, the operator may use the "OPC" option as described in paragraph II. 4 to adjust the gain

and/or blanking levels for proper operation. The operator should ensure that there is sufficient signal strength to the receiver before adjusting gain to maximum.

I. 2 NORMAL OPERATION.

a. No operator intervention is required for normal operation.

He may exercise the options described in Section II.

b. **MANUAL UNATTENDED OPERATION:** Prior to departing the station at the end of a duty day the operator will:

1. Set all receiver controls to the average settings as determined from the previous days Parameter History for the upcoming 16 hour period. See Section III. 1 for correlation between NUSTAD and EECO values.

2. If the processor should fail, the system will automatically return the receivers to the manual mode of operation.

I. 3 MAINTENANCE.

Prior to taking a channel down for maintenance the operator will place the Normal/Maintenance switch on the ADACS to the maintenance position. When maintenance is complete return the switch to the normal position.

.....SYSTEM RESTART AT 023 215549.....

023 215549 CURRENT RECEIVER STATUS, DET 215

CHAN	FREQ	C	I	M	AM	GAIN	TR	BLL	DIURNAL	SIGMA	AMPLITUDE
A	17.8	1	1	0	A						
B	21.0	0	1	0	A						
C	21.0	0	1	0	A						
D	16.0	1	1	0	A						
E	22.3	0	1	0	A						
F	18.6	1	1	0	A						
G	24.0	4	1	0	A						
H	ZERO INPUT...										
I	17.8	7	1	0	A						
J	ZERO INPUT...										
K	ZERO INPUT...										
L	ZERO INPUT...										
M	24.0	7	1	0	A						
N	17.8	7	1	0	A						

023 215554 A IR
023 215554 A CC05
023 215554 B IR
023 215554 B CC05
023 215554 C IR
023 215554 C CC01
023 215554 D IR
023 215554 D CC05
023 215554 E IR
023 215554 E CC01
023 215554 F IR
023 215554 F CC05
023 215554 G IR
023 215554 G CC05
023 215554 I IR
023 215554 M IR
023 215554 N IR
023 215620 D GC20
023 215642 H GC20
023 215708 A BC05
023 215713 B BC05
023 215718 C BC05
023 215723 D BC05
023 215729 E BC05

Figure I.1
SYSTEM LOG OF NORMAL INITIAL STARTUP

SECTION II. OPERATOR/NUSTAD COMMUNICATIONS.

II. 1 GENERAL.

Normally, NUSTAD waits for the operator to initialize an action request via the ASR 35 Teletype. This is referred to as NUSTAD being in the REQUEST MODE. When an action request message has been received, NUSTAD may require additional information from the operator. An inquiry message will be typed on the ASR 35 and NUSTAD will enter the RESPONSE MODE awaiting the information from the operator.

The operator may return to the REQUEST MODE at any time by depressing the CONSOLE INTERRUPT switch on the processor front panel. The following message is typed, and NUSTAD returns to the REQUEST MODE:

BACKGROUND TASK ABORTED BY OPERATOR

The list of action request options is limited, but allows the operator to enter messages into the station log, review DRFUD data tapes, and determine the system status on command.

II. 2 CALLING NUSTAD.

The operator may request the attention of NUSTAD by typing the / character. If NUSTAD is in the REQUEST MODE and ready to

process an action request, it will respond by crossing the / to form the letter X and spacing over. NUSTAD is now ready to interpret a keyword action request.

Example: Operator calls: /
 NUSTAD responds: X

II. 3 OPERATOR ACTION REQUESTS.

Once the operator has NUSTAD's attention, he may enter an action request by typing a keyword from the following list:

<u>Keyword</u>	<u>Resulting Action</u>
DFC:	Execute DRFUD format checking program (See Section II. 8. 1 for programs operation).
DDD:	Execute DRFUD data display program (See Section II. 8. 2 for program operation).
STD:	Generate the CURRENT RECEIVER STATUS message on the ASR 28.
CRC:	Perform a CHANNEL REDUNDANCY CHECK.
OPC:	Perform an operator initiated parameter change (See Section II. 4 for format).
LOG:	Enter an operator generated message into the station log (See Section II. 5 for format).

II. 4 THE OPC OPTION.

The operator can request NUSTAD to make changes in parameter settings for individual sensor channels. NUSTAD will make the change

unless there is a signal in progress on the requested channel, in which case NUSTAD will inform the operator of the fact. If a change is made, the new value will be logged.

The format for the request is: X OPC:C, F, VV

WHERE: C is the channel for which the change is requested (A thru N).

F is the function indicating which parameter is to be changed.

Use: B for Blanking Level

G for RF Gain

T for Tracking Rate

V is a two digit value to which the requested function is to be set. Permissible ranges are:

Blanking Level (00 thru 63)

RF Gain (00 thru 24)

Tracking Rate (00 thru 09)

NOTE: See Section III. 1 for correlation between the NUSTAD and EECO setting values.

II. 5 THE LOG OPTION.

The operator may enter information into the station log by using the LOG option. The text of the message he generates on the ASR 35 will be logged on the ASR 28.

The format used is: X LOG: (Message text)

The text of the message is terminated by depressing the return key.

II. 6 COMMUNICATION ERROR MESSAGES.

When NUSTAD interprets operator messages, it informs the operator of any errors it encounters. These are generally self-explanatory, but a further explanation is given below.

INVALID SYSTEM CALL - If NUSTAD is in the REQUEST MODE and the first character encountered is not a /, the message is ignored and an invalid system call message is typed.

INVALID KEYWORD, MSG IGNORED - If the first four characters after NUSTAD responds to the operator's call are not given in the keyword list above, the message is ignored and NUSTAD returns to the REQUEST MODE.

The following messages apply specifically to the OPC option:

INVALID CHANNEL - Channel designation was not an alphabetic character between A and N. Parameter change was not permitted.

INVALID FUNCTION - Function designation was not B, G, or T. Parameter change was not permitted.

VALUE NOT WITHIN LIMITS - The requested new value was not within the limits specified above for the function.

INVALID FORMAT - The message was not entered in the proper format. This message usually reflects a misplaced or missing comma in the message.

See Section II. 8 for interpretation of error messages associated with the DFC: and DDD: options.

II. 7 MESSAGE EDITING.

While the operator is preparing a message he may correct typing errors by using the following editing features:

RUB OUT - depressing the rub out will cause NUSTAD to ignore the last character typed. The first RUB OUT will delete the most previous character.

Immediately typing a second will delete the character just prior to that, etc. When a RUB OUT attempts to delete a nonexistent character, i. e., one prior to the first character of the keyword, the entire

message is ignored and NUSTAD returns to the
REQUEST MODE.

CANCEL - Simultaneously depressing the CONTROL and
X keys will cause NUSTAD to ignore the entire message.

II. 8 BACKGROUND PROCESSING OF DRFUD DATA.

NUSTAD provides for tape format error detection and a data display of DRFUD data in a "useable" format. The error checking routine may be used separately or in conjunction with the display routine.

The use of bit position 0, 1, and 2 of the switch register provide more versatility in using these programs. Prior to starting each format check or data display, the operator should decide which switch options he desires and set these switches accordingly.

II. 8.1 DRFUD FORMAT CHECK OPTION (DFC:).

The operator initiates a format check by mounting a DRFUD tape on the tape drive and calling NUSTAD as described in Section II. 2 (calling NUSTAD). After NUSTAD acknowledges, the operator types:

DFC:

followed by a return character. The processor will respond with:

NO. OF BLOCKS TO BE CHECKED

This request is for the number of DRFUD scans the operator wishes to have checked; each scan should be equal to one second of DRFUD data. The maximum request for one check is limited by the computer word size to 32,767 scans, which is just over 9 hours and 22 minutes of recorded DRFUD data. Checking more data than this will require that it be broken down into smaller segments.

NO. OF BLOCKS TO BE CHECKED	AMOUNT OF RECORDED DRFUD DATA CHECKED
1	1 Sec
60	1 Min
300	5 Min
600	10 Min
3600	1 Hour
32767	9 hrs 22 min 47 sec
(Max number of scans)	(Max data checked at one time)

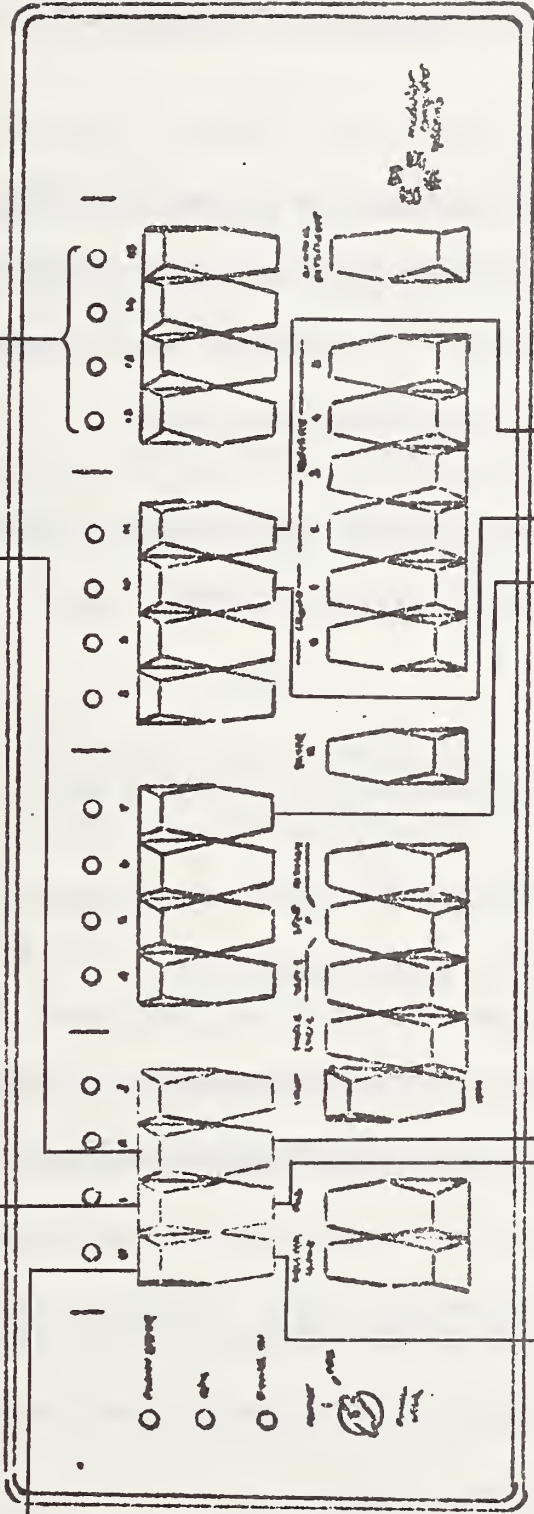
TABLE II. 1
BLOCK COUNT TO TIME CONVERSION

Bit 1 Reset - Processor continues using data from the buffer now in memory.

Bit 0 Reset - Background task is aborted after detecting first format error.

Bit 2 Reset - Parity check performed (even parity expected).

Bits 12, 13, 14, & 15 - Channel designation for compressed data display (Ref Table 12.1).



Bit 11 Set - Enables compressed data display.

Bit 10 Set - Enables signal reject message display (Ref Table 15.1).

Bit 7 Set - Enables special operator options (Sect 8.1).

Bit 2 Set - Parity errors ignored.

Bit 1 Set - Each time DFC or DDD is called, the storage buffer (MTBUF) is refilled with new DRFUD data.

Bit 0 Set - Program does not terminate input options (Sect 8.1).

FIGURE II.1

After receiving the processor's request for the number of data blocks, the operator should type a positive integer no greater than 32767 followed by a return character. The processor then responds in one of three ways:

1. If the tape or tape unit is not ready, NUSTAD outputs a message indicating an improper tape drive condition. The operator should correct the improper condition and continue.
2. If the operator errs in his response, NUSTAD outputs: "INVALID CHARACTER, TASK ABORTED."
3. Reads and checks format on DRFUD tape.

Once the background task is aborted, the operator corrects the error by recalling NUSTAD and starting over.

Once the format checking process has begun, it will continue until a termination statement is output, which will be one of the following:

1. "TASK COMPLETED" indicates the requested amount of data has been checked and no errors were detected.

2. "FORMAT CHECK TERMINATED" indicates that an error was detected and Bit 0 of the switch register was reset.

The error message indicates which bit is in an improper state (See Figure II. 2) and displays the scan in which the error was detected. Once a termination message is output, NUSTAD returns to the REQUEST MODE.

II. 8. 2 DRFUD DATA DISPLAY OPTION (DDD:).

To call for a DRFUD DATA DISPLAY, the operator calls NUSTAD and enters

DDD:

followed by a return character. NUSTAD responds with:

SPECIFY ACTION DESIRED, 1, 2, OR 3

ACTION OPTION 1

OPTION 1 requires that the operator specify the data times at which the display is to begin and end. First NUSTAD requests:

SPECIFY START TIME DESIRED

The operator should respond with three digits of DOY, a space character, and six digits of TOD, followed by a return character.

NUSTAD will attempt to locate the requested start time on the DRFUD tape. Once the proper start time has been located, NUSTAD requests:

SPECIFY STOP TIME DESIRED

The operator should respond as above. NUSTAD begins displaying the DRFUD data at the data time requested and continues with one output record for each second of the requested time interval.

ACTION OPTION 2

OPTION 2 requires that the operator specify the data time at which the display is to begin and a number of data blocks (scans) that he wishes to have displayed.

The start time is requested and entered as above. NUSTAD then requests:

NO. OF BLOCKS TO BE DUMPED

The operator should respond with a positive integer less than 32767 followed by a return character. NUSTAD begins displaying the DRFUD data at the data time requested and continues until the requested number of scans has been displayed.

ACTION OPTION 3

OPTION 3 requires that the operator specify only the number of data blocks (scans) to be displayed. A start time is not required since

the display is started wherever the DRFUD tape is positioned. NUSTAD requests:

NO. OF BLOCKS TO BE DUMPED

Operator response should be as above. NUSTAD begins displaying DRFUD data and continues until the requested number of scans has been displayed.

If the DRFUD tape is positioned past a start time requested in OPTION 1 or 2, a message will be output stating this. The operator should rewind the tape and try again, ensuring that Bit 1 of the switch register is set. If the same message is output on the second try, the desired time is not contained on the DRFUD tape.

The display routine works in conjunction with the format check and if during the display process an error is detected, the Bit in error is noted and the scan containing the error is displayed. NUSTAD checks Bit 0 of the switch register to determine whether to abort the task or continue after each detected error.

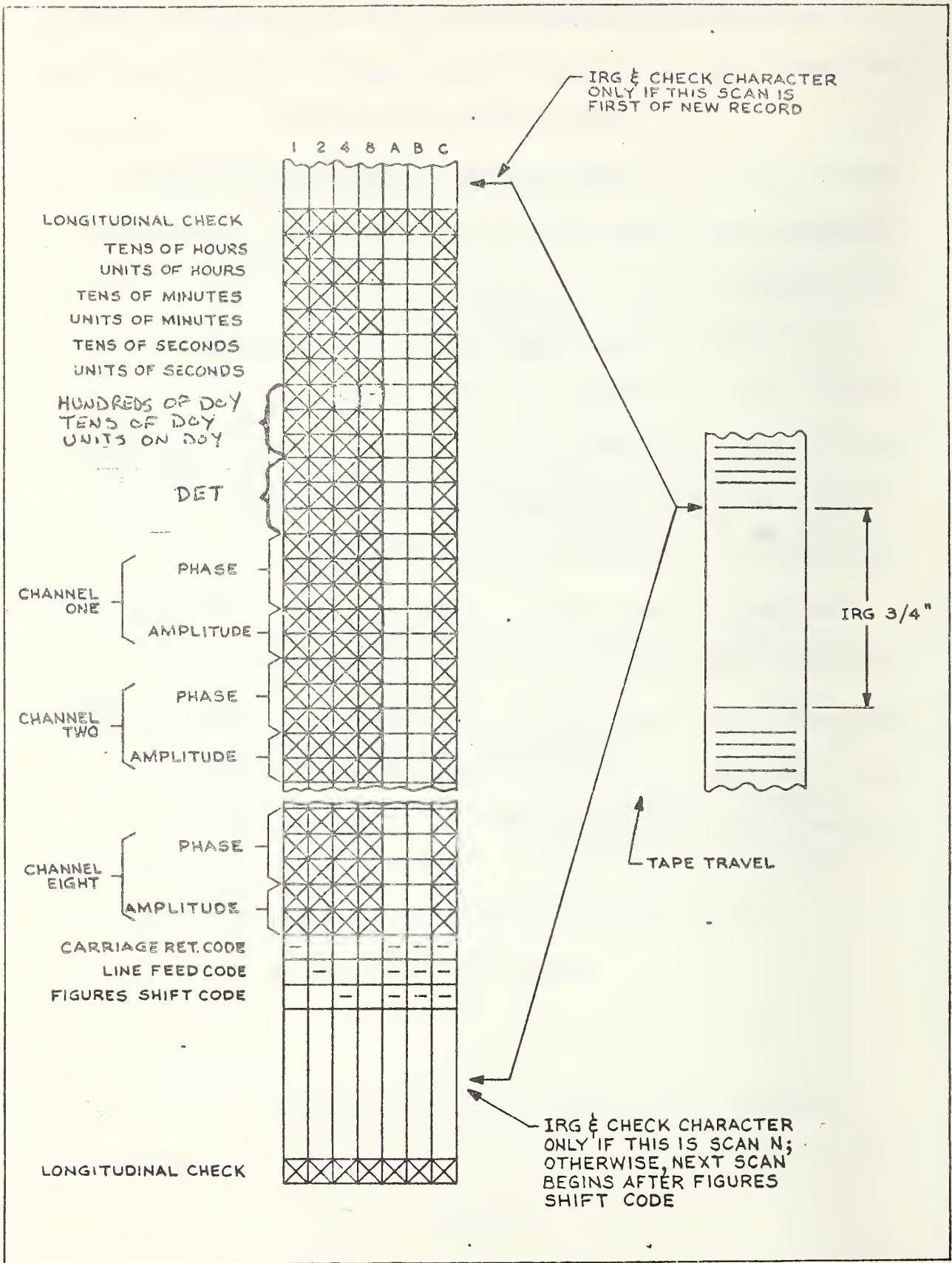


FIGURE II. 2

DRFUD TAPE FORMAT (NEW U SYSTEM)

X DFC:

{ OPERATOR CALLS FOR A DRFUD
FORMAT CHECK.

NO. OF BLOCKS TO BE CHECKED?4096

{ OPERATOR WANTS 4096 SCANS TO
BE CHECKED.

FORMAT ERROR BIT 8

{ SOMEWHERE IN THE CHECKING
PROCESS, THE ROUTINE DETECTED
AN EOF CODE AS IS SHOWN IN
THE DUMP AS A ??.

TJD ??0925 DJY 024 DET 215

73.5 42 58.8 33 73.4 31 00.1 04 28.1 39 00.2 39 53.2 40 00.1 05 313234

FORMAT CHECK TERMINATED

{ TERMINATION STATEMENT
INDICATING THAT BIT 0
OF THE SWITCH REG WAS SET.

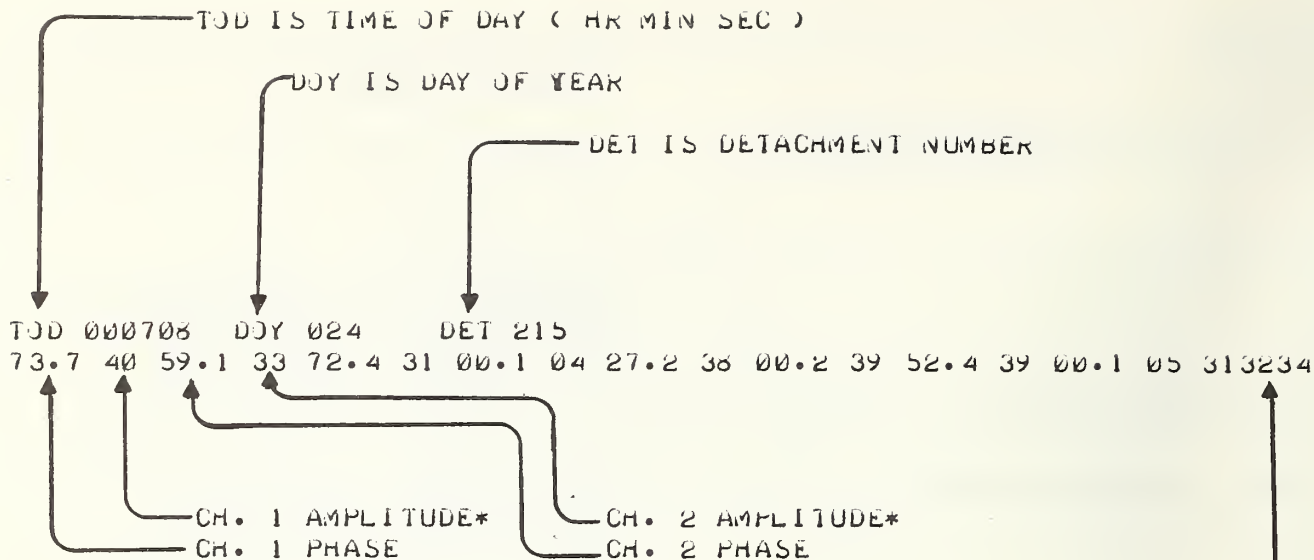
X DFC:

NO. OF BLOCKS TO BE CHECKED?700

TASK COMPLETED

{ TERMINATION STATEMENT INDICATES
CHECK OF ALL 700 SCANS DETEC-
NO ERRORS. THERE IS NO DUMP
INITIATED IF NO ERRORS ARE
DETECTED.

FIGURE II. 3
EXAMPLE: DRFUD FORMAT CHECK.



FIXED TRAILER CHARACTERS
 SHOULD ALWAYS BE 313234
 IF A CHARACTER IS DROPPED
 SOMEWHERE IN THE SCAN, THESE
 CHARACTERS WILL SHIFT TO THE
 LEFT.

* ALL EIGHT CHANNELS OF AMPLITUDE AND PHASE ARE
 DUMPED.

FIGURE II. 4
 EXAMPLE: DRFUD DATA DUMP FORMAT.

X DDD:

DRFUD DATA DUMP CALL

SPECIFY ACTION DESIRED, 1, 2, OR 3. 1

MACHINE RESPONSE & OPERATOR DESIRE.

SPECIFY START TIME DESIRED. 024 000608

INITIAL MACHINE RESPONSE ON OPTION 1. TIME REQUESTED BY OPERATOR.

SPECIFY STOP TIME DESIRED. 024 000620

SECONDARY MACHINE RESPONSE ON OPTION 1. TIME REQUESTED BY OPERATOR.

TJD 000608 DJY 024 DET 215
73.9 41 59.0 33 73.5 32 00.1 04 27.3 38 00.2 38 53.1 38 00.1 05 313234

TJD 000609 DJY 024 DET 215
73.8 40 59.1 33 73.0 32 00.1 04 27.1 38 00.2 38 53.1 38 00.1 05 313234

TJD 000610 DJY 024 DET 215
74.1 41 59.0 33 73.2 32 00.1 04 27.1 38 00.2 38 52.9 38 00.2 04 313234

TJD 000611 DJY 024 DET 215
74.0 41 59.1 33 73.3 32 00.1 04 27.2 38 00.2 38 52.6 38 00.1 04 313234

TJD 000612 DJY 024 DET 215
74.7 40 59.1 33 73.2 32 00.1 04 27.1 38 00.2 38 52.4 38 00.1 05 313234

TJD 000613 DJY 024 DET 215
74.6 41 59.1 33 73.2 32 00.1 04 27.3 38 00.1 38 52.4 38 00.1 05 313234

TJD 000614 DJY 024 DET 215
74.3 41 58.9 33 72.9 32 00.1 04 27.4 38 00.2 38 52.4 38 00.1 05 313234

TJD 000615 DJY 024 DET 215
74.0 41 59.1 33 73.4 32 00.1 04 27.2 38 00.2 38 52.4 38 00.1 04 313234

TJD 000616 DJY 024 DET 215
73.7 40 59.1 33 73.0 32 00.1 04 27.3 38 00.2 39 52.4 38 00.1 04 313234

TJD 000617 DJY 024 DET 215
73.7 40 58.8 33 72.4 32 00.1 04 27.3 38 00.2 39 52.4 38 00.1 05 313234

TJD 000618 DJY 024 DET 215
74.2 41 58.8 33 73.2 32 00.1 04 27.2 38 00.2 39 52.3 38 00.2 04 313234

EXAMPLE: DRFUD DATA DUMP OPTION 1.

FIGURE II. 5

X DDD:

SPECIFY ACTION DESIRED, 1, 2, OR 3. 2

SPECIFY START TIME DESIRED. 024 000700

FUD TAPE POSITIONED PAST TIME DESIRED.

{ MESSAGE INFORMING OPERATOR
THAT HE MUST REWIND TAPE
AND TRY AGAIN.

OPERATOR REWINDS TAPE, AND CALLS FOR DDD: AGAIN.....

X DDD:

SPECIFY ACTION DESIRED, 1, 2, OR 3. 2

SPECIFY START TIME DESIRED. 024 000700

NO. OF BLOCKS TO BE DUMPED. 15

{ OPERATOR SELECTS OPTION 2.
START TIME FURNISHED BY OPER.
OPERATOR ASKS FOR 15 SCANS
TO BE DUMPED.

TDD 000700 DJY 024 DET 215
73.6 40 60.3 33 74.2 31 00.1 04 27.3 38 00.2 39 52.7 39 00.1 05 313234

TDD 000701 DJY 024 DET 215
73.6 40 60.4 33 74.4 31 00.1 04 27.3 38 00.2 39 52.5 39 00.1 05 313234

TDD 000702 DJY 024 DET 215
74.0 41 60.4 33 74.4 31 00.2 04 27.4 39 00.2 39 52.3 39 00.1 05 313234

TDD 000702 DJY 024 DET 215
74.0 41 60.3 33 74.4 31 00.1 04 27.4 39 00.2 39 52.4 39 00.1 05 313234

TDD 000703 DJY 024 DET 215
74.0 41 60.3 33 74.5 31 00.1 04 27.4 39 00.2 39 52.6 39 00.1 05 313234

TDD 000704 DJY 024 DET 215
74.0 41 60.1 33 74.6 31 00.1 04 27.4 39 00.2 39 52.6 39 00.1 05 313234

TDD 000705 DJY 024 DET 215
73.7 40 60.0 33 74.1 31 00.1 04 27.3 38 00.2 39 52.4 39 00.1 05 313234

FIGURE II. 6

EXAMPLE: DRFUD DATA DUMP OPTION 2.

X DDD:

SPECIFY ACTION DESIRED, 1, 2, OR 3. 3

NO. OF BLOCKS TO BE DUMPED. 10

OPERATOR SELECTS OPTION 3.

OPERATOR WANTS TO START
WHEREVER THE DATA POINTER
IS POSITIONED AND DUMP
TEN SCANS.

TJD 000925 DJY 024 DET 215
73.5 42 58.8 33 73.4 31 00.1 04 28.1 39 00.2 39 53.2 40 00.1 05 313234

TJD 000926 DJY 024 DET 215
74.0 42 58.8 33 73.4 31 00.1 04 28.0 39 00.2 39 53.2 40 00.1 05 313234

TJD 000927 DJY 024 DET 215
74.0 43 58.7 33 73.4 31 00.2 04 28.1 39 00.2 39 53.4 40 00.1 05 313234

TJD 000928 DJY 024 DET 215
74.0 43 58.8 33 74.2 31 00.1 04 28.1 39 00.2 39 53.2 40 00.1 05 313234

TJD 000929 DJY 024 DET 215
74.0 43 59.2 33 74.9 31 00.1 04 28.0 39 00.1 39 53.0 40 00.1 05 313234

TJD 000930 DJY 024 DET 215
74.0 43 60.0 33 76.2 31 00.1 04 28.0 39 00.1 39 53.4 40 00.1 05 313234

TJD 000930 DJY 024 DET 215
73.8 42 60.5 33 76.3 31 00.1 04 28.1 39 00.1 39 53.0 40 00.1 05 313234

TJD 000931 DJY 024 DET 215
73.8 42 60.4 33 75.4 31 00.1 04 28.1 39 00.2 39 53.7 40 00.1 05 313234

TJD 000932 DJY 024 DET 215
74.1 42 60.1 33 74.8 31 00.1 04 28.1 39 00.1 39 53.4 40 00.1 05 313234

TJD 000933 DJY 024 DET 215
73.9 42 59.8 33 74.3 31 00.1 04 28.1 39 00.2 39 53.4 40 00.1 05 313234

FIGURE II. 7

EXAMPLE: DRFUD DATA DUMP OPTION 3.

X DDD:

SPECIFY ACTION DESIRED, 1, 2, OR 3. 1

SPECIFY START TIME DESIRED. 300 000000

MAG TAPE PARITY ERROR, CALLING TASK ABORTED.

{ THIS CHECK CAN BE
BYPASSED BY SETTING
BIT 3 ON THE SWITCH
REGISTER.

X DDD:

SPECIFY ACTION DESIRED, 1, 2, OR 3. 1

SPECIFY START TIME DESIRED. 025 000000

MAG TAPE NOT READY, CALLING TASK ABORTED.

{ OPERATOR SHOULD PUT
MAG TAPE UNIT "ON LINE

X DDD:

SPECIFY ACTION DESIRED, 1, 2, OR 3. 1

SPECIFY START TIME DESIRED. 025 000000

TAPE UNIT INOPERATIVE, CALLING TASK ABORTED.

{ OPERATOR SHOULD TURN ON
ON POWER TO TAPE UNIT.

FIGURE II. 8

MAG TAPE CONDITION MESSAGES.....

X DDD:

SPECIFY ACTION DESIRED, 1, 2, OR 3. 2

SPECIFY START TIME DESIRED. 022 000700

FUD TAPE POSITIONED PAST DAY DESIRED.

X DDD:

SPECIFY ACTION DESIRED, 1, 2, OR 3. 2

SPECIFY START TIME DESIRED. 024 000700

FUD TAPE POSITIONED PAST TIME DESIRED.

IN EACH OF THE CASES ABOVE, THE OPERATOR SHOULD REWIND THE DRFUD TAPE AND PERFORM THE SEARCH ONCE MORE. IF IT FAILS AGAIN, THE TIME DESIRED IS NOT CONTAINED ON THE DRFUD TAPE.*

X DDD:

SPECIFY ACTION DESIRED, 1, 2, OR 3. 1

SPECIFY START TIME DESIRED. 024000700

INVALID FORMAT ON TIME REQUEST

{ OPERATOR FAILED TO LEAVE
A SPACE BETWEEN DAY AND
TIME.

X DDD:

SPECIFY ACTION DESIRED, 1, 2, OR 3. 1

SPECIFY START TIME DESIRED. 024 00070X

INVALID FORMAT ON TIME REQUEST

{ ILLEGAL CHARACTER USED
IN TIME REQUEST (X)

FIGURE II.9

* SET SWITCH REGISTER BIT POSITION 1 BEFORE SEARCHING THE SECOND TIME.

The station log will be maintained on the ASR 28 Teletype. Messages entered into the log may be divided into four major categories:

- (1) Routine; change of status, etc.
- (2) Tables; current or historical data on system operating status.
- (3) Reportable Signal Descriptors.
- (4) Operator generated messages.

III. 1 ROUTINE MESSAGES.

Routine messages are generated by NUSTAD to log all changes in equipment status.

- (1) Format: the general format is:

DDD TTTTTT C MM OOOOOOO

where: D represents a three digit DOY

T represents a six digit TOD for event

C indicates either a channel (A thru N)
or T for the total system

M represents a two digit coded message

O represents additional information when
necessary

(2) Coded messages: The following are the coded messages which may occur, and their meanings.

<u>Two Char Code</u>	<u>Meaning of Code</u>	<u>Additional Information</u>
AF	AIS Failure	NONE
AU	Auto/Manual Switch Set to Auto	NONE
BC	Blanking Level Changed	New Value
CC	Cardioid Configuration Changed	New Configuration Code
CF	Channel Put Coasting for Signal Failure	NONE
CN	Channel Put Coasting for Noise	NONE
CS	Channel Put Coasting for Saturation	NONE
D	Channel Put Down	NONE
FC	Frequency Changed	New Value
GC	RF Gain Changed	New Value
IR	INE Condition Reset	NONE
IS	INE Condition Set	NONE
MN	Auto/Manual Switch Set to Manual	NONE
MP	Memory Parity Error Discovered	Location of Error
MR	Maintenance Switch Reset	NONE
MS	Maintenance Swtich Set	NONE
PF	Processor Power Failure	NONE

<u>Two Char Code</u>	<u>Meaning of Code</u>	<u>Additional Information</u>
PR	Processor Power Restored	NONE
RS	Redundancy Check Successful	NONE
SD	System Dead in Scheduler	NONE
TC	Tracking Rate Changed	New Value
U	Channel Brought to Up State	Freq, Track Rate, Gain, Blank Level

(3) The various parameter values relate to the values on the EECO 881M as follows:

TRACKING RATE: Both the receiver and the processor work with ten discrete positions. The following table relates numeric values:

<u>EECO</u>	<u>PROCESSOR</u>
0.1	0
0.15	1
0.3	2
0.5	3
0.7	4
1.0	5
1.5	6
3.0	7
5.0	8
7.0	9

TABLE III. 1
TRACKING RATE EQUIVALENTS

RF GAIN: While the processor/ADACS uses 25 discrete values, the EECO control is by means of a continuously adjustable potentiometer. Conversion from one set of values to the other may be approximated with the following scale:

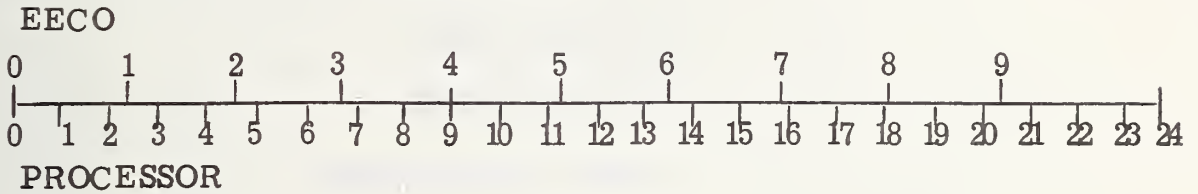


FIGURE III. 1
RF GAIN EQUIVALENTS

BLANKING LEVEL: The processor/ADACS uses 64 discrete values, the EECO control is by means of a continuously adjustable potentiometer. Conversion from one set of values to the other may be approximated with the following scale:

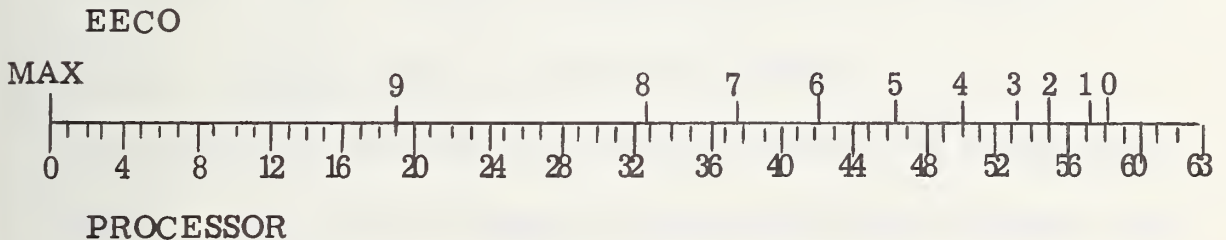


FIGURE III. 2
BLANKING LEVEL EQUIVALENTS

CARDIOID CONFIGURATION: The cardioid configuration is maintained as a one digit number which is interpreted as follows:

<u>VALUE</u>	<u>CONFIGURATION</u>
7	NO CARDIOID USED
6	WHIP REVERSE
5	LOOP REVERSE
4	LOOP/WHIP REVERSE
2	WHIP NORMAL
1	LOOP NORMAL
Ø	LOOP/WHIP NORMAL

TABLE III. 2
CARDIOID CONFIGURATION CODE INTERPRTATION

(4) SYSTEM RESTART: Whenever the system is reinitialized and restarted, the following message is logged:

.....SYSTEM RESTART AT DDD TTTTTT..

If the restart is processor initiated, the reason will be found as the last message prior to the restart message (PF, MP, etc.). If there is not a message indicating a processor discovered reason for restarting the system, the restart will be assumed to be operator initiated. The operator may induce a system restart by depressing the MASTER CLEAR switch on the processor control panel.

.....SYSTEM RESTART AT 023 215549.....

023 215549 CURRENT RECEIVER STATUS, DET 215

CHAN	FREQ	C	I	M	AM	GAIN	TR	BLL	DIURNAL	SIGMA	AMPLITUDE
A	17.8	1	1	0	A						
B	21.0	0	1	0	A						
C	21.0	0	1	0	A						
D	16.0	1	1	0	A						
E	22.3	0	1	0	A						
F	18.6	1	1	0	A						
G	24.0	4	1	0	A						
H	ZERO INPUT...										
I	17.8	7	1	0	A						
J	ZERO INPUT...										
K	ZERO INPUT...										
L	ZERO INPUT...										
M	24.0	7	1	0	A						
N	17.8	7	1	0	A						

023 215554 A IR
023 215554 A CC05
023 215554 B IR
023 215554 B CC05
023 215554 C IR
023 215554 C CC01
023 215554 D IR
023 215554 D CC05
023 215554 E IR
023 215554 E CC01
023 215554 F IR
023 215554 F CC05
023 215554 G IR
023 215554 G CC05
023 215554 I IR
023 215554 M IR
023 215554 N IR
023 215620 D GC20
023 215642 H GC20
023 215708 A BC05
023 215713 B BC05
023 215718 C BC05
023 215723 D BC05
023 215729 E BC05
023 215740 F BC05

FIGURE III. 3
Example of System Restart in Station Log

(5) REDUNDANCY FAILURE: If the periodic check between channels monitoring the same path should fail, the following message is generated:

DDD TTTTTT RED FAILURE, CHANS C AND C ON LEVEL N

where: C and C are the redundant channels and N is the reason for failure. N is interpreted as follows:

<u>(LEVEL)</u> <u>N</u>	<u>REASON</u>
1	The channel status, determined by the amplitude characteristics, is not the same for both channels.
2	The analysis status, indicating the progress into waveform processing, is not the same for both channels.
3	The short term noise characteristics are not the same on both channels.
4	The diurnal slope estimates are not approximately equal.

NOTE: If no two channels are monitoring the same path, no message is generated by the redundancy check program. All failures are logged. If no failures are found, a RS message is generated.

III. 2 TABLES.

The system generates several tables to provide data summarizing system performance and establishing current performance.

(1) CURRENT RECEIVER STATUS.

At present times, NUSTAD generates a table to record current parameter settings, processing values, and necessary status information. This status table may also be generated upon operator request by using the STD option.

Abbreviations used in the status table, in the order in which they appear:

CHAN	CHANNEL (A thru N)
FREQ	RECEIVER FREQUENCY (KHz)
C	CARDIOID CONFIGURATION (see above for interpretation)
I	INE STATUS $\emptyset \Rightarrow$ Reset, 1 \Rightarrow Set
M	MAINTENANCE STATUS $\emptyset \Rightarrow$ Reset, 1 \Rightarrow Set
AM	AUTO/MANUAL STATUS A \Rightarrow Auto, M \Rightarrow Manual
GAIN	RF GAIN (see above for interpretation)
TR	TRACKING RATE (see above for interpretation)
BLL	BLANKING LEVEL (see above for interpretation)
DIURNAL	DIURNAL SLOPE (see definition)
SIGMA	STANDARD DEVIATION (see definition)
AMPLITUDE	SIGNAL STRENGTH (-20 thru +20 dB)
NO INPUT	NO DIGITAL INPUT TO IOIS FOR THIS CHANNEL

LINE 1:

DDD TTTTTT CURRENT RECEIVER STATUS, DET NNN

where: D represents a three digit DOY

T represents a six digit TOD

N represents a three digit Det number

LINE 2: Column headings

LINE 3 thru 16: Channel status for each channel in tabular form. If INE or MAINTENANCE is set, no output is listed past the AM column; the channel is always down in these cases.

(2) PARAMETER HISTORY.

Thrice daily a table is generated to establish historical data useful in determining path reliability. The following information is logged for each channel:

1. The percentage of time that the system was operating during which the channel was in the UP state.
2. The percentage of up time that the channel was at each tracking rate.
3. The percentage of up time that the channel was in each gain setting group.

024 060040 CURRENT RECEIVER STATUS, DET 215

CHAN	FREQ	C	I	M	AM	GAIN	TR	BLL	DIURNAL	SIGMA	AMPLITUDE	
A	17.8	5	0	0	A	09	8	00	-000.8	00.62	-09.80	UP
B	21.0	5	0	0	A	12	9	02	-000.1	00.62	-06.76	UP
C	21.0	1	0	0	A	12	9	04	-000.1	00.62	-03.16	UP
D	16.0	5	0	0	A	20	2	00	-000.1	00.25	-05.04	UP
E	22.3	1	0	0	A	09	2	01	&001.7	06.68	&00.68	UP
F	18.6	5	0	0	A	11	9	00	-000.1	00.00	-00.08	UP
G	24.0	5	0	0	A	20	3	00	-000.7	01.37	-01.24	UP
H	ZERO INPUT...											
I	17.8	7	0	0	A	11	8	00	-008.9	00.00	-19.96	DOWN
J	ZERO INPUT...											
K	ZERO INPUT...											
L	ZERO INPUT...											
M	24.0	7	0	0	A	19	9	00	&023.3	10.81	-06.48	C,R
N	17.8	7	0	0	A	24	6	00	-000.4	11.00	-18.80	C,R

024 090153 CURRENT RECEIVER STATUS, DET 215

CHAN	FREQ	C	I	M	AM	GAIN	TR	BLL	DIURNAL	SIGMA	AMPLITUDE	
A	17.8	5	0	0	A	12	9	00	-000.1	00.18	-03.04	UP
B	21.0	5	0	0	A	16	1	16	&000.6	03.00	-05.36	UP
C	21.0	1	0	0	A	14	1	17	&000.7	03.00	-06.96	UP
D	16.0	5	0	0	A	22	2	00	&000.0	00.81	-01.76	UP
E	22.3	1	0	0	A	11	1	01	&001.5	00.25	-00.68	UP
F	18.6	5	0	0	A	11	9	00	-000.2	00.06	&00.76	UP
G	24.0	5	0	0	A	18	9	00	-000.1	00.25	&04.52	UP
H	ZERO INPUT...											
I	17.8	7	1	1	M							
J	ZERO INPUT...											
K	ZERO INPUT...											
L	ZERO INPUT...											
M	24.0	7	0	0	A	20	9	00	-000.1	00.06	&03.48	UP
N	17.8	7	0	0	A	24	9	00	-000.1	11.93	-19.44	DOWN

FIGURE III. 4

Examples of Receiver Status Table

Gain settings are divided into eight groups. The values in each group are:

<u>GROUP</u>	<u>SETTINGS INCLUDED</u>
0	0, 1, 2
1	3, 4, 5
2	6, 7, 8
3	9, 10, 11
4	12, 13, 14
5	15, 16, 17
6	18, 19, 20
7	21, 22, 23, 24

TABLE III. 3
GAIN SETTING GROUPS

024 080040 PARAMETER HISTORY, DET 215

CHAN	A	B	C	D	E	F	G	H	I	J	K	L	M	N
UP	99	100	100	99	100	100	100	0	0	0	0	0	99	73
TRACK RATE														
0	0	0	0	12	7	0	1	0	0	0	0	0	0	0
1	0	0	0	9	8	0	1	0	0	0	0	0	0	0
2	0	0	0	19	41	0	1	0	0	0	0	0	0	0
3	0	0	0	16	25	0	1	0	0	0	0	0	0	0
4	0	0	0	6	18	0	0	0	0	0	0	0	0	0
5	0	0	0	8	1	0	1	0	0	0	0	0	0	0
6	0	0	0	6	0	0	0	0	0	0	0	0	1	0
7	0	0	0	14	0	2	1	0	0	0	0	0	1	1
8	4	0	0	11	0	5	3	0	0	0	0	0	0	4
9	95	100	100	0	0	93	91	0	0	0	0	0	98	95
GAIN														
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
2	0	0	0	0	0	0	0	0	0	0	0	0	0	0
3	56	0	0	0	58	98	0	0	0	0	0	0	0	0
4	44	100	100	0	30	2	49	0	0	0	0	0	0	0
5	0	0	0	0	12	0	24	0	0	0	0	0	73	0
6	0	0	0	88	0	0	27	0	0	0	0	0	27	0
7	0	0	0	12	0	0	0	0	0	0	0	0	0	100

FIGURE III. 5

Example of Parameter History Table

When the waveform analysis programs have determined that a signal meets reporting criteria NUSTAD generates its report in three parts.

(1) PART I:

The first part of the report is generated as soon as NUSTAD considers a signal reportable. This message supplies the operator with a relatively detailed description of the waveform thru the current time and is primarily for the operator's information.

Abbreviations used in the Part I of the **REPORTABLE SIGNAL DESCRIPTION**, in the order in which they appear:

CHAN	CHANNEL (A thru N)
FREQ	RECEIVER FREQUENCY (KHz)
TR	TRACKING RATE
GN	RF GAIN
BL	BLANKING LEVEL
CARD	CARDIOID CONFIGURATION
SIGMA	STANDARD DEVIATION
AMP	AMPLITUDE
DIUR	DIURNAL SLOPE
ON	@ ONSET

PK	@ PEAK
1P	@ FIRST PEAK OF COCO SIGNAL
2P	@ VALLEY POINT OF COCO SIGNAL
HR	@ HALF RECOVERY (Minimum of 10 minutes past onset)
FR	@ SIGNAL TERMINATION

The following format is used:

LINE 1:

DDD TTTTTT REPORTABLE SIGNAL DESCRIPTION, DET NNN

where: D reflects a three digit DOY

T reflects a six digit TOD

N reflects a three digit Det number

LINE 2: A three digit serial signal identification number, used to associate the three parts of the message with the signal.

LINE 3:

CHAN C; FREQ FF.F LLL; TRN; GNNN; BLNN; CARD N

where: C represents the channel reporting the signal

F represents a three digit frequency of the channel

L represents a three character set of call letters associated with that frequency. UNK is used if they are unknown.

N represents the current values associated with the TRACKING RATE, RF GAIN, BLANKING LEVEL, and CARDIOID CONFIGURATION.

LINE 4:

SIGNAL TYPE: YYYYYY SIGMA: SS.SS

where: Y represents the signal type being reported (ALPHA, BRAVO, COCO, or DELTA)

S represents the standard deviation of the phase at onset.

LINE 5: Column headings

LINE 6: (data concerning onset)

ON DDD TTTTTT PPPPP.PP AAA.AA RRRR.R

where: D represents DOY of onset

T represents TOD of onset

P is the relative phase value at onset*

A is the signal amplitude at onset

R is the DIURNAL SLOPE at onset

(* NOTE: The Relative PHASE assumes a range from -4096.00 microseconds through +4095.93 microseconds).

LINE 6-a: (Data concerning first peak of COCO signal; not used with other types)

1P DDD TTTTTT PPPPP.PP AAA.AA

where: D represents DOY at first peak
T represents TOD at first peak
P is the relative phase value at first peak
A is the signal amplitude at first peak

LINE 6-b: (Data concerning "valley point" of COCO signal;
not used with other types)

2P DDD TTTTTT PPPPP.PP AAA.AA

where: D represents DOY at valley point
T represents TOD at valley point
P is the relative phase value at valley point
A is the signal amplitude at valley point

LINE 7: (data concerning signal peak)

PK DDD TTTTTT PPPPP.PP AAA.AA

where: D represents DOY at signal peak
T represents TOD at signal peak
P is the relative phase value at signal peak
A is the signal amplitude at signal peak

LINE 8: (data at the point where a signal is considered
valid, generally this is the point of Half
Recovery).

HR DDD TTTTTT PPPPP.PP AAA.AA RRRR.R

where: D represents DOY at valid recovery
T represents TOD at valid recovery
P is the relative phase value at valid recovery
A is the signal amplitude at valid recovery
R is the current DIURNAL SLOPE and approximates the rate of recovery

LINE 9:

OFFSET: NNNN.NN

where: N is the phase offset, including compensation for the diurnal

LINE 10:

ONSET RATE: NNN.N

where: N is the average rate of change of the phase between onset and peak

024 213300 REPORTABLE SIGNAL DESCRIPTION, DET 215
 002
 CHAN B;FREQ 21.0 UNK;TR4;GN16:BL02;CARDS
 SIGNAL TYPE: ALPHA SIGMA 02.25

	DOY	TIME	PHASE	AMP	DIUR
ON	024	212231	&0033.37	-05.88	&000.0
PK	024	212254	&0018.37	-05.16	
HR	024	213259	&0028.75	-05.60	&000.6

OFFSET: 0015.00
 ONSET RATE: 039.1

025 104102 REPORTABLE SIGNAL DESCRIPTION, DET 215
 013
 CHAN C;FREQ 21.0 UNK;TR4;GN15:BL04;CARD1
 SIGNAL TYPE: BRAVO SIGMA 00.56

	DOY	TIME	PHASE	AMP	DIUR
ON	025	103052	&0070.31	-04.88	-000.2
PK	025	103120	&0091.43	-04.60	
HR	025	104055	&0105.93	-04.88	&000.8

OFFSET: 0021.18
 ONSET RATE: 045.4

024 223921 REPORTABLE SIGNAL DESCRIPTION, DET 215
 005
 CHAN B;FREQ 21.0 UNK;TR5;GN16:BL02;CARDS
 SIGNAL TYPE: DELTA SIGMA 00.50

	DOY	TIME	PHASE	AMP	DIUR
ON	024	222916	&0058.43	-05.08	-000.2
PK	024	223015	&0049.81	-05.32	
HR	024	223921	&0058.56	-05.40	&000.3

OFFSET: 0008.50
 ONSET RATE: 003.6

FIGURE III. 6
 Examples of Part I of Reportable Signal Descriptions

(2) PART II:

The second part of the report is generated when NUSTAD has determined that the signal has terminated. Part II supplements the data generated in Part I to supply the operator with the remaining necessary information pertaining to the wave.

Abbreviations used are the same as described for Part I, above.

The following format is used:

LINES 1 thru 5 are the same as in Part I.

LINE 6: (data at signal termination)

FR DDD TTTTTT PPPPP.PP AAA.AA RRRR.R

where: D represents DOY of final recovery

T represents TOD of final recovery

P is the relative phase value at final recovery

A is the signal amplitude at final recovery

R is the DIURNAL SLOPE at final recovery

LINE 7:

SIGNAL TERMINATED: MMMMM

where: M takes on one of the following values:

<u>VALUE</u>	<u>WHEN</u>
NORMALLY	Recovery becomes full in phase and DIURNAL SLOPE
TIME LAPSE	Time since onset reaches 1 hour
SIGNAL LOST	Signal Amplitude drops out during recovery *
NEW ONSET	A new onset has been detected and the current signal analysis has been terminated to allow analysis of the new waveform

(*NOTE: A signal is also reported if the amplitude drops out after recovery has begun, but before half recovery.)

024 214421 REPORTABLE SIGNAL DESCRIPTION, DET 215
002
CHAN B;FREQ 21.0 UNK;TR4;GN16:BL02;CARD5
SIGNAL TYPE: ALPHA SIGMA 02.25
DOY TIME PHASE AMP DIUR
FR 024 214359 &0033.87 -05.24 &000.2
SIGNAL TERMINATED: NORMALLY

025 110343 REPORTABLE SIGNAL DESCRIPTION, DET 215
013
CHAN C;FREQ 21.0 UNK;TR4;GN15:BL04;CARD1
SIGNAL TYPE: BRAVO SIGMA 00.56
DOY TIME PHASE AMP DIUR
FR 025 110339 &0116.00 -04.36 &000.0
SIGNAL TERMINATED: NORMALLY

024 224531 REPORTABLE SIGNAL DESCRIPTION, DET 215
005
CHAN B;FREQ 21.0 UNK;TR5;GN16:BL02;CARD5
SIGNAL TYPE: DELTA SIGMA 00.50
DOY TIME PHASE AMP DIUR
FR 024 224518 &0058.12 -04.48 &000.0
SIGNAL TERMINATED: NORMALLY

FIGURE III. 7
Examples of Part II of Reportable Signal Descriptions

(3) PART III:

The third part of the RSD message is generated as soon after Part II has been output as NUSTAD can find sufficient storage to ensure room for the verbage of this part. Part III is intended as part of the text of any message to be transmitted to HQ. Its format is such as to minimize possible data loss when transmitted over standard communication lines (i. e., numeric values are spelled out alphabetically; 2 being transmitted as TWO, etc.).

The following format is used:

LINE 1:

RSD (serial number)

where: the serial number is a three digit value used to identify the signal on this channel. It is initialized each time NUSTAD is reloaded and incremented for each new RSD message generated.

LINE 2:

(Class), (Type), (Frequency & Call Letters), (Path), (DOY)

where: class is either A or B to indicate whether class A or class B criteria have been met.

The type is either Alpha, Bravo, Coco, or Delta. The frequency is given digitally with the call letters for that transmitter. The path will be either FRONT or BACK.

LINES 3 & 4:

(Onset time), (Offset), (Direction)
(Onset Rate), (Onset Amplitude)

where: the onset time is given as six digits of hours, minutes, and seconds. The maximum phase offset is given in three digits to microseconds. The direction will be either ADVANCE or RETARD. The onset rate is the average rate-of-change in microseconds/minute. The amplitude is in dB from -20 to +20.

LINE 5: (paragraph B)

(Time from onset to peak), (Peak Amplitude)

The time from onset to peak is given as three digits representing tenths of minutes.

LINE 6: (paragraph C)

HALF (Time from onset to half recovery), (Half recovery
amplitude)

Time is again in tenths of minutes, and the amplitude in dB.

LINE 7: (paragraph D)

FULL (Time from onset), (Amplitude at full recovery)

or

LOST (Phase offset remaining) MICROSECONDS, (Amplitude)

or

HOUR (Phase offset remaining) MICROSECONDS, (Amplitude)

or

(Phase offset remaining) MICROSECONDS, (Amplitude),
SECOND ONSET

When the signal continues to full recovery, the first case above is used. The time from onset to full recovery is given in tenths of minutes and the amplitude in dB.

If the signal drops out before full recovery is reached, the remaining offset to recovery is given with the amplitude (Case 2 above).

If signal processing is terminated after one hour without full recovery, the third case is used to give the remaining offset and the signal amplitude.

If a possible new onset occurs prior to full recovery, the current signal is terminated using the fourth case to indicate the remaining offset to full recovery and the signal amplitude.

LINE 8: (paragraph E)

(Tracking Rate), (Sigma at Onset) MICROSECONDS, (Type)

where: the tracking rate is interpreted by the table in Section III. 1 above. The four digit onset Standard Deviation is given in hundredths of microseconds and the type is repeated from LINE 2.

LEADERS: To enable the operator to identify the Part III text on the punched paper tape, sections of unpunched tape (LEADERS) are run through the punch on the ASR 28. The format is:

LEADER	PART III OF RSD	LEADER
--------	-----------------	--------

024 213300 REPORTABLE SIGNAL DESCRIPTION, DET 215
002
CHAN B;FREQ 21.0 UNK;TR4;GN16:BL02;CARDS
SIGNAL TYPE: ALPHA SIGMA 02.25
DOY TIME PHASE AMP DIUR
ON 024 212231 &0033.37 -05.88 &000.0
PK 024 212254 &0018.37 -05.16
HR 024 213259 &0028.75 -05.60 &000.6
OFFSET: 0015.00
ONSET RATE: 039.1

024 214421 REPORTABLE SIGNAL DESCRIPTION, DET 215
002
CHAN B;FREQ 21.0 UNK;TR4;GN16:BL02;CARDS
SIGNAL TYPE: ALPHA SIGMA 02.25
DOY TIME PHASE AMP DIUR
FR 024 214359 &0033.87 -05.24 &000.2
SIGNAL TERMINATED: NORMALLY

RSD ZERO ZERO TWO
A, ALPHA, 21.0 UNK, BACK, ZERO TWO FOUR
A. TWO ONE TWO TWO THREE ONE, ZERO ONE FIVE, ADVANCE
ZERO THREE NINE, MINUS ZERO SIX DB
B. PEAK ZERO ZERO THREE, MINUS ZERO SIX DB
C. HALF ONE ZERO FOUR, MINUS ZERO SIX DB
D. FULL TWO ONE FOUR, MINUS ZERO SIX DB
E. FOUR, ZERO TWO TWO FIVE MICROSECONDS, ALPHA

FIGURE III.9

Parts I, II, and III of A Reportable Signal Description

(4) Understanding Real-Time RSD Messages.

1. When NUSTAD generates an RSD message, the parameters and values reported in the three parts supply information useful in determining the cause of the signal.

2. **LONG TERM NOISE:** If the diurnal estimate at onset (RSD, Part I, Last element in line beginning ON) is greater than about 2 to 3 microseconds/minute, the signal is probably the result of long term noise.

3. **SHORT TERM NOISE:** If the value of SIGMA at onset (RSD, Parts I, II, and III) is large, especially at the lower tracking rates, the signal is probably nothing more than short term noise with characteristics meeting criteria.

4. **KEYING:** If any of the reported amplitude points are abnormally low, or if the channel goes coasting during the signal, or if the onset is near the transmitter's normal keying period, the signal is probably the result of phase variations due to the transmitter's keying cycle. The channel's integrater balance should be checked for this type of signal report. (NOTE: Anytime an "U" message is generated, the channel is coming out of a coasting or down state.

If the coasting is less than 30 seconds, a "C" message will not have been printed.)

5. OVER RECOVERY: If the phase recovery value is significantly greater than the onset value for ALPHA or DELTA signals, or significantly greater than one cycle above the onset value for BRAVO signals, the signal is probably the result of natural causes (long term noise, etc.).

IV. OPERATOR GENERATED MESSAGES.

NUSTAD permits the operator to enter messages in the station log via the LOG option described in Section II. 5. The message appears in the log with the following format:

DDD TTTTTT L: (Message text)

023 220149 G TC08
023 220149 I TC08
023 220149 M TC08
023 220149 N TC08
023 220302 L: NJSTAD GOING UNATTENDED.....JRE.....
023 220319 A TC09
023 220319 B TC08
023 220319 E TC09
023 220319 F TC09
023 220319 G TC09

FIGURE III. 10

Example of Operator Message Among Routine Messages

APPENDIX IV

SYSTEM TERMS AND ABBREVIATIONS

SYSTEM TERMS AND ABBREVIATIONS.

- ADACS Automatic Data Analysis and Control System. The interface "box" between the sensor and the processor.
- AIS Analog Input Subsystem. The hardware subsystem of the MODCOMP III processor which controls input of the analog data from the sensor.
- ASCII American Standard Code for Information Interchange. A 7 bit character code for data transmission on the ASR 35 Teletype.
- BAUDOT A 5 bit character code used for data transmission on the ASR 28 Teletype.
- BCD Binary Coded Decimal. A data coding scheme which retains the integrity of the decimal numbering system by coding each decimal digit individually in binary.
- CPU The processor's Central Processing Unit, the program control and execution section of the computer.
- CR The RETURN character on the ASR 35 Teletype.

- DFC A MODCOMP assembler defined pseudo-op used to Define Constants.
- DI Data Interrupt. One of two types of interrupts generated by peripheral devices in data transmissions. Refer to MODCOMP III Reference Manual No. 310-103000-000; Chapter V.
- DMP Direct Memory Processor. A hardware Input/Output subsystem of the MODCOMP III processor which permits data transmission in blocks rather than by CPU control for each character.
- DOY The Julian designation for the Day of Year.
- DRFUD The Digital Recorder For U Data. The subsystem which collects raw sensor data.
- INE Invalid data, Equipment sensed. A condition detected by hardware circuitry in the sensor and ADACS which alerts NUSTAD not to process data on that sensor channel.

- IOIS The Input/Output Interface Subsystem. The hardware subsystem of the MODCOMP III processor which controls digital Input/Output between the sensor/ADACS and the processor, and includes a set of eight user defined external interrupts.
- NULL A nonprinting character.
- NUSTAD New U System of Totally Automated Design. The complete software package of the automated U technique.
- ROS The Real-Time Operating System section of NUSTAD.
- RSD The Reportable Signal Description message generated by NUSTAD in three parts when a signal meeting reporting criteria is encountered.
- SAL The Stand Alone Linking Loader (MODCOMP No. 600000-012). The program used to load NUSTAD into memory.
- SI Service Interrupt. One of two types of interrupts generated by peripheral devices in data transmissions. Refer to MODCOMP III Reference Manual No. 310-103000-000; Chapter V.

TOD The Time of Day.

WTC The World Time Clock. The timing subsystem which supplies NUSTAD with the BCD time and timing interrupts.

APPENDIX V

MODCOMP III INSTRUCTION SET

LISTED BY OPERATION TYPE

MNEMONIC	OP. CODE	DESCRIPTION	
LOAD, STORE, AND TRANSFER INSTRUCTIONS			
LDM*,r,x	A	E5	Load Register from Memory
LDI,r	V	ED	Load Register from Memory Immediate
LDS,r,d		F5	Load Reg. from Memory Short Displaced
LDX,r,x		FD	Load Reg. from Memory Short Indexed
STM*,r,x	A	E6	Store Register in Memory
STI,r	V	EE	Store Register in Memory Immediate
STS,r,d		F6	Store Reg. in Memory Short Displaced
STX,r,x		FE	Store Reg. in Memory Short Indexed
LBX,r,x		AE	Load Byte from Memory
SBX,r,x		AF	Store Syte in Memory
LFM*,r,x	A	A4	Load File from Memory
LFS,r,d		B4	Load File from Memory Short Displaced
LFX,r,x		BC	Load File from Memory Short Indexed
SFM*,r,x	A	A5	Store File in Memory
SFS,r,d		B5	Store File in Memory Short Displaced
SFX,r,x		BD	Store File in Memory Short Indexed
TRR,r,s		6D	Transfer Register to Register
TRRB,r,s	B	7D	Transfer Reg. to Reg. and Branch if Nonzero
GMR,r,b		67	Generate Mask in Register
GMRB,r,b	B	77	Generate Mask in Reg. and Branch Unconditionally

MNEMONIC	OP.CODE	DESCRIPTION	
ARITHMETIC INSTRUCTIONS			
ADM*,r,x	A	E0	Add Memory to Register
ADI,r	V	E8	Add Memory to Register Immediate
ADS,r,d		F0	Add Memory to Register Short Displaced
ADX,r,x		F8	Add Memory to Register Short Indexed
ADMM*,r,x	A	C0	Add Register to Memory
ADMB*,r,x	A,B	C4	Add Reg. to Memory and Branch if Nonzero
ADSM,r,d		D0	Add Register to Memory Short Displaced
ADSB,r,d	B	D4	Add Reg. to Mem. Short Disp. and Branch if Nonzero
ADXM,r,x		D8	Add Register to Memory Short Indexed
ADXB,r,x	B	DC	Add Reg. to Mem. Short Indx. and Branch if Nonzero
ADR,r,s		68	Add Register to Register
ADRB,r,s	B	78	Add Reg. to Reg. and Branch if Nonzero
DAR,r,s		22	Double Precision Add Register to Register
SUM*,r,x	A	E1	Subtract Memory from Register
SUI,r	V	E9	Subtract Memory from Register Immediate
SUS,r,d		F1	Subtract Memory from Register Short Displaced
SUX,r,x		F9	Subtract Memory from Register Short Indexed
SUR,r,s		69	Subtract Register from Register
SURB,r,s	B	79	Subtract Reg. from Reg. and Branch if Nonzero
MPM*,r,x	A	A0	Multiply Memory by Register
MPS,r,d		B0	Multiply Memory by Register Short Displaced
MPX,r,x		B8	Multiply Memory by Register Short Indexed
MPR,r,s		20	Multiply Register by Register
DVM*,r,x	A	A1	Divide Register by Memory
DVS,r,d		B1	Divide Register by Memory Short Displaced
DVX,r,x		B9	Divide Register by Memory Short Indexed
DVR,r,s		21	Divide Register by Register
CRMB*,r,x	A,B,B	C7	Compare Memory and Register
CRSB,r,d	B,B	D7	Compare Memory and Register Short Displaced
CRXB,r,x	B,B	DF	Compare Memory and Register Short Indexed
TRO,r		OE	Transfer and Reset Overflow Status
TTR,r,s		6F	Transfer 2's Complement Register to Register
TTRB,r,s	B	7F	Trans. 2's Complement Reg. to Reg. and Branch if Nonzero

MNEMONIC	OP. CODE	DESCRIPTION	
LOGICAL INSTRUCTIONS			
ETM*,r,x	A	E2	Extract Memory from Register
ETI,r	V	EA	Extract Memory from Register Immediate
ETS,r,d		F2	Extract Memory from Register Short Displaced
ETX,r,x		FA	Extract Memory from Register Short Indexed
ETMM*,r,x	A	C1	Extract Register from Memory
ETMB*,r,x	A,B	C5	Extract Reg. from Mem. and Branch if Nonzero
ETSM,r,d		D1	Extract Register from Memory Short Displaced
ETSB,r,d	B	D5	Ext. Reg. from Mem. Sht. Dis. & Br. if Nonzero
ETXM,r,x		D9	Extract Reg. from Memory Short Indexed
ETXB,r,x	B	DD	Ext. Reg. from Mem. Sht. Indx. & Br. if Nonzero
ETR,r,s		6A	Extract Register from Register
ETRB,r,s	B	7A	Ext. Reg. from Reg. and Branch if Nonzero
ORM*,r,x	A	E3	OR Memory and Register
ORI,r	V	EB	OR Memory and Register Immediate
ORS,r,d		F3	OR Memory and Register Short Displaced
ORX,r,x		FB	OR Memory and Register Short Indexed
ORMM*,r,x	A	C2	OR Register and Memory
ORSM,r,d		D2	OR Register and Memory Short Displaced
ORXM,r,x		DA	OR Register and Memory Short Indexed
ORR,r,s		6B	OR Register and Register
ORRB,r,s	B	7B	OR Register and Register and Branch if Nonzero
XOM*,r,x	A	E4	Exclusive OR Memory and Register
XOI,r	V	EC	Exclusive OR Memory and Register Immediate
XOS,r,d		F4	Exclusive OR Mem. and Reg. Short Displaced
XOX,r,x		FC	Exclusive OR Mem. and Reg. Short Indexed
XOR,r,s		6C	Exclusive OR Register and Register
ZRR,r		6C	Zero Register
XORB,r,s	B	7C	Exclusive OR Reg. and Reg. and Branch if Nonzero
TOR,r,s		OD	Trans. 1's Complement Register to Register
TRMB*,r,x	A,B	C6	Test Reg. and Mem. and Branch if any 1's Compare
TRSB,r,d	B	D6	Test Reg. and Mem. Sht. Disp. & Br. if any 1's Compare
TRXB,r,x	B	DE	Test Reg. and Mem. Sht. Indx. & Br. if any 1's Compare
TERB,r,s	B	7E	Test Reg. and Reg. and Br. if any 1's Compare

MNEMONIC	OP.CODE	DESCRIPTION
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SHIFT INSTRUCTIONS

LAD, r, c	2E	Shift Left Arithmetic Double
RAD, r, c	2A	Shift Right Arithmetic Double
LAS, r, c	2F	Shift Left Arithmetic Single
RAS, r, c	2B	Shift Right Arithmetic Single
LLD, r, c	2C	Shift Left Logical Double
RLD, r, c	28	Shift Right Logical Double
LLS, r, c	2D	Shift Left Logical Single
RLS, r, c	29	Shift Right Logical Single
LRS, r, c	0F	Left Rotate Single

BIT MANIPULATION INSTRUCTIONS

LBR, r, b		65	Load Bit in Register
LBRB, r, b	B	75	Load Bit in Register and Branch Unconditionally
ABMM*, b, x	A	80	Add Bit in Memory
ABMB*, b, x	A, B	84	Add Bit in Memory and Branch in Nonzero
ABSM, b, d		90	Add Bit in Memory Short Displaced
ABSB, b, d	B	94	Add Bit in Mem. Short Disp. and Branch if Nonzero
ABXM, b, x		98	Add Bit in Memory Short Indexed
ABXB, b, x	B	9C	Add Bit in Mem. Short Indx. and Branch if Nonzero
ABR, r, b		60	Add Bit in Register
ABRB, r, b	B	70	Add Bit in Register and Branch if Nonzero
SBR, r, b		61	Subtract Bit in Register
SBRB, r, b	B	71	Subtract Bit in Register and Branch if Nonzero
ZBMM*, b, x	A	81	Zero Bit in Memory
ZBMB*, b, x	A, B	85	Zero Bit in Memory and Branch if Nonzero
ZBSM, b, d		91	Zero Bit in Memory Short Displaced
ZBSB, b, d	B	95	Zero Bit in Mem. Shrt Disp. and Branch if Nonzero
ZBXM, b, x		99	Zero Bit in Memory Short Indexed
ZBXB, b, x	B	9D	Zero Bit in Mem. Shrt Indx. and Branch if Nonzero
ZBR, r, b		62	Zero Bit in Register
ZBRB, r, b	B	72	Zero Bit in Register and Branch if Nonzero
OBMM*, b, x	A	82	OR Bit in Memory
OBSM, b, d		92	OR Bit in Memory Short Displaced
OBXM, b, x		9A	OR Bit in Memory Short Indexed
OBR, r, b		63	OR Bit in Register
OBRB, r, b	B	73	OR Bit in Register and Branch Unconditionally
XBR, r, b		64	Exclusive OR Bit in Register
XBRB, r, b	B	74	Excl. OR Bit in Register and Branch if Nonzero
TBMB*, b, x	A, B	86	Test Bit in Memory and Branch if One
TBSB, b, d	B	96	Test Bit in Mem. Shrt Disp. and Branch if One
TBXB, b, x	B	9E	Test Bit in Mem. Shrt Indx. and Branch if One
TBRB, r, b	B	76	Test Bit in Register and Branch if One
CBMB*, b, x	A, B, B	87	Compare Bit and Memory
CBSB, b, d	B	97	Compare Bit and Memory Short Displaced
CBXB, b, x	B	9F	Compare Bit and Memory Short Indexed

MNEMONIC	OP. CODE	DESCRIPTION
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BYTE MANIPULATION INSTRUCTIONS

MUR,r,s	0B	Move Upper Byte Register to Register
MLR,r,s	0C	Move Lower Byte Register to Register
MBR,r,s	08	Move Byte Right Register to Register
MBL,r,s	09	Move Byte Left Register to Register
IBR,r,s	0A	Interchange Bytes Register to Register

UNCONDITIONAL BRANCH INSTRUCTIONS

BLM*,r,x	A	E7	Branch and Link
BLI,r		EF	Branch and Link Immediate
BRU*,x	A	E7	Branch Unconditionally
HOP,d		F7	Branch Short Displaced
BRX,x		FF	Branch Short Indexed

CONTROL INSTRUCTIONS

HLT	00	Halt
NOP	66	No Operation

INTERRUPT AND CALL INSTRUCTIONS

SIE,1	26-1	Set Interrupt Enable
RIE,1	27-1	Reset Interrupt Enable
SIR,1	26-2	Set Interrupt Request
RIR,1	27-2	Reset Interrupt Request
SIA,1	26-0	Set Interrupt Active
RIA,1	27-0	Reset Interrupt Active
REX,n	23	Request Executive Service
RMI	01	Request Multiprocessor Interrupt
CAR	24	Clear Active Interrupt and Return
CIR	25	Clear Interrupt and Return

INPUT/OUTPUT INSTRUCTIONS

ISA,r,u	48	Input Status from I/O Group A
ISB,r,u	49	Input Status from I/O Group B
ISC,r,u	4A	Input Status from I/O Group C
ISD,r,u	4B	Input Status from I/O Group D
IDA,r,u	4C	Input Data From I/O Group A
IDB,r,u	4D	Input Data From I/O Group B
IDC,r,u	4E	Input Data From I/O Group C
IDD,r,u	4F	Input Data From I/O Group D
OCA,r,u	40	Output Command to I/O Group A
OCB,r,u	41	Output Command to I/O Group B

MNEMONIC	OP. CODE	DESCRIPTION
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INPUT/OUTPUT INSTRUCTIONS - CONTINUED

OCC,r,u	42	Output Command to I/O Group C
OCD,r,u	43	Output Command to I/O Group D
ODA,r,u	44	Output Data to I/O Group A
ODB,r,u	45	Output Data to I/O Group B
ODC,r,u	46	Output Data to I/O Group C
ODD,r,u	47	Output Data to I/O Group D

LISTED ALPHABETICALLY BY MNEMONIC

MNEMONIC	OP. CODE	DESCRIPTION
A		
ABMB*,b,x	A,B	84 Add Bit in Memory and Branch if Nonzero
ABMM*,b,x	A	80 Add Bit in Memory
ABR,r,b		60 Add Bit in Register
ABRB,r,b	B	70 Add Bit in Register and Branch if Nonzero
ABSB,b,d	B	94 Add Bit in Mem. Short Disp. and Branch if Nonzero
ABSM,b,d		90 Add Bit in Memory Short Displaced
ABXB,b,x	B	9C Add Bit in Mem. Short Indx. and Branch if Nonzero
ABXM,b,x		98 Add Bit in Memory Short Indexed
ADI,r	V	E8 Add Memory to Register Immediate
ADM*,r,x	A	E0 Add Memory to Register
ADMB*,r,x	A,B	C4 Add Reg. to Memory and Branch if Nonzero
ADMM*,r,x	A	C0 Add Register to Memory
ADR,r,s		68 Add Register to Register
ADRB,r,s	B	78 Add Reg. to Reg. and Branch if Nonzero
ADS,r,d		F0 Add Memory to Register Short Displaced
ADSB,r,d	B	D4 Add Reg. to Mem. Short Disp. and Branch if Nonzero
ADSM,r,d		D0 Add Register to Memory Short Displaced
ADX,r,x		F8 Add Memory to Register Short Indexed
ADXB,r,x	B	DC Add Reg. to Mem. Short Indx. and Branch if Nonzero
ADXM,r,x		D8 Add Register to Memory Short Indexed
B		
BLI,r		EF Branch and Link Immediate
BLM*,r,x	A	E7 Branch and Link
BRU*,x	A	E7 Branch Unconditionally
BRX,x		FF Branch Short Indexed
C		
CAR		24 Clear Active Interrupt and Return
CBMB*,b,x	A,B,B	87 Compare Bit and Memory
CBSB,b,d	B	97 Compare Bit and Memory Short Displaced
CBXB,b,x	B	9F Compare Bit and Memory Short Indexed
CIR		25 Clear Interrupt and Return
CRMB*,r,x	A,B,B	C7 Compare Memory and Register
CRSB,r,d	B,B	D7 Compare Memory and Register Short Displaced
CRXB,r,x	B,B	DF Compare Memory and Register Short Indexed

MNEMONIC		OP. CODE	DESCRIPTION
D			
DAR, r, s		22	Double Precision Add Register to Register
DVM*, r, x	A	A1	Divide Register by Memory
DVR, r, s		21	Divide Register by Register
DVS, r, d		B1	Divide Register by Memory Short Displaced
DVX, r, x		B9	Divide Register by Memory Short Indexed
E			
ETI, r	V	EA	Extract Memory from Register Immediate
ETM*, r, x	A	E2	Extract Memory from Register
ETMB*, r, x	A, B	C5	Extract Reg. from Mem. and Branch if Nonzero
ETMM*, r, x	A	C1	Extract Register from Memory
ETR, r, s		6A	Extract Register from Register
ETRB, r, s	B	7A	Ext. Reg. from Reg. and Branch if Nonzero
ETS, r, d		F2	Extract Memory from Register Short Displaced
ETSB, r, d	B	D5	Ext. Reg. from Mem. Sht. Dis. & Br. if Nonzero
ETSM, r, d		D1	Extract Register from Memory Short Displaced
ETX, r, x		FA	Extract Memory from Register Short Indexed
ETXB, r, x	B	DD	Ext. Reg. from Mem. Sht, Indx. & Br. if Nonzero
ETXM, r, x		D9	Extract Reg. from Memory Short Indexed
G			
GMR, r, b		67	Generate Mask in Register
GMRB, r, b	B	77	Generate Mask in Register and Branch Unconditionally
H			
HLT		00	Halt
HOP, d		F7	Branch Short Displaced
I			
IBR, r, s		0A	Interchange Bytes Register to Register
IDA, r, u		4C	Input Data From I/O Group A
IDB, r, u		4D	Input Data From I/O Group B
IDC, r, u		4E	Input Data From I/O Group C
IDD, r, u		4F	Input Data From I/O Group D
ISA, r, u		48	Input Status from I/O Group A
ISB, r, u		49	Input Status from I/O Group B
ISC, r, u		4A	Input Status from I/O Group C
ISD, r, u		4B	Input Status from I/O Group D

MNEMONIC		OP.CODE	DESCRIPTION
L			
LAD,r,c		2E	Shift Left Arithmetic Double
LAS,r,c		2F	Shift Left Arithmetic Single
LBR,r,b		65	Load Bit in Register
LBRB,r,b	B	75	Load Bit in Register and Branch Unconditionally
LBX,r,x		AE	Load Byte from Memory
LDI,r	V	ED	Load Register from Memory Immediate
LDM*,r,x	A	E5	Load Register from Memory
LDS,r,d		F5	Load Register from Memory Short Displaced
LDX,r,x		FD	Load Register from Memory Short Indexed
LFM*,r,x	A	A4	Load File from Memory
LFS,r,d		B4	Load File from Memory Short Displaced
LFX,r,x		BC	Load File from Memory Short Indexed
LLD,r,c		2C	Shift Left Logical Double
LLS,r,c		2D	Shift Left Logical Single
LRS,r,c		0F	Left Rotate Single
M			
MBL,r,s		09	Move Byte Left Register to Register
MBR,r,s		08	Move Byte Right Register to Register
MLR,r,s		0C	Move Lower Byte Register to Register
MPM*,r,x	A	A0	Multiply Memory by Register
MPR,r,s		20	Multiply Register by Register
MPS,r,d		B0	Multiply Memory by Register Short Displaced
MPX,r,x		B8	Multiply Memory by Register Short Indexed
MUR,r,s		0B	Move Upper Byte Register to Register
N			
NOP		66	No Operation

MNEMONIC	OP.CODE	DESCRIPTION	
O			
OBSM*,b,x	A	82	OR Bit in Memory
OBR,r,b		63	OR Bit in Register
OBRB,r,b	B	73	OR Bit in Register and Branch Unconditionally
OBSM,b,d		92	OR Bit in Memory Short Displaced
OBXM,b,x		9A	OR Bit in Memory Short Indexed
OCA,r,u		40	Output Command to I/O Group A
OCB,r,u		41	Output Command to I/O Group B
OCC,r,u		42	Output Command to I/O Group C
ODC,r,u		43	Output Command to I/O Group D
ODA,r,u		44	Output Data to I/O Group A
ODB,r,u		45	Output Data to I/O Group B
ODC,r,u		46	Output Data to I/O Group C
ODD,r,u		47	Output Data to I/O Group D
ORI,r	V	EB	OR Memory and Register Immediate
ORM*,r,x	A	E3	OR Memory and Register
ORMM*,r,x	A	C2	OR Register and Memory
ORR,r,s		6B	OR Register and Register
ORRB,r,s	B	7B	OR Register and Register and Branch if Nonzero
ORS,r,d		F3	OR Memory and Register Short Displaced
ORSM,r,d		D2	OR Register and Memory Short Displaced
ORX,r,x		FB	OR Memory and Register Short Indexed
ORXM,r,x		DA	OR Register and Memory Short Indexed

R

RAD,r,c		2A	Shift Right Arithmetic Double
RAS,r,c		2B	Shift Right Arithmetic Single
REX,n		23	Request Executive Service
RIA,1		27-0	Reset Interrupt Active
RIE,1		27-1	Reset Interrupt Enable
RIR,1		27-2	Reset Interrupt Request
RLD,r,c		28	Shift Right Logical Double
RLS,r,c		29	Shift Right Logical Single
RMI		01	Request Multiprocessor Interrupt

MNEMONIC	OP.CODE	DESCRIPTION
S		
SBR,r,b		61 Subtract Bit in Register
SBRB,r,b	B	71 Subtract Bit in Register and Branch if Nonzero
SBX,r,x		AF Store Byte in Memory
SFM*,r,x	A	A5 Store File in Memory
SFS,r,d		B5 Store File in Memory Short Displaced
SFX,r,x		BD Store File in Memory Short Indexed
SIA,l		26-0 Set Interrupt Active
SIE,l		26-1 Set Interrupt Enable
SIR,l		26-2 Set Interrupt Request
STI,r	V	EE Store Register in Memory Immediate
STM*,r,x	A	E6 Store Register in Memory
STS,r,d		F6 Store Reg. in Memory Short Displaced
STX,r,x		FE Store Reg. in Memory Short Indexed
SUI,r	V	E9 Subtract Memory from Register Immediate
SUM*,r,x	A	E1 Subtract Memory from Register
SUR,r,s		69 Subtract Register from Register
SURB,r,s	B	79 Subtract Reg. from Reg. and Branch if Nonzero
SUS,r,d		F1 Subtract Memory from Register Short Displaced
SUX,r,x		F9 Subtract Memory from Register Short Indexed

T

TBMB*,b,x	A,B	86 Test Bit in Memory and Branch if One
TBRB,r,b	B	76 Test Bit in Register and Branch if One
TBSB,b,d	B	96 Test Bit in Mem. Shrt Disp. and Branch if One
TBXB,b,x	B	9E Test Bit in Mem. Shrt Indx. and Branch if One
TERB,r,s	B	7E Test Reg. and Reg. and Br. if any 1's Compare
TOR,r,s		OD Trans. 1's Complement Register to Register
TRMB*,r,x	A,B	C6 Test Reg. and Mem. and Branch if any 1's Compare
TRO,r		OE Transfer and Reset Overflow Status
TRR,r,s		6D Transfer Register to Register
TRRB,r,s	B	7D Transfer Reg. to Reg. and Branch if Nonzero
TRSB,r,d	B	D6 Test Reg. and Mem. Sht. Disp. & Br. if any 1's Compare
TRXB,r,x	B	DE Test Reg. and Mem. Sht. Indx. & Br. if any 1's Compare
TTR,r,s		6F Transfer 2's Complement Register to Register
TTRB,r,s	B	7F Trans. 2's Comp. Reg. to Reg. and Br. if Nonzero

MNEMONIC	OP.CODE	DESCRIPTION
X		
XBR,r,b		64 Exclusive OR Bit in Register
XBRB,r,b	B	74 Excl. OR Bit in Register and Branch if Nonzero
XOI,r	V	EC Exclusive OR Memory and Register Immediate
XOM*,r,x	A	E4 Exclusive OR Memory and Register
XOR,r,s		6C Exclusive OR Register and Register
XORB,r,s	B	7C Exclusive OR Reg. and Reg. and Branch if Nonzero
XOS,r,d		F4 Exclusive OR Mem. and Reg. Short Displaced
XOX,r,x		FC Exclusive OR Mem. and Reg. Short Indexed
Z		
ZMB*,b,x	A,B	85 Zero Bit in Memory and Branch if Nonzero
ZMM*,b,x	A	81 Zero Bit in Memory
ZBR,r,b		62 Zero Bit in Register
ZBRB,r,b	B	72 Zero Bit in Register and Branch if Nonzero
ZBSB,b,d	B	95 Zero Bit in Mem. Shrt. Disp. and Branch if Nonzero
ZBSM,b,d		91 Zero Bit in Memory Short Displaced
ZBXB,b,x	B	9D Zero Bit in Mem. Shrt Indx. and Branch if Nonzero
ZBXM,b,x		99 Zero Bit in Memory Short Indexed
ZRR,r		6C Zero Register

LISTED BY NUMERIC OP. CODE

OP. CODE	MNEMONIC	DESCRIPTION
00	HLT	Halt
01	RMI	Request Multiprocessor Interrupt
02-07	Unused	
08	MBR,r,s	Move Byte Right Register to Register
09	MBL,r,s	Move Byte Left Register to Register
0A	IBR,r,s	Interchange Bytes Register to Register
0B	MUR,r,s	Move Upper Byte Register to Register
0C	MLR,r,s	Move Lower Byte Register to Register
0D	TOR,r,s	Trans. 1's Complement Register to Register
0E	TRO,r	Transfer and Reset Overflow Status
0F	LRS,r,c	Left Rotate Single
10-1F	Unused	
20	MPR,r,s	Multiply Register by Register
21	DVR,r,s	Divide Register by Register
22	DAR,r,s	Double Precision Add Register to Register
23	REX,n	Request Executive Service
24	CAR	Clear Active Interrupt and Return
25	CIR	Clear Interrupt and Return
26-0	SIA,l	Set Interrupt Active
26-1	SIE,l	Set Interrupt Enable
26-2	SIR,l	Set Interrupt Request
27-0	RIA,l	Reset Interrupt Active
27-1	RIE,l	Reset Interrupt Enable
27-2	RIR,l	Reset Interrupt Request
28	RLD,r,c	Shift Right Logical Double
29	RLS,r,c	Shift Right Logical Single
2A	RAD,r,c	Shift Right Arithmetic Double
2B	RAS,r,c	Shift Right Arithmetic Single
2C	LLD,r,c	Shift Left Logical Double
2D	LLS,r,c	Shift Left Logical Single
2E	LAD,r,c	Shift Left Arithmetic Double
2F	LAS,r,c	Shift Left Arithmetic Single
30-3F	Unused	
40	OCA,r,u	Output Command to I/O Group A
41	OCB,r,u	Output Command to I/O Group B
42	OCC,r,u	Output Command to I/O Group C
43	OCD,r,u	Output Command to I/O Group D
44	ODA,r,u	Output Data to I/O Group A
45	ODB,r,u	Output Data to I/O Group B
46	ODC,r,u	Output Data to I/O Group C
47	ODD,r,u	Output Data to I/O Group D
48	ISA,r,u	Input Status from I/O Group A
49	ISB,r,u	Input Status from I/O Group B
4A	ISC,r,u	Input Status from I/O Group C
4B	ISD,r,u	Input Status from I/O Group D
4C	IDA,r,u	Input Data from I/O Group A

OP. CODE	MNEMONIC		DESCRIPTION
4D	IDB,r,u		Input Data from I/O Group B
4E	IDC,r,u		Input Data from I/O Group C
4F	IDD,r,u		Input Data from I/O Group D
50-5F	Unused		
60	ABR,r,b		Add Bit in Register
61	SBR,r,b		Subtract Bit in Register
62	ZBR,r,b		Zero Bit in Register
63	OBR,r,b		OR Bit in Register
64	XBR,r,b		Exclusive OR Bit in Register
65	LBR,r,b		Load Bit in Register
66	NOP		No Operation
67	GMR,r,b		Generate Mask in Register
68	ADR,r,s		Add Register to Register
69	SUR,r,s		Subtract Register from Register
6A	ETR,r,s		Extract Register from Register
6B	ORR,r,s		OR Register and Register
6C	XOR,r,s		Exclusive OR Register and Register
	ZRR,r		Zero Register
6D	TRR,r,s		Transfer Register to Register
6E	Unused		
6F	TTR,r,s		Transfer 2's Complement Register to Register
70	ABRB,r,b	B	Add Bit in Register and Branch if Nonzero
71	SBRB,r,b	B	Subtract Bit in Register and Branch if Nonzero
72	ZBRB,r,b	B	Zero Bit in Register and Branch if Nonzero
73	OBRB,r,b	B	OR Bit in Register and Branch Unconditionally
74	XBRB,r,b	B	Excl. OR Bit in Register and Branch if Nonzero
75	LBRB,r,b	B	Load Bit in Register and Branch Unconditionally
76	TBRB,r,b	B	Test Bit in Register and Branch if One
77	GMRB,r,b	B	Generate Mask in Register and Branch Unconditionally
78	ADRB,r,s	B	Add Reg. to Reg. and Branch if Nonzero
79	SURB,r,s	B	Subtract Reg. from Reg. and Branch if Nonzero
7A	ETRB,r,s	B	Ext. Reg. from Reg. and Branch if Nonzero
7B	ORRB,r,s	B	OR Register and Register and Branch if Nonzero
7C	XORB,r,s	B	Exclusive OR Reg. and Reg. and Branch if Nonzero
7D	TRRB,r,s	B	Transfer Reg. to Reg. and Branch if Nonzero
7E	TERB,r,s	B	Test Reg. and Reg. and Br. if any 1's Compare
7F	TTRB,r,s	B	Trans. 2's Comp. Reg. to Reg. and Br. if Nonzero
80	ABMM*,b,x	A	Add Bit in Memory
81	ZBMM*,b,x	A	Zero Bit in Memory
82	OBMM*,b,x	A	OR Bit in Memory
83	Unused		
84	ABMB*,b,x	A,B	Add Bit in Memory and Branch if Nonzero
85	ZBMB*,b,x	A,B	Zero Bit in Memory and Branch if Nonzero
86	TBMB*,b,x	A,B	Test Bit in Memory and Branch if One
87	CBMB*,b,x	A,B,B	Compare Bit and Memory
88-8F	Unused		
90	ABSM,b,d		Add Bit in Memory Short Displaced

OP. CODE	MNEMONIC		DESCRIPTION
91	ZBSM,b,d		Zero Bit in Memory Short Displaced
92	OBSM,b,d		OR Bit in Memory Short Displaced
93	Unused		
94	ABSB,b,d	B	Add Bit in Mem. Short Disp. and Branch if Nonzero
95	ZBSB,b,d	B	Zero Bit in Mem. Shrt Disp. and Branch if Nonzero
96	TBSB,b,d	B	Test Bit in Mem. Shrt Disp. and Branch if One
97	CBSB,b,d	B	Compare Bit and Memory Short Displaced
98	ABXM,b,x		Add Bit in Memory Short Indexed
99	ZBXM,b,x		Zero Bit in Memory Short Indexed
9A	OBXM,b,x		OR Bit in Memory Short Indexed
9B	Unused		
9C	ABXB,b,x	B	Add Bit in Mem. Short Indx. and Branch if Nonzero
9D	ZBXB,b,x	B	Zero Bit in Mem. Shrt Indx. and Branch if Nonzero
9E	TBXB,b,x	B	Test Bit in Mem. Shrt Indx. and Branch if One
9F	CBXB,b,x	B	Compare Bit and Memory Short Indexed
A0	MFM*,r,x	A	Multiply Memory by Register
A1	DVM*,r,x	A	Divide Register by Memory
A2-A3	Unused		
A4	LFM*,r,x	A	Load File from Memory
A5	SFM*,r,x	A	Store File in Memory
A6-AD	Unused		
AE	LBX,r,x		Load Byte from Memory
AF	SBX,r,x		Store Byte in Memory
B0	MPS,r,d		Multiply Memory by Register Short Displaced
B1	DVS,r,d		Divide Register by Memory Short Displaced
B2-B3	Unused		
B4	LFS,r,d		Load File from Memory Short Displaced
B5	SFS,r,d		Store File in Memory Short Displaced
B6-B7	Unused		
B8	MPX,r,x		Multiply Memory by Register Short Indexed
B9	DVX,r,x		Divide Register by Memory Short Indexed
BA-BB	Unused		
BC	LFX,r,x		Load File from Memory Short Indexed
BD	SFX,r,x		Store File in Memory Short Indexed
BE-BF	Unused		
C0	ADMM*,r,x	A	Add Register to Memory
C1	ETMM*,r,x	A	Extract Register from Memory
C2	ORMM*,r,x	A	OR Register and Memory
C3	Unused		
C4	ADMB*,r,x	A,B	Add Reg. to Memory and Branch if Nonzero
C5	ETMB*,r,x	A,B	Extract Reg. from Mem. and Branch if Nonzero
C6	TRMB*,r,x	A,B	Test Reg. and Mem. and Branch if any 1's Compare
C7	CRMB*,r,x	A,B,B	Compare Memory and Register
C8-CF	Unused		
D0	ADSM,r,d		Add Register to Memory Short Displaced
D1	ETSM,r,d		Extract Register from Memory Short Displaced
D2	ORSM,r,d		OR Register and Memory Short Displaced

OP. CODE	MNEMONIC		DESCRIPTION
D3	Unused		
D4	ADSB,r,d	B	Add Reg. to Mem. Short Disp. and Branch if Nonzero
D5	ETSB,r,d	B	Ext. Reg. from Mem. Shrt Disp. & Br. if Nonzero
D6	TRSB,r,d	B	Test Reg. and Mem. Shrt Disp. & Br. if any 1's Compare
D7	CRSB,r,d	B,B	Compare Memory and Register Short Displaced
D8	ADXM,r,x		Add Register to Memory Short Indexed
D9	ETXM,r,x		Extract Reg. from Memory Short Indexed
DA	ORXM,r,x		OR Register and Memory Short Indexed
DB	Unused		
DC	ADXB,r,x	B	Add Reg. to Mem. Short Indx. and Branch if Nonzero
DD	ETXB,r,x	B	Ext. Reg. from Mem. Shrt. Indx. & Br. if Nonzero
DE	TRXB,r,x	B	Test Reg. and Mem. Shrt Indx & Br. if any 1's Compare
DF	CRXB,r,x	B,B	Compare Memory and Register Short Indexed
EO	ADM*,r,x	A	Add Memory to Register
E1	SUM*,r,x	A	Subtract Memory from Register
E2	ETM*,r,x	A	Extract Memory from Register
E3	ORM*,r,x	A	OR Memory and Register
E4	XOM*,r,x	A	Exclusive OR Memory and Register
E5	LDM*,r,x	A	Load Register from Memory
E6	STM*,r,x	A	Store Register in Memory
E7	BLM*,r,x	A	Branch and Link
	BRU*,x	A	Branch Unconditionally
E8	ADI,r	V	Add Memory to Register Immediate
E9	SUI,r	V	Subtract Memory from Register Immediate
EA	ETI,r	V	Extract Memory from Register Immediate
EB	ORI,r	V	OR Memory and Register Immediate
EC	XOI,r	V	Exclusive OR Memory and Register Immediate
ED	LDI,r	V	Load Register from Memory Immediate
EE	STI,r	V	Store Register in Memory Immediate
EF	BLI,r		Branch and Link Immediate
FO	ADS,r,d		Add Memory to Register Short Displaced
F1	SUS,r,d		Subtract Memory from Register Short Displaced
F2	ETS,r,d		Extract Memory from Register Short Displaced
F3	ORS,r,d		OR Memory and Register Short Displaced
F4	XOS,r,d		Exclusive OR Mem. and Reg. Short Displaced
F5	LDS,r,d		Load Register from Memory Short Displaced
F6	STS,r,d		Store Reg. in Memory Short Displaced
F7	HOP,d		Branch Short Displaced
F8	ADX,r,x		Add Memory to Register Short Indexed
F9	SUX,r,x		Subtract Memory from Register Short Indexed
FA	ETX,r,x		Extract Memory from Register Short Indexed
FB	ORX,r,x		OR Memory and Register Short Indexed
FC	XOX,r,x		Exclusive OR Mem. and Reg. Short Indexed
FD	LDX,r,x		Load Register from Memory Short Indexed
FE	STX,r,x		Store Reg. in Memory Short Indexed
FF	BRX,x		Branch Short Indexed

r - General Purpose Destination Register (0-15)
s - General Purpose Source Register (0-15)
l - Interrupt Level (0-31)
n - Executive Service Number (0-255)
c - Shift Count (0-15)
u - I/O Unit Number (0-15)
b - Bit Position (0-15)
* - Indirect Addressing
A - Address Operand
x - Index Register
d - Short Displaced Offset (0-15)
V - Value on Immediate Instruction
B - Branch Address
G - I/O Group

```

1  PGM
2  INT
3  EXT
4  EXT
5  EXT
6  EXT
7  EXT
8  EXT
9  EXT
10 EXT
11 EXT
12 EXT
13 *****
14 * CAT NO. 400000-000
15 *****
16 * LOW MEMORY CONSTANTS
17 *****
18 R 0000
19 R 0001
20 X 0021
21 A 0023
22 X 0028
23 X 0030
24 X 0029
25 A 003F
26 X 0025
27 A 0063
28 A 0073
29 A 008A
30 X 000C
31 X 008A
32 X 0001
33 A 0098
34 X 0006
35 X 0006
36 A 00A0
37 X 000A
38 X 000R
39 X 0013
40 A 00A7
41 X 0013
42 A 00C4
43 X 0026
44 A 00CA
45 X 0002
46 A 00D1
47 X 000D
48
49

E$INT
F:ANL,E:ANF
E:I35,E:35S,E:SKD,E:035
E:IOF,E:I28,U:TRK,U:GAN,U:BLI
E:SEC,E:MIN,E:URU,E:ANL,E:SFI
E:MIT,F:350,E:280,E:T08,M:INT
U:CPC,U:CCN,U:CST,U:RTY,U:TMI
U:TM3,U:TM4,E:FRT,E:28F
E:028,U:TRF,U:AST,U:DIU,U:IHB
U:SLP,U:FAC,U:OTM,E:TSI,E:MTI
E:PWD,E:PAR,E:ABR,F:MTP,B:STT
E:TSF
*****
* CAT NO. 400000-000
*****
* LOW MEMORY CONSTANTS
*****
R 0000 ANFNG RESTART ON CLEAR
R 0001
X 0021 PWR DOWN/UP ENTRY
A 0023 MEM PARITY ENTRY
X 0028 CONSOLE INTERRUPT ENTRY
X 0030 TIME-SLICE INT. ENTRY
X 0029 AIS DMP TC
A 003F AIS DMP TA
X 0025 ASR 35 DI ENTRY
A 0063 ASR 28 DI ENTRY
A 0073 -ASR 28 DI ENTRY
A 008A I SEC INT ENTRY
X 000C I MIN INT ENTRY
X 008A I NE INT ENTRY
X 0001 I NE INT ENTRY
A 0098 MAG TAPE SI ENTRY
X 0006 ASR 35 SI ENTRY
X 0006 AIS SI ENTRY
A 00A0
X 000A
X 000R
X 0013
A 00A7
X 0013
A 00C4
X 0026
A 00CA
X 0002
A 00D1
X 000D
*****
* SET PROGRAM ORIGIN
*****

```



```

94      0126      F643      PINIT STM,4,3      U.CCN      U.CCN=0
95      0127      X 0015
95      0128      F643      STM,4,3      U.CPC      U.CPC=0
96      0129      X 0014
96      012A      F653      STM,5,3      U.CST=4
97      012R      X 0016
97      012C      E663      STM,6,3      U.RTY      U.RTY=-18
98      012D      X 0017
98      012E      E643      STM,4,3      U.IHB      U.IHB=0
99      012F      X 0021
99      0130      ABR,3,15
100     0131      603F      LDI,15      14
101     0132      A 000E      SUPR,15,3  PINIT
102     0133      79F3      LDI,1      -30
103     0134      R 0126
103     0135      A FFE2      STM,1      U.TM1      U.TM1=-30
104     0136      A FFE2      LDI,1      -12
105     0137      X 0018      STM,1      U.TM3
106     0138      A FFF4      LDI,1      -9
107     0139      X 0019      STM,1      U.TM4      TCLK=-9
108     013A      E610      GCLK=-12
109     013B      X 0010
110     013C      X 0011
111     013D      A FFF7
112     013E      E610      STM,1
113     0140      X 001A
114     0141      6C11      ZRR,1
115     0142      E610      STM,1      E.350      CLEAR FLAG
116     0143      X 0010
117     0144      E610      STM,1      E.280
118     0145      X 0011      LDI,1      #4400
119     0146      E610      OCA,1,#A
120     0147      A 4400      CCP,1,8
121     0148      401A      OCB,1,9
122     0149      4118      LDI,1      #AC00
123     014A      4119      OCA,1,#A
124     014B      E610      AC00      OCBM,0      E.T08
125     014C      A AC00
126     014D      401A
127     014E      8200
128     014F      X 0012
129     0150      6C11
130     0151      E610
131     0152      X 0005

```

```

*****
* START-UP ASR 28 FOR OUTPUT
* CLEAR ASR 28 FOR OPERATOR INPUT
*****
*****
* LOG RESTART ON ASR 28...
*****

```

READ
SCHEDULE STATUS DISPLAY

TERMINATE
TERMINATE
ASR 28

125	0153	F610	STM,1	E.28F
126	0154 X	001C	BLM,14	E:FRT
127	0155	F7E0	DFC	STIME
128	0156 X	001B	BLM,14	E:035
129	0157 R	0189	DFC	MSG1
130	0158	E7E0	BLM,14	E:028
131	0159 X	0004	DFC	MSG0
132	015A R	0198	BLM,14	
133	015B	E7E0	DFC	
134	015C X	001D	LOI,1	14080
135	015D R	0173	STM,1	F.MTP
136	015E	FD10	ZRR,1	B.STT
137	015F A	3700	STM,1	E.TSF
138	0160	E610	BRU	E:SKD
139	0161 X	002A	ZBMM,0	
140	0162	6C11		
141	0163	E610		
142	0164	002B		
143	0165	8100		
144	0166 X	002C		
145	0167	E700		
146	0168 X	0003		
147	0169	6C22		
148	016A	E620		
149	016B X	002B		
150	016C	F620		
151	016D X	0005		
152	016E	E7E0		
153	016F X	0004		
154	0170 R	019A		
155	0171	F700		
156	0172 X	0003		

* F: FMT SET-UP

* DUMMY TASKS
F: ANL ZRR,2
STM,2
R. STT
E. IOF
E. 035
BLM,14
DFC
BRU
MSG8
E: SKD
*****=

MESSAGE S.....

MODCOMP ASSEMBLY

```

149 0173 A 0D0A #0D0A,#0A0A, #0A0A
    0174 A 0A0A
    0175 A 0A0A
150 0176 A 2E2E
    0177 A 2E2E
    0178 A 2E2E
    0179 A 2E2E
    017A A 2E2F
    017B A 2E2F
    017C A 2E2E
    017D A 2E2F
    017E A 2E2E
    017F A 2E2E
    0180 A 5359
    0181 A 5354
    0182 A 454D
    0183 A 2052
    0184 A 4553
    0185 A 5441
    0186 A 5254
    0187 A 2041
    0188 A 5420
151 0189 A 3132
    018A A 3320
    018B A 3132
    018C A 3334
    018D A 3536
    018E A 2E2E
    018F A 2E2E
    0190 A 2E2E
    0191 A 2E2E
    0192 A 2E2F
    0193 A 2E2E
    0194 A 2E2E
    0195 A 2E2E
    0196 A 2E2E
    0197 A 2E2E
153 0198 A 0D0A
    0199 A 0000
    019A A 0D0A
154 019B A 463A
    019C A 414E
    019D A 4C20
    019E A 4341
    019F A 4C4C
    01A0 A 4544
    01A1 A 0D0A
    01A2 A 0000
155 01A3 R 0100

```

.....SYSTEM RESTART

| AT |

STIME DFC |123 123456.....|

#0D0A,0

#0D0A,|F:ANL CALLED|,#0D0A,#0000

ANFNG

TOTAL ASSEMBLY ERRORS = 0 SIZE = 419

```

1  E$SKD
2  E:SKD,E:TSK,E:RSK,B:STT,E:TSF
3  B:RS1,B:RS2,B:RET
4  F:T01,E:T02,E:T03,E:T04,E:T05
5  E:T09,E:T10,E:T11,E:T12,E:T13
6  E:T06,E:T07,E:T08,E:T14,E:T15
7  F:LOG,U:RTA,E:OMI,M:LEV,M:STD
8  F:FMT,F:DMP,F:ANL,U:PST,E:T28
9  U:RED,E:UIN,E:T35,M:ESC,U:RP3
10  E:ANF,E:MAG,B:ORT
11  CAT NO. 40000-001
12  * THIS IS THE NEW AUTOMATED U SYSTEM SCHEDULER. TASKS ARE SCHEDULED
13  * BY SETTING THE 0 BIT FOR THAT TASK IN THE TASK TABLE. WHEN THE
14  * SCHEDULER FINDS A TASK REQUIRING SERVICE, IT RESETS THE SCHEDULE
15  * BIT AND TRANSFEPS CONTROL TO THAT ROUTINE.
16  E:SKD LDM,3 MXLIM R3 FOR COUNTER
17  LDI,1 E:TSK USE R1 AS TABLE BASE
18  SKD1 LDI,2 15
19  SKD2 TBMB,0,2 E:TSK,FOUND BRANCH IF SCHEDULE BIT IS SET
20  SBPB,2,15 SKD2 DECREMENT INDEX, NEXT TASK
21  SRRB,3,15 SKD1 CONTROL COUNT UNSUCCESSFUL PASSES
22  TRXB,0,1 E:SKD IS SYSTEM ALREADY DEAD
23  DBXM,0,1 #1307 SET SYSTEM DEAD FLAG
24  LDI,5
25  BLM,4 E:LOG
26  BRU E:ANF
27  FOUND ZBXM,0,1 CLEAR DEAD FLAG
28  ZBMM,0,2 E:TSK
29  BPU*,2 E:TSK
30  * SYSTEM TASK TABLE, TASKS LISTED IN INVERTED ORDER OF PRIORITY.
31  * FIRST ENTRY IS THE SYSTEM DEAD FLAG.
32  E:TSK DFC 0 SYSTEM DEAD FLAG
33  E:T15 DFC E:BSK 15. BACKGROUND SCHEDULER
34  E:T14 DFC M:LEV 14. GAIN/T.R. HISTORY LOG
35  E:T13 DFC U:PST 13. OPER PAPAM. CONTROL
36  E:T12 DFC E:T35 12. ASR 35 TELETYPE ROUTINE
37  E:T11 DFC E:MAG 11. MAG TAPE HANDLER

```

```

38 0021 X 0012 E.T10 DFC B:ORT
39 0022 A 0000 E.T09 DFC 0
40 0023 X 0005 M:STD M:STD
41 0024 X 000F U:RP3 U:RP3
42 0025 X 0003 E:OMI E:OMI
43 0026 X 0004 E:T28 E:T28
44 0027 X 0008 U:RED U:RED
45 0028 X 000C F:T04 DFC F:T04
46 0029 X 000E E:T02 DFC E:T02
47 002A X 0002 E:T01 DFC E:T01
48 002B A 0000 MXLIM DFC 0
49
50 002C E520 * THIS ROUTINE CONTROLS THE BACKGROUND (NON-REAL TIME) TASKS.
    002D R 0047 E:RSK LDM,2 B:STT
    002E ED30 LDI,3 4
    002F A 0004 STM,3 B:STT
51 0030 E630 SET BACKGROUND STATE = 5
    0031 R 0047 LDI,4 E:TSF
52 0032 ED40 OBM,0,4
    0033 R 0048 BRU*,2 BTABL
53 0034 9A04
54 0035 E70A
55 0036 R 0037
56
57 0037 R 0042 * BACKGROUND TASK TABLE.
    0038 X 0006 RTABL DFC BRSET
    0039 X 0007 F:FMT F:FMT
    003A X 0008 F:DMP F:DMP
    003B R 003C F:ANL F:ANL
    003C $+1 $+1
58 003C A410 * IF B:STT = 4, A BACKGROUND TASK IS ALREADY IN EXECUTION. RESET
59 003D R 0049 LFM,1 B:RSI
60 003E A4A0 LFM,10 B:RS2
61 003F R 0050 BRU* B:RET
62 0040 E708
63 0041 R 0056
64 0042 9904 BRSET ZRM,0,4
65 0043 F620 STM,2 B:STT
66 0044 R 0047 BRU E:SKD
67 0045 E700
68 0046 R 0000
69 0047 A 0000 B:STT DFC 0
70 0048 A 0000 E:TSF DFC 0
71 0049 A 0000 R:RS1 RES 7,0
72 0050 A 0000 B:RS2 RES 6,0
73 0051 A 0000 B:RET DFC 0
74 0052 A 0000
75 0053 ENF

```



```

1      PGM          E$TSI
2      INT          E:TSI,B:IOR
3      EXT          E:TSF,B:RS1,B:RS2,B:RET,E:T15,
4      EXT          F:SKD,E:IOF,E:O35,E:MGS
5      * THE TIME SLICING INTERRUPT COMES 10 TIMES PER SECOND. IT IS USED
6      * TO PREVENT BACKGROUND ROUTINES FROM HOLDING ON TO THE PROCESSOR
7      * TOO LONG AND THEREBY HAMPERING THE REAL TIME TASKS.
8      * IF THE SYSTEM IS IN A BACKGROUND TASK WHEN THE INTERRUPT OCCURS,
9      * THE SYSTEM SAVES ALL STATUS INFORMATION NECESSARY TO RESUME
10     * PROCESSING AND RESCHEDULES THE BACKGROUND TASK IN THE SCHEDULER.
11     * IF NC BACKGROUND TASK IS IN EXECUTION, THE INTERRUPT IS IGNORED.
12     E:TSI TBMB,0      E:TSF,TSFI      IS TIME-SLICING ENABLED?

13     CIR          CLEAR INTERRUPTS AND RETURN
14     TSFI SFM,1     B:RS1      SAVE REGISTERS
15     SFM,10       B:RS2
16     LDM,2        #3E          (FILL IN LOCATION WITH RET. ADDR))
17     STM,2        R:RET      SAVE REENTRY LOCATION
18     ORMM,0       E:T15      RESCHEDULE BACKGROUND SYSTEM
19     ZBMM,0       E:TSF      RESET TIME SLICING FLAG.
20     RIA,#F
21     RIR,#F
22     BRU          E:SKD      RETURN TO SCHEDULER.

23     * B$IOR IS THE SYSTEM I/O HANDLER FOR BACKGROUND LEVEL TASKS. THIS
24     * ROUTINE WILL TAKE CARE OF NECESSARY HOUSEKEEPING TO RETURN CONTROL
25     * TO THE SYSTEM ONCE THE I/O OPERATION HAS BEGUN AND ENSURE
26     * CONTINUATION OF THE BACKGROUND TASK ONCE THE I/O HAS BEEN COMPLETED.
27     * THE CALLING SEQUENCE DEPENDS ON THE DESIRED DEVICE.....
28     * FOR I/O TO THE ASR 35..
29     RLM,15       R:IOR
30     DFC          (PARAMETER)
31     DFC          (MSG START ADDR.)
32     * WHERE OUTPUT CONTINUES TO A NULL CHAR. (#0000)
33     * AND FOR I/O TO THE MAG TAPE UNIT..
34     RLM,15       B:IOR
35     DFC          (PARAMETER)
36     DFC          (MSC START ADDR.)
37     DFC          (COUNT OF WORDS)
38     * PARAMETER WORD INTERPRETATION..
39     * BIT 0..0 DO NOT RESCHEDULE BACKGROUND SYSTEM AFTER I/O
40     * BIT 1..0 RESCHEDULE BACKGROUND SYSTEM AFTER I/O IS DONE
41     * BIT 1..0 NO RESPONSE IS EXPECTED

```



```

42 *      1 RESPONSE IS EXPECTED FROM OPERATOR ON ASR 35
43 *      BIT 2..0 I/O ON ASR 35
44 *      1 I/O ON MAG TAPE
45 *      BIT 3..0 INPUT (REQUEST FOR MAG TAPE ONLY)
46 *      1 OUTPUT
47 *      R:IOR SFM,1      B.RS1      SAVE REGISTERS
48 *      SFM,10          B.RS2
49 *      LDX,2,15
50 *      ABR,15,15
51 *      LDX,3,15
52 *      ABR,15,15
53 *      LDX,4,15
54 *      ZBMM,0
55 *      STM,2
56 *      TBRB,2,2
57 *      STM,3
58 *      RLM,14
59 *      RES
60 *      BIOR2 STM,15
61 *      BRU
62 *      BIOR1 STM,3
63 *      STM,4
64 *      BLM,13
65 *      RES
66 *      RES
67 *      ABR,15,15
68 *      BRU
69 *      END

A510
0014 X A510
0015 X 0002
0016 A5A0
0017 X 0003
0018 FD2F
0019 60FF
001A FD3F
001B 60FF
001C FD4F
001D 8100
001E X 0001
001F E620
0020 X 0008
0021 7622
0022 R 002C
0023 E630
0024 R 0027
0025 E7E0
0026 X 0009
0027 A 0000
0028 E6F0
0029 X 0004
002A E700
002B X 0007
002C E630
002D R 0032
002E E640
002F R 0033
0030 E7D0
0031 X 000A
0032 A 0000
0033 A 0000
0034 60FF
0035 E700
0036 R 0028
0037

E.TSF
E.IOF
BIOR1
$+4
E:035
I,0
B.RET
E:SKD
$+6
$+5
E:MGS
I,0
RES
ABR,15,15
BIOR2
END

GET PARAMETER
GET MSG START ADDR.
GET WORD COUNT FOR MAG TAPE.
RESET E.JSF TO INHIBIT INTERRUPT
SAVE PARAMETER FOR E:035 USE.
BRU IF MAG TAPE CALL
MSG. START ADDR. PARAMETER
SAVE REENTRY ADDRESS
MSG. START ADDR PARAMETER
WORD COUNT PARAMETER
MOVE RETURN ADDR. OVER LAST PARM.

```

MDDCOMP ASSEMBLY

1	PGM	E\$CLK
2	INT	E:SEC,U.TM1,M.TRT,M.GNT
3	INT	M.UPT,E.TM3,E:MIN
4	EXT	M.TM1,E.T02,E:LOG,E.T04,E.T08,U.IHB,U.CST
5	EXT	U.TRK,U.GAN,E.TI4
6	CAT NO.	40000-003
7		* THIS IS THE SYSTEM SOFTWARE CLOCK. IT'S TWO ROUTINES, E:SEC AND
8		* E:MIN, RESPOND TO THE 1/SECOND AND 1/MINUTE TIME INTERRUPTS.

```

10 * E:SEC IS ENTERED VIA THE 1 SECOND TIME PULSE GENERATED BY THE WORLD
11 * TIME CLOCK.
12 * 1. THE 5 SECOND DELAY FOR THE ENTRY TO M:ESC IS COUNTED DOWN.
13 * 2. THE INPUT OF U DATA IS INITIATED.
14 * E:SEC SFM,1 SAVRS SAVE REGISTERS 1 - 7 FOR USE.
15 * PULSE TO KEEP ADACS IN AUTO
16 LRR,4,0
17 ODC,4,#E
18 LDM,1 M,1M1 GET DELAY TIMER
19 TRRP,1,1 $+3 IS TIMER ALREADY ZERO?
20 HOP,SECO1
21 ARMB,15 M,1M1,SECO1 INCREMENT AND CHECK FOR ZERO
22 OBM,0 E,TO2 SCHEDULE M:SEC IF ZERO
23 * AIS DATA READ SEQUENCE
24 SECO1 LDI,1 #4400 SET UP TO READ U DATA
25 OCB,1,0
26 LDI,1 #4000
27 OCB,1,0
28 LDI,1 #C829
29 OCB,1,0
30 LDI,1 #D000
31 OCB,1,1
32 ISB,2,#1
33 TBRB,2,7 $+3
34 HOP,ANERR
35 ZBMM,0 SFLAG CLEAR ANALOG SYSTEM FLAG
36 RETN1 LFM,1 SAVRS
37 * CIR CLEAR INTERRUPT AND RETURN
38 ANERR TBM,0 SFLAG,RETN1
39 OBM,0 SFLAG SET ANALOG SYSTEM FLAG
40 LDI,5 #1312 GENERATE IAFI MESSAGE
41 BLM,4 E:LOG
0000 A510
0001 R 0195
0002 6540
0003 464E
0004 E510
0005 X 0001
0006 7D11
0007 R 0009
0008 F706
0009 84F0
000A X 0001
000B R 000E
000C 8200
000D X 0002
000E ED10
000F A 4400
0010 4110
0011 ED10
0012 A 4000
0013 4110
0014 ED10
0015 A C829
0016 4110
0017 ED10
0018 A D000
0019 4111
001A 4921
001B 7627
001C R 001E
001D F706
001E 8100
001F R 01A3
0020 A410
0021 R 0195
0022 2500
0023 8600
0024 R 01A3
0025 R 0020
0026 8200
0027 R 01A3
0028 ED50
0029 A 1312
002A E740
002B X 0003

```

MODCOMP ASSEMBLY ONE SECOND INTERRUPT.....

42	002C	E700	BRU	RETNI
	002D	R 0020		

```

44 * E:MIN IS ENTERED VIA THE 1 MINUTE TIME PULSE GENERATED BY THE WORLD
45 * TIME CLOCK.
46 * 1. SCHEDULES REDUNDANCY CHECK EVERY 30 MINUTES.
47 * 2. SCHEDULES STATUS DUMP EVERY FOUR HOURS.
48 * 3. SAVES PARAMETER STATUS FOR LATER DUMP.
49 * SAVRR
      A510
      R 002E
      R 002F
      R 0030
      R 0031
      R 0032
      R 0033
      A 0034
      R 0035
      R 0036
      R 0037
      X 0038
      R 0039
      R 003A
      R 003B
      R 003C
      R 003D
      A 003E
      R 003F
      R 0040
      R 0041
      R 0042
      X 0043
      R 0044
      R 0045
      X 0046
      R 0047
      R 0048
      R 0049
      R 004A
      R 004B
      X 004C
      R 004D
      R 004E
      R 004F
      R 0050
      X 0051
      R 0052
      A 0053
      R 0054
      R 0055
      R 0056
      R 0057
      R 0058
      R 0059
      R 005A

      ABMR,15      U.,TM1,MIN02
      LDI,2        -30      TIME TO SCHEDULE REDUN. CHECK
      STM,2        U.,TM1   RESET TIMER TO 30 MINUTES
      ORMM,0      E.,T04    SCHEDULE REDUNDANCY TEST
      MIN02 LDI,1      M.,TRT  BASE OF TRACK RATE TABLE IN R1
      LDI,2        M.,GNT   BASE OF GAIN TABLE IN R2
      LDI,3        -1      R3 FOR CHANNEL POINTER
      ARMM,15     E.,TM3   INCREMENT SYSTEM UP TIMER
      MIN04 ABR,3,15    NEXT CHANNEL
      TRMB,0,3   U.,IHB,MIN03  BYPASS OPERATION ON PROC. INHIBITED
      LDM,4,3     U.,CST   GET CHANNEL STATUS
      SBRB,4,15  MIN03    BRANCH IF CHANNEL NOT UP
      ABMM,15,3  M.,UPT   LOG CHANNEL AS UP
      LDM,4,3     U.,TRK   PUT TRACK RATE IN R4
      ADR,4,1     ADR,4,1  CELL POINTER IN R4
      ABXM,15,4  ZRR,4    INC. COUNTER FOR THIS T.R. ON CHAN.
      LDM,5,3     U.,GAN   PUT CHAN IN R5
      LDI,6       #3
      DVR,4,6    TBRB,5,12 $+3  DIVIDE GAIN BY 4
      TBRB,5,12 $+3  PUT 1241 IN LAST GROUP
      HOP,$+2    (GAIN NOT 24)
      SBR,5,15   MOVE BACK TO LAST GOODP.
      ADR,5,2    CELL POINTER TO PRO:ER GROUP
      ABXM,15,5  INC. COUNT IN CELL

```

75	005B	ED40	MIN03	LDI,4	10	
005C	A	000A				
76	005D	6814	ADR,1,4			MOVE TO BASE FOR NEXT CHAN. (+10)
77	005E	614E	SBR,4,14			
78	005F	6824	ADR,2,4			MOVE TO BASE FOR NEXT CHAN. (+8)
79	0060	ED40	LDI,4	-13		
	0061	FFF3				
80	0062	7843	ADR,4,3	MIN04		LOOP IF LAST CHAN. NOT DONE
	0063	0041				
81	0064	ED10	LDI,1	#4000		SET UP TO READ DOY/TIME GROUP
	0065	A				
82	0066	431F	ODD,1,#F			
83	0067	4F2F	IDD,2,#F			
84	0068	4F3F	IDD,3,#F			TIME GROUP IN R2,R3
85	0069	0D22	TOR,2,2			
86	006A	0D33	TOR,3,3			
87	006B	2C29	LLD,2,9			
88	006C	EA20	ETI,2	#8180		PUT HR/MIN IN R2
89	006D	A				
89						* HOURS AND MINS IN R2. CHECK TIME TABLES TO SEE
90						* IF ITS TIME TO SCHEDULE M\$LEV OR M\$STD.
91	006E	ED10	LDI,1	-6		
006F	A	FFFA				
92	0070	6D32	MIN01	TRR,3,2		COPY TIME
93	0071	E131	SUM,3,1	LEVTB+6		CHECK AGAINST TABLE
0072	R	01AC				
94	0073	7D33	TRR,3,3	\$+3		
0074	R	0076				
95	0075	F704	HOP,MIN06			FOUND IN TABLE
96	0076	701F	ABR,1,15	MIN01		
0077	R	0070				
97	0078	F703	HOP,MIN05			
98	0079	8200	MIN06	OBMM,0	E.T14	SCHEDULE M\$LEV
007A	X	000A				
007B	A	ED10	MIN05	LDI,1	-24	
007C	A	FFE8				
100	007D	6D32	MIN07	TRR,3,2		COPY TIME
101	007E	F131	SUM,3,1	STDTR+24		CHECK VS. TABLE
007F	R	01C4				
102	0080	7D33	TRP,3,3	\$+3		
0081	R	0083				
103	0082	F704	HOP,MIN08			FOUND IN TABLE
104	0083	701F	ABR,1,15	MIN07		
0084	R	007D				
105	0085	F703	HOP,\$+3			SCHEDULE M\$STD
106	0086	8200	MIN08	ORMM,0	E.T08	
0087	X	0005				
107	0088	A410	LFM,1	SAVRR		CLEAR INTERRUPT AND RETURN
0089	R	019C				
108	008A	2500	CIR			

MODCOMP ASSEMBLY

ONE MINUTE INTERRUPT.....

110 * M. TRT IS A TABLE OF TRACK RATE SETTINGS.

111 * TR.....

112 * CHAN 0 1 2 3 4 5 6 7 8 9

113 * 0

114 * 1

115 * .

116 * .

117 * 13

0089 A 0000 M. TRT RES 140,0

118 * M. GNT IS A TABLE OF GAIN SETTINGS

119 * GAIN.....

120 * CHAN 0 1 2 3 4 5 6 7

121 * 0

122 * 1

123 * .

124 * .

125 * .

126 * 13

0117 A 0000 M. GNT RES 112,0

127 * M. UPT IS A TABLE OF UP TIME

128 * M. UPT RES 14,0

129 * SAVRS RES 7

130 * SAVRR RES 7

131 * SFLAG DFC 0

132 * U. TM1 DFC -30

133 * E. TM3 DFC 0

134 * TABLE OF TIMES TO CALL M\$LEV

135 * LEVTR DFC #0000

136 * DFC #1000

137 * DFC #2C00

138 * DFC 3,0

139 * TABLE OF TIMES TO CALL M\$STD

140 * STDTR DFC #0400

141 * DFC #0C00

142 * DFC #2000

143 * DFC #2800

144 * DFC #3000

145 * DFC #4400

146 * RES 18,0

147 * END

01C4

ANALOG SUBSYSTEM ERROR FLAG
REDUNDANCY CHECK TIMER
SYSTEM UP TIMER
0000 HRS
0800 HRS
1600 HRS
(EXPAND 3 TIMES)
0200 HRS
0600 HRS
1000 HRS
1400 HRS
1800 HRS
2200 HRS
(EXPAND 18 TIMES)

Address	OpCode	OpType	OpName	Comment
35	0029			
36	002A			
	002B	R	DBR,2,0	SET BIT IN DATA WORD
	002C		CBMM,15	CYCLE SHIFT BIT
37	002D	R	BLM,5	SEND SHIFT
	002E		ZBMM,15	CLEAR SHIFT BIT
38	002F	R	STFAZ	STORE FAZ AWAY
	0030		STX,2,1	NEXT STORE LOC.
39	0031		ABR,1,15	GET BLANK LEVEL VALUE
40	0032		LDM,2,7	
41	0033	R	TRPB,2,0	IS BLL NEG.?
42	0034		ETI,2	CLEAR GARBAGE BITS
43	0035	R	#F800	
	0036		HOP,\$+2	CLEAR BLL TO 0
44	0037	A	ZRR,2	
	0038		STX,2,1	NEXT CHAN.
45	0039		ABR,1,15	LOOP THRU 42 SAMPLES
46	003A		ABR,3,15	
47	003B		ABR,7,15	
48	003C		LDI,4	READ TIME GROUP TO END OF ROW
49	003D	R	#4000	
	003E		OCD,4,#F	STORE UPPER HALF
50	003F	A	IDD,5,#F	
	0040		TOR,5,5	STORE LOWER HALF
51	0041		STX,5,1	TOP OF NEXT ROW
52	0042		ABR,1,15	
53	0043		IDD,5,#F	
54	0044		TOR,5,5	
55	0045		STX,5,1	
56	0046		ABR,1,15	
57	0047		FE51	
58	0048		FE51	
59	0049		601F	
60	004A		STM,1	
	004B	R	UINI	
61	004C		E.SF1,START	START UP IF FLAG SET
	004D	R	TBMB,0	
62	004E	R	ABMB,15	TEN SAMPLES YET
	004F		SCONT,E:SKD	
	0050	R	-440	YES, MOVE BUFFER TO WORKING STORE
63	0051	X	UPDAT LDI,2	
	0052		LDI,3	
64	0053	A	ADI,3	
	0054		LDI,3	
65	0055	X	ADI,3	
	0056		LDI,3	
66	0057	A	ADI,3	
	0058		LDI,3	
67	0059	R	LOOP1 LDM,1,2	GET NEXT VALUE
	005A		STX,1,3	STORE IT

MODCOMP ASSEMBLY STATUS CHANGE INTERRUPT ROUTINE.....

89				
90	0080	8200		
	0081	X 0009		
91	0082	2500		

* INTERRUPT RESPONSE ROUTINE, SCHEDULES E:UIN
E:ANL 0BMM,0 E.T03
CIR

```

93      * E$MIT IS THE ROUTINE WHICH TRANSMITS DIGITAL DATA TO THE RECEIVER
94      * FROM THE APROCESSOR.
95      * CALLING SEQUENCE.....
96      *   BLM,5      E:MIT
97      *   WITH CHANNEL DESIGNATOR IN P3 (0 FOR A, ETC.)
98      *   SAVE R6, R7 FOR INTERNAL USE
          E:MIT SFM,6      SVR67
          LDM,6,3      U,TRK      TRACK RATE IN BITS 0 - 3.
          LLS,6,5      U,GAN      MAKE ROOM FOR GAIN
          ORM,6,3
          LLS,6,6      U,RLI      ADD IN BLANKING LEVEL IN BITS 9 - 14
          ORM,6,3
          LLS,6,1      SHIFT      MAKE ROOM FOR SHIFT BIT
          ORM,6
          TRF,7,3      ADI,7      #4660      PUT SHIFT BIT IN BIT 15, OUTPT READY
          STM,7      $+2
          DFC      0      EXECUTE OUTPUT
          LFM,6      SVR67      RESTORE REGISTERS
          BRX,5      RETURN
          SVR67 RES      2
          * INPUT BUFFER.....
          * TIME/VALUE.....
          * 0  AMPA FAZA BLNA AMP3 FAZE BLNB .....AMPN FAZN BLNN TIME~
          * +1  AMPA FAZA BLNA .....
          * +2  AMPA FAZA BLNA .....
          * +3  AMPA FAZA BLNA .....
          * ..... THRU .....
          * +9  AMPA FAZA BLNA .....
          * OP (14 CHANS * 3 INPUTS + TIME)* 10 SECONDS = 440 WORDS
          F,UBU RES      42,0      INPUT BUFFER
          E,URE RES      440,0      CYCLE SHIFT BIT
          SHIFT DFC      0      SAMPLE COUNTER
          SCONT DFC      -10      START FLAG
          E,SFI DFC      0
          END
0083      A560
0084      R 0099
0085      F563
0086      X 0003
0087      2D65
0088      F363
0089      X 0004
008A      2D66
008B      F363
008C      X 0005
008D      2D61
008E      F360
008F      R 027D
0090      6D73
0091      F870
0092      A 4660
0093      0E70
0094      R 0095
0095      A 0000
0096      A460
0097      F 0099
0098      FF05
0099
009B      A 0000
00C5      A 0000
027D      A 0000
027E      A FFF6
027F      A 0000
0280      END

```

1	PGM	E\$SUB
2	INT	E:PWD,E:PAR,E:ABR,B:ORT
3	EXT	E:FRT,E.TM1,E.TM2,E:FCT,E.28F
4	EXT	E:028,E:ANF,B.STT,E.350,E.T10
5	EXT	E.I0F,E:035,E:SKD
6		CAT NO. 40000-019
7		* E:PWD POWER DOWN SYSTEM INTERRUPT
8		* E:PMU POWER UP SYSTEM INTERRUPT
9		* E:PAR MEMORY PARITY ERROR SYSTEM INTERRUPT
10		* E:ABR CONSOLE INTERRUPT


```

12          ED10          0000
13          R            0010
14          E610          0002
15          A            0021
16          ED10          0004
17          A            4000
18          431F          0006
19          4F2F          0007
20          0D22          0008
21          E620          0009
22          R            006F
23          4F2F          000C
24          0D22          000D
25          F620          000E
           P            0070
           F            F700

           ED10          0010
           R            0021
           A            4000
           ED10          0021
           A            4000
           431F          0021
           4F2F          0021
           0D22          0021
           E620          0021
           R            006F
           4F2F          0021
           0D22          0021
           F620          0021
           P            0070
           F            F700

           STM,1         #21
           LDI,1         #4000
           OCC,1,#F
           IDD,2,#F
           TOR,2,2
           STM,2
           PWDI1
           IDD,2,#F
           TOR,2,2
           STM,2
           PWDI1+1
           HOP,$
           WAIT FOR PWR LOSS
           READ PWR DOWN TIME

```

* E:PWD IS ENTERED JUST BEFORE THE PROCESSOR POWER
* DROPS OUT.
E:PWD LDI,1

```

27          0010          ED10
28          0011          R 0000
29          0012          E610
30          0013          A 0021
31          0014          E7E0
32          0015          X 0001
33          0016          R 007C
34          0017          E510
35          0018          R 006F
36          0019          E610
37          001A          X 0002
38          001B          E510
39          001C          R 0070
40          001D          E610
41          001E          X 0003
42          001F          E7E0
43          0020          X 0004
44          0021          R 0073
45          0022          6C11
46          0023          E610
47          0024          X 0005
48          0025          E7E0
49          0026          X 0006
50          0027          R 0071
51          0028          E700
52          0029          X 0007

```

* E:PWU IS ENTERED WHEN POWER IS RESTORED.
E:PWU LDI,1
STM,1 #21
BLM,14 E:FRT POWER UP TIME
DFC PMS1
LDM,1 PWDTI
STM,1 E.TM1
LDM,1 PWDTI+1
STM,1 E.TM2
BLM,14 E:FCT
DFC PWRMS+2
ZRP,1
STM,1 E.28F
BLM,14 E:028 LOG DOWN/UP
DFC PWRMS
BRU E:ANF

```

45          E7E0
46          002A X 0001
47          002C P 0087
48          002D A E570
          002E A 0022
          002F FD30
          0030 A FFFE
          0031 6C66
          0032 2C64
          0033 0926
          0034 E960
          0035 A 000A
          0036 7660
          0037 R 003B
          0038 E820
          0039 A 3700
          003A F703
          003B E820
          003C A 3000
          003D 6C66
          003E 2C64
          003F 6826
          0040 E960
          0041 A 000A
          0042 7660
          0043 R 0047
          0044 E820
          0045 A 0037
          0046 F703
          0047 E820
          0048 A 0030
          0049 E623
          004A R 0093
          004B 703F
          004C P 0031
          004D 6C66
          004E E660
          004F X 0005
          0050 E7E0
          0051 X 0006
          0052 R 0085
          0053 80F0
          0054 R 0094
          0055 0055
          0056 R 0094
          0057 7610
          0058 X 0007

          E:PAR BLM,14 E:FPT
          DEC PAPMS+2
          LDM,7 #22
          LDI,3 -2
          PAR1 7RP,6
          LLD,6,4
          MRL,2,6
          SUI,6 #A
          TRRB,6,0 $+5
          ADI,2 #3700
          HOP,$+3
          ADI,2 #3000
          ZRP,6
          LLD,6,4
          ADR,2,6
          SUI,6 #A
          TRRB,6,0 $+5
          ADI,2 #37
          HOP,$+3
          ADI,2 #30
          STM,2,3 PARS1+4
          ABRB,3,15 PARI
          ZRP,6
          STM,6 E.28F
          BLM,14 E:028
          DEC PARS
          ABMM,15 PARCT
          LDM,1 PARCT
          TBRB,1,0 E:ANF

          FIRST DIGIT
          DIGIT 0-9

          LOG MP FOUND
          TRY TWO RESTARTS
    
```

 * E:PAR IS ENTERED WHEN A MEMORY PARITY ERROR IS
 * DETECTED.
 E:PAR BLM,14 E:FPT

MODCOMP ASSEMBLY MEMORY PARITY ERROR ROUTINE.....

77	0059	2740	RIE,#0
78	005A	2741	RIE,#1
79	005B	274C	RIE,#C
80	005C	274D	RIE,#D
81	005D	274E	RIE,#E
82	005E	274F	RIE,#F
83	005F	F700	HOP,\$

KILL SYSTEM

MDDCOMP ASSEMBLY

CONSOLE INTERRUPT ROUTINE.....

```

85
86
87      0060      8200
      0061      X 000A
      0062      2500
88
89
90      0063      6C11
      0064      E610
      0065      X 0008
      0066      E610
      0067      X 0009
      0068      E610
      0069      X 0008
      006A      E7E0
      006R      X 000C
      006C      R 0095
      006D      E700
      006E      X 000D

*****
* CONSOLE INTERRUPT USED TO ABORT BACKGROUND PROGRAM
E:ABR 0BMM,0 E.T10
*****
CIR
*****
* B:OPT LOGS THE OPERATOR INDUCED ABORTION.
*****
R:OPT ZRP,1 B.STT ABORT TASK
STM,1 E.350 CLEAR ASR 35
STM,1 E.IOF
BLM,14 E:035
DFC RMSC
BRU E:SKD
RETURN
*****

```

SUBROUTINE SUPPORT.....

MODCOMP ASSEMBLY

```

99 *****
100 PWD T RES 2,0 POWER TIME *****
101 PWRMS DFC #0A0A,#0D0A,|123 123456 T PFI,#0D0A *****

102 PMS1 DFC |123 123456 T PRI,0

103 PARM5 DFC #0D0A,#0D0A,|123 123456 T MPI

104 PAF S1 DFC | AT 00001,0

105 PARCT DFC -2
106 BMSG DFC #0D0A,|BACKGROUND TASK ABORTED |

006F A 0000
0071 A 0A0A
0072 A 0D0A
0073 A 3132
0074 A 3320
0075 A 3132
0076 A 3334
0077 A 3536
0078 A 2020
0079 A 5420
007A A 5046
007B A 0D0A
007C A 3132
007D A 3320
007E A 3132
007F A 3334
0080 A 3536
0081 A 2020
0082 A 5420
0083 A 5052
0084 A 0000
0085 A 0D0A
0086 A 0D0A
0087 A 3132
0088 A 3320
0089 A 3132
008A A 3334
008B A 3536
008C A 2020
008D A 5420
008E A 4D50
008F A 2041
0090 A 5420
0091 A 3030
0092 A 3030
0093 A 0000
0094 A FFFE
0095 A 0D0A
0096 A 4241
0097 A 434B
0098 A 4752
0099 A 4F55
009A A 4E44
009B A 2054
009C A 4153
009D A 4R20
009E A 4142
009F A 4F52
00A0 A 5445

```

MODCOMP ASSEMBLY SUBROUTINE SUPPORT.....

```
107      00A1 A 4420
          00A2 A 4259
          00A3 A 204F
          00A4 A 5045
          00A5 A 5241
          00A6 A 544F
          00A7 A 522E
          00A8 A 0D0A
          00A9 A 0000
          END
          DFC      IRY OPERATOR.#0D0A,0
```

108 TOTAL ASSEMBLY ERRORS = 0 SIZE = 170


```

1 1 0000          A440
2 2 0001          X 0001
3 3 0002          0966
4 4 0003          6876
5 5 0004          0964
6 6 0005          6865
7 7 0006          ED50
8 8 0007          A FFE4
9 9 0008          6D26
10 10 0009          6D37
11 11 000A          E425
12 12 000B          P 0112
13 13 000C          7D22
14 14 000D          R 0014
15 15 000E          E435
16 16 000F          R 0113
17 17 0010          R 0014
18 18 0011          R E700
19 19 0012          R 0020
20 20 0013          R 705D
21 21 0014          R 0008
22 22 0015          R 7607
23 23 0016          R 005E
24 24 0017          R 6C44
25 25 0018          X E640
26 26 0019          X 0003
27 27 001A          X F7E0
28 28 001B          X 0002
29 29 001C          R 0112
30 30 001D          R E700
31 31 001E          X 0006
32 32 0020          8605
33 33 0021          R 0114
34 34 0022          R 002F
35 35 0023          8615
36 36 0024          R 0114
37 37 0025          R 0058

```

PGM INT
EXT
EXT
EXT
CAT NO. 400000-002
* THIS IS THE OPERATOR'S MESSAGE INTERPRETOR ROUTINE. IT IS SCHEDULED
* IN RESPONSE TO AN OPERATOR MESSAGE VIA THE ASR 35. E\$OMI EXAMINES
* THE MESSAGE FOR FORMAT VALIDITY AND SCHEDULES APPROPRIATE TASKS IN
* RESPONSE.
E:\$OMI L\$M,4 F.351 GET FIRST 4 INPUT CHARS.
MBL,6,6 PACK CHARS. IN R6 AND R7
ORR,7,6
MBL,6,4
ORR,6,5
LDI,5
-28 SET TABLE POINTER FOR FIRST ENTRY
OMI1 TRR,2,6 COPY KEYWORD IN R2,R3
TRR,3,7
XCM,2,5 DO THESE MATCH?
MSGTB+28
OMI2 TRRB,2,2 NO...
XOM,3,5 MSGTB+29 MAYBE...
TRRB,3,3 OMI2 NO...
BRU FOUND YES...
OMI2 ABRB,5,13 NEXT ENTRY IF ANY UNCHECKED.
TRRB,0,7 OMI6
OMI7 ZRR,4
STM,4 E.ICF SEND MSG. TO OPERATOR ON ASR 35
BLM,14 E:035 I INVALID KEYWORD, MSG. IGNORED!
DFC MSGI
BRU E:SKD

* MATCH FOUND.....
* SET UP DESIRED OPERATION
FOUND TBMB,0,5 MSGTB+30,TLOG BIT 0 SET FOR LOG INPUT
TBMB,1,5 MSGTB+30,TSET BIT 1 SET FOR MAJOR TASK REQUEST

35	0026	F525	LDM,2,5	MSGTB+30	BACKGROUND TASK REQUEST, GET B.STT
	0027	R 0114			
36	0028	OC22	MLF,2,2	R.STT	CLEAR UPPER BYTE
37	0029	E620	STM,2		
	002A	X 0007			
38	002B	8200	DBMM,0	E.T15	SCHEDULE BACKGROUND TASK.
39	002C	X 0008			
	002D	E700	BRU	E:SKD	RETURN
	002E	X 0006			
40	002F	E7E0	BLM,14	E:FRT	GET REAL TIME AND FORMAT FOR OUTPUT
	0030	X 000B			
41	0031	R 0125	DFC	MSG2+1	
42	0032	ED20	LDI,2	E.35I	
	0033	X 0001			
43	0034	602D	ARR,2,13		ADDR OF FIFTH INPUT CHARACTER
44	0035	* 6C33	ZRR,3		OUTPUT BUFFER POINTER
45	0036	FD42	LDX,4,2		GET NEXT CHARACTER
46	0037	7D54	TRRR,4,4	\$+4	BRANCH ON END OF MESSAGE
	0038	R 003B			
47	0039	E700	BRU	OMI4	
	003A	R 004E			
48	003B	0955	MBL,5,5		
49	003C	602F	ARR,2,15		GET NEXT CHARACTER
50	003D	FD42	LDX,4,2		BRANCH ON END OF MESSAGE
51	003E	7D44	TRRR,4,4	\$+4	
	003F	R 0042			
52	0040	E700	BRU	OMI5	
	0041	R 0049			
53	0042	6B54	ORR,5,4		
54	0043	E653	STM,5,3	MSG2A	
	0044	R 012C			
55	0045	603F	ARR,3,15		
56	0046	602F	ARR,2,15		
57	0047	E700	BRU	OMI3	
	0048	R 0036			
58	0049	E350	ORR,5	SPACE	FILL WORD WITH SPACE CHARACTER
	004A	F 0123			
59	004B	F653	STM,5,3	MSG2A	
	004C	R 012C			
60	004D	603F	ARR,3,15		
61	004E	6C55	ZRR,5		
62	004F	E653	STM,5,3	MSG2A	MESSAGE TERMINATOR
	0050	R 012C			
63	0051	E650	STM,5	E.28F	
	0052	X 000D			
64	0053	E7E0	RLM,14	E:028	
	0054	X 0004			
65	0055	R 0124	DFC	MSG2	
66	0056	E700	BRU	E:SKD	RETURN
	0057	X 0006			

MODCOMP ASSEMBLY

67	0058	E545	TSET	LDM,4,5	MSGTB+31	GET E\$SKD ENTRY
68	0059	R 0115				
	005A	R 8204				
	005R	A 0000		ORMM,0,4	0	SET SCHEDULE BIT FOR THAT ENTRY
69	005C	E700		BRU	E:SKD	AND RETURN.
	005D	X 0006				

```

71          005E      6D26
72          005F      6D37
73          0060      EC20
74          0061      A      4449
75          0062      R      7D22
76          0063      R      006A
77          0064      EC30
78          0065      A      533A
79          0066      R      7D33
80          0067      R      006A
81          0068      E700
82          0069      R      0086
83          006A      6D26
84          006B      6D37
85          006C      EC20
86          006D      A      5345
87          006E      7D22
88          006F      R      0018
89          0070      EC30
90          0071      A      543A
91          0072      R      7D33
92          0073      R      0018
93          0074      ED10
94          0075      X      0001
95          0076      6010
96          0077      E7E0
97          0078      R      00A9
98          0079      6DF4
99          007A      FD51
100         007B      601F
101         007C      F950
102         007D      A      003D
103         007E      7D55
104         007F      R      00EE
105         0080      E7E0
106         0081      R      00A9
107         0082      FF4F
108         0083      6D4F
109         0084      E700
110         0085      R      008B
111         0086      ED10
112         0087      X      0001

*****
* IF BIT 7 IS SET IN THE SWITCH REGISTER, WE ENABLE
* TWO ADDITIONAL OPERATOR REQUESTS:
* DIS:HHHH DISPLAY CONTENTS OF #HHHH ON ASR 35
* SET:HHHH=CCCC STORE #CCCC IN #HHHH
OMI6 TRP,2,6
TRP,3,7
XOI,2          #4449      IDI
TRPR,2,2      OMI8      NOT DIS REQUEST
XOI,3          #533A      IS:I
TRPR,3,3      OMI8      NOT DIS REQUEST
BPU           DISPL
OMI8 TRF,2,6
TRR,3,7
XOI,2          #5345      ISEI
TRPR,2,2      OMI7      NOT SET REQUEST
XOI,3          #543A      IT:I
TRPR,3,3      OMI7      NOT SET REQUEST

* SET: REQUEST FOUND
LDI,1          E.351
ABF,1,13
BLM,14        GETHX
TRP,15,4
LDX,5,1
ARP,1,15
SUI,5         #003D
TRPR,5,5     FMTER
BLM,14        GETHX

* SET LOCATION TO VALUE
STX,4,15
TRR,4,15
BPU
* DIS: REQUEST FOUND
DISPL LDI,1   E.351
POINT TO FIRST DIGIT
GET ADDR IN R4
HOLD ADDR.
CHECK FOR = SIGN
NOT PROPER FORMAT
STORE VAL
LOG CHANGE
    
```

105	0088	601D	ABR,1,13	GETX	GET ADDR TO BE DISPLAYED
106	0089	E7E0	RLM,14		
107	008A	00A9			
108	008B	FD54	DISP1 LDX,5,4	GET VALUE	
109	008C	ED10	* DISPLAY ADDRESS IN R4 AND VALUE IN R5		
110	008D	012C	LDI,1	MSG2A	
111	008E	EDF0	LDI,15	#203C	STORE I < I FOR OUTPUT
112	008F	203C	STX,15,1		R7=ADDR
113	0090	FEF1	ABR,1,15		
114	0091	601F	TRP,7,4	PUTX	
115	0092	6D74	BLM,14		
116	0093	E7E0	LDI,15	#3E3D	STORE I >= I FOR OUTPUT
117	0094	00CF	STX,15,1		R7=VALUE
118	0095	EDF0	ABR,1,15		
119	0096	3E3D	TRP,7,5	PUTX	
120	0097	FEF1	RLM,14		
121	0098	601F	LDI,15	#0D0A	
122	0099	6D75	STX,15,1		TERMINATE MSG
123	009A	E7E0	ABR,1,15		OUTPUT VALUE
124	009B	00CF	ZRR,15	MSG2A	
125	009C	EDF0	STX,15,1	E:SKD	
126	009D	0D0A	STM,15		
127	009E	FEF1	BLM,14	E:D35	
128	009F	601F	DFC	MSG2A	
129	00A0	6CFF	BRU	E:SKD	
130	00A1	FEF1			
131	00A2	E6F0			
132	00A3	0003			
133	00A4	E7E0			
134	00A5	X 0002			
135	00A6	R 012C			
136	00A7	E700			
137	00A8	X 0006			
138	00A9	E0D0			
139	00AA	FFFF			
140	00AB	FD51			
141	00AC	601F			
142	00AD	E950			
143	00AE	A 0030			
144	00AF	7650			
145	00B0	R 00BD			
146	00B1	E950			
147	00B2	A 000A			
148	00B3	7650			
149	00B4	R 00C5			
150	00B5	E950			
151	00B6	A 0007			
152	00B7	7650			

* GETX RETRIVES HEX CHARS FROM INPUT BUFFER
GETX LDI,13

-4

GETX LDX,5,1
ABR,1,15
SUI,5
#30
HXERR
VAL<30

SUI,5
#A
GETH2
#7
CHECK DIGIT A-F

HXERR

SECRET OPTIONAL REQUESTS.....

MODCOMP ASSEMBLY

139	00B8	R	00RD						
	00R9		E950		SUI,5	#6			
140	00BA	A	0006		TBRB,5,0	GETH3			
	00BB		7650						
	00BC	R	00C8						
141	00BD		6C44		ZRR,4	E:ICF			INVALID HEX DIGIT
142	00BE	X	E640		STM,4				
	00BF		0003						
143	00C0	X	E7E0		BLM,14	E:035			
	00C1		0002						
144	00C2	X	0154		DFC	MSG3			
145	00C3	R	E700		BRU	E:SKD			
	00C4		0006						
146	00C5	X	E850		GETH2 ADI,5	#A			DIGIT 0-9
	00C6		000A						
147	00C7	A	F703		HOP,\$+3				
148	00C8		F850		GETH3 ADI,5	#10			DIGIT A-F
	00C9		0010						
149	00CA	A	2D5C		LLS,5,12				
150	00CB		2C44		LLD,4,4				MOVE DIGIT TO R4
151	00CC		70DF		ABRR,13,15	GETH1			
	00CD	R	00AB						
152	00CE		FF0E		BRX,14				RETURN, R4=VAL
153	00CF		E000		* PUTHX STORES HEX VALUES IN OUTPUT BUFFER				
154	00D0	A	FFFE		PUTHX LDI,13	-2			
	00D1		6C66		PUTHI ZRR,6				
156	00D2		2C64		LLD,6,4				
157	00D3		0926		MBL,2,6				FIRST DIGIT
158	00D4		E960		SUI,6	#A			
	00D5	A	000A						
159	00D6		7660		TBRB,6,0	\$+5			
	00D7	R	00DR						
160	00D8		E820		ADI,2	#3700			DIGIT A-F
	00D9	A	3700						
161	00DA		F703		HOP,\$+3				
162	00DB		E820		ADI,2	#3000			DIGIT 0-9
	00DC	A	3000						
163	00DD		6C66		ZRR,6				
164	00DE		2C64		LLD,6,4				
165	00DF		6826		ADR,2,6				SECOND DIGIT
166	00E0		E960		SUI,6	#A			
	00E1	A	000A						
167	00E2		7660		TBRB,6,0	\$+5			
	00E3	R	00E7						
168	00E4		E820		ADI,2	#37			DIGIT A-F
	00E5	A	0037						
169	00E6		F703		HOP,\$+3				
170	00E7		E820		ADI,2	#30			
	00E8	A	0030						

171	00E9	FF21	STX,2,1		
172	00EA	601F	ABR,1,15		
173	00EB	700F	ABRB,13,15	PUTH1	
	00EC	R 00D1			
174	00ED	FF0E	BRX,14		RETURN
175	00EE	6C44	* FORMAT ERROR MSG OUTPUT		
176	00EF	E640	FMT ER ZRP,4		
177	00F0	X 0003	STM,4	E.IOF	
178	00F1	E7E0	BLM,14	E:035	
	00F2	X 0002			
179	00F3	P 015F	DFC	MSG4	
180	00F4	E700	BRU	E:SKD	
	00F5	X 0006			

* THE TABLE OF VALID MESSAGES GIVES NECESSARY INFORMATION TO RESPOND
 * TO THE OPERATOR'S REQUEST. THE FOLLOWING GIVES THE FORMAT OF THE
 * ENTRIES.
 * WORD 1..FIRST TWO CHARACTERS OF KEYWORD.
 * WORD 2..LAST TWO CHARACTERS OF KEYWORD.
 * WORD 3..BIT 0..SET FOR LOG MESSAGE
 * BIT 1..SET FOR TASK SCHEDULED
 * BIT 2..SET FOR BACKGROUND TASK
 * BIT 3-15..B.STT VLLUE FOR BACKGROUND TASK
 * WORD 4..ADDRESS OF TASK TO BE SCHEDULED IN E:SKD.
 * MSGTR DFC ILOG:!,#8000,#0000

187	00F6	A	4C4F	
183	00F7	A	473A	
184	00F8	A	8000	
185	00F9	A	0000	
186	00FA	A	4446	
187	00FB	A	433A	
188	00FC	A	2001	
189	00FD	X	0008	
190	00FE	A	4444	
191	00FF	A	443A	
192	0100	A	2002	
	0101	X	0008	
	0102	A	4444	
193	0103	A	413A	
	0104	A	2003	
	0105	X	0008	
194	0106	A	4F50	
	0107	A	433A	
	0108	A	4000	
	0109	X	000C	
195	010A	A	5354	
	010B	A	443A	
	010C	A	4000	
	010D	X	0009	
196	010E	A	4352	
	010F	A	433A	
	0110	A	4000	
	0111	X	000A	
	0112	A	494E	
197	0113	A	5641	
	0114	A	4C49	
	0115	A	4420	
	0116	A	4B45	
	0117	A	5957	
	0118	A	4F52	
	0119	A	442C	
	011A	A	204D	
	011B	A	5347	
	011C	A	2E20	
	011D	A	4947	

DFC IDFC:!,#2001,F.T15
 DFC IDDD:!,#2002,F.T15
 DFC IDDA:!,#2003,F.T15
 DFC IDPC:!,#4000,F.T13
 DFC ISTD:!,#4000,F.T08
 DFC ICRC:!,#4000,F.T04
 MSG1 DFC IINVALID KEYWORD, MSG. IGNORED!,#000A,0

011E	A	4E4F			
011F	A	5245			
0120	A	4420			
0121	A	0D0A			
0122	A	0000			
200	0123	A	0020	SPACE	DFC
201	0124	A	0D0A	MSG2	DFC
	0125	A	3132		
	0126	A	3320		
	0127	A	3132		
	0128	A	3334		
	0129	A	3536		
	012A	A	204C		
	012B	A	3A20		
202	012C	A	0000	MSG2A	RES
203	0154	A	494E	MSG3	DFC
	0155	A	5641		
	0156	A	4C49		
	0157	A	4420		
	0158	A	4845		
	0159	A	5820		
	015A	A	4449		
	015B	A	4749		
	015C	A	5420		
	015D	A	0D0A		
	015E	A	0000		
204	015F	A	494E	MSG4	DFC
	0160	A	5641		
	0161	A	4C49		
	0162	A	4420		
	0163	A	464F		
	0164	A	524D		
	0165	A	4154		
	0166	A	0D0A		
205	0167	A	0000		
	0168			END	

#0020
 #0D0A,|123 123456 L: |

40,0 LOG MSG BUFFER
 |INVALID HEX DIGIT|,#0D0A,0

|INVALID FORMAT|,#0D0A,0

```

1
2
3
4
5
6
7
8
9
10

PGM          E:T28
INT          E:I28,E:T28,E:028,E:28F,E:280
FXT          F:T05,F:T07,F:SKD
CAT NC.     400000-015
*           *
* $T28 IS THE ASP 28 HANDLER. THE PROGRAM CONSISTS
* OF THREE PARTS:
* E:I28 RESPONDS TO A DI FROM THE ASR 28
* E:T28 OUTPUTS BAUDOT CHARS TO THE ASR 28
* F:028 TRANSLATES ASCII TO BAUDOT AND BUFFERS
* THE OUTPUT FOR E:T28.
*

```

MODCOMP ASSEMBLY

E: I28.....

```

12
13
14
15
16
17

```

0000	8200	
0001	X 0001	
0002	2500	CIF

* E: I28 RESPONDS TO THE DI (DATA INTERRUPT) GENERATED
* BY THE COMPLETION OF EACH CHARACTER TRANSFER.
* THIS ROUTINE SCHEDULES E: T28 IN E\$SKD.
E: I28 DBMM, 0 E: T05

```

19          ED20
20          R 00E0
21          E530
22          R 00DF
23          R 8620
24          R 08E0
25          R 0018
26          000A 603F
27          000R 7633
          000C R 000E
          000D F702
          000E 6C33
          000F E630
          0010 R 00DF
          0011 6D43
          0012 E140
          0013 R 00DE
          0014 7D44
          0015 R 001A
          0016 E700
          0017 R 0023
          0018 8120
          0019 R 08E0
          001A AE92
          001B A450
          001C R 08E1
          001D 4158
          001E 4169
          001F 4599
          0020 4178
          0021 E700
          0022 X 0003
          0023 8100
          0024 R 08E0
          0025 8610
          0026 R 08E0
          0027 R 002A
          0028 E700
          0029 X 0003
          002A 8200
          002B X 0002
          002C 8110

          LDM,3 PNTOT BYTE POINTER
          TRMB,2 T28IC,T281
          ABR,3,15 MOVE POINTER TO NEXT BYTE (CHAR)
          TRPR,3,3 $+3
          HOP,$+2 RECYCLE POINTER
          ZRR,3 STORE UPDATED POINTER
          STM,3 PNTOT
          TRR,4,3 PNTIN
          SUM,4 $+6 CHECK FOR EMPTY BUFFER
          TRRR,4,4 ?
          BRU EMPTY
          T281 ZRMM,2 T28IO
          LBX,9,2 GET CHAR FROM BUFFER
          LFM,5 CONTP GET CONTROL WORDS
          OCB,5,8
          CCR,6,9 OUTPUT CHAR (R9)
          ODB,9,9 RETURN
          OCR,7,8
          BRU E:SKD
          EMPTY ZRMM,0 T28IO CLEAR OUTPUT FLAG
          TRMB,1 T28IO,RESCH RESCH REPORT?
          BRU BRU NO, RETURN
          RESCH ORMM,0 E.T07 RESCHEDULE REPORT GENERATOR
          ZBMM,1 T28IO CLEAR RESCH BIT

```

```

*****
* E:T28 IS ENTERED AFTER THE TRANSFER OF A CHARACTER
* HAS BEEN COMPLETED AND THE ASP 28 IS READY TO
* ACCEPT ANOTHER.
* THEREFORE, THIS ROUTINE CHECKS TO SEE IF THE BUFFER
* IS EMPTY.
* IF NOT, THE POINTER SHOWS WHEPE THE
* NEXT CHARACTER COMES FROM, SO IT SENDS IT.
* BYTE ADDRESSING SET UP.
E:T28 LDI,2

```

MODCOMP ASSEMBLY E:T28.....

52	002D	R	08E0						
	002E		E700						E:SKD
	002F	X	0003		BRU				

```

54 *****
55 *****
56 *****
57 *****
58 *****
59 *****
60 *****
61 *****
62 *****
63 *****
64 *****
65 *****
66 *****
67 *****
68 *****
69 *****
70 *****
71 *****
72 *****
73 *****
74 *****
75 *****
76 *****
77 *****
78 *****
79 *****
80 *****
81 *****
82 *****
83 *****
84 *****
85 *****
86 *****
87 *****
88 *****

* E:028 IS THE WAY THE SYSTEM SETS UP OUTPUT MESSAGES
* FOR THE ASR 28. A PROGRAM CALLS THIS ROUTINE
* TO HAVE A MESSAGE TRANSLATED FROM ASCII TO
* BAUDOT AND THOSE CHARACTERS STUFFED AWAY IN
* THE CHRBF.
* CALLING SEQUENCE:
*   RLM,14   E:028
*   (ADDRESS OF FIRST CHAR. TO BE PRINTED)
* THE FIRST WORD FOLLOWING THE MESSAGE MUST BE #0000
* E.28F IS CHECKED AND SETS FLAGS IN I2810 ACCORDINGLY.
E:028 SFM,1   SAVPS
                LDX,2,14
                ABR,14,15
                ZRF,3
                LDI,4   CHRBF
                LDI,10  BAUDT
                LDM,5   PNTIN
                ENTRY POINTER
                GET START ADDRESS
                MOVE RETURN ADDR.
                R3 INPUT BYTE POINTER
                BASE OF CHARACTER BUFFER

* SET UP FIRST CHAR TO BE ILTR MODE| SELECT
  LDI,1   #1F
                STORE
                BLM,7   STORE CHARACTER
                ZRMM,8   FLAG
                SET FOR LTR MODE
                GET ASCII CHAR
                MOVE POINTER
                IS CHAR=0
                YES, DONE
                TRANSLATE TO BAUDOT CHAR
                CHECK MODE VS. CUR. MODE
                RIT 8 SET = > CHANGE MODE.
                STORE BAUDOT CHAR

                STRIT ZBR,1,8
                RLM,7   STORE
                BRU     NXTWF
                * MDCHN CHANGES MODE
                MDCHN TRMB,8   FLAG,FTOL
                MDCHN R 0041
                8680
                0052 R 08EC
                0053 R 08EC
                0054 R 0061
                A510
                0030 R
                0031 R 08E5
                0032 FD2E
                0033 60EF
                0034 6C33
                0035 ED40
                0036 R 00E0
                0037 EDA0
                0038 R 009E
                0039 E550
                003A R 00DE
                003B ED10
                003C A 001F
                003D F770
                003E R 008C
                003F 8180
                0040 R 08EC
                0041 AER2
                0042 603F
                0043 7D8B
                0044 R 0047
                0045 E700
                0046 R 006D
                0047 AE1A
                0048 F560
                0049 R 08EC
                004A 6C61
                004B 7668
                004C R 0052
                004D 621B
                004E E770
                004F R 008C
                0050 E700
                0051 R 0041
                8680
                0052 R 08EC
                0053 R 08EC
                0054 R 0061

```



```

89      0055      E610
90      0056      R 005E      * CHANGE LTR TO FIG.....
          STM,1      HOLD1
91      0057      ED10      LDI,1      #1B      I FIG| CHARACTER
          0058      A 001B      BLM,7      STORE
          0059      E770      OBMM,8      FLAG      SET BIT 8 FOR FIG.
          005A      R 008C      LDI,1      0
          005B      8280      HOLD1 EQU  $-1
          005C      R 08EC      BRU      STRIT
          005D      ED10
          005E      A 0000
          005F      R 005E
          0060      R 004D
          0061      E610      * CHANGE FIG TO LTR
          0062      R 006A      FTOL STM,1      HOLD2
          0063      ED10      LDI,1      #1F      ILTR| CHARACTER
          0064      A 001F      BLM,7      STORE
          0065      E770      ZBMM,8      FLAG      RESET BIT 8 FOR LTR
          0066      R 008C      LDI,1      0
          0067      8180      HOLD2 EQU  $-1
          0068      R 08EC      BRU      STRIT
          0069      ED10
          006A      A 0000
          006B      R 006A
          006C      R 004D
          006D      8600
          006E      R 08E4
          006F      R 0071
          0070      F703      HOP,$+3      NO
          0071      8210      OBMM,1      T281C      YES, FLAG T2810
          0072      R 08E0      TBMB,0      T2810,RETRN  IS OUTPUT UNDERWAY?
          0073      8600
          0074      R 08E0
          0075      P 0089
          0076      ED50
          0077      A 4002
          0078      4158
          0079      4919      OCB,$+8
          007A      7613      ISR,1,9
          007B      R 007E      TBRB,1,3      $+4      NO POWER TO 28.
          007C      F700      BRU
          007D      R 009R      OBMM,0      T281C
          007E      8200
          007F      R 08E0
          0080      8600
          0081      R 08E4
          0082      R 0071
          0083      F703
          0084      8210
          0085      R 08E0
          0086      8600
          0087      R 08E0
          0088      P 0089
          0089      ED50
          0090      A 4002
          0091      4158
          0092      4919
          0093      7613
          0094      R 007E
          0095      F700
          0096      R 009R
          0097      8200
          0098      R 08E0
          0099      8600
          0100      R 08E4
          0101      R 0071
          0102      F703
          0103      8210
          0104      R 08E0
          0105      8600
          0106      R 08E0
          0107      P 0089
          0108      ED50
          0109      A 4002
          0110      4158
          0111      4919
          0112      7613
          0113      R 007E
          0114      F700
          0115      R 009R
          0116      8200
          0117      R 08E0

```

* FINI WHEN BUFFER FULL, OR NO MORE DATA.
FINI TBMB,0 E.28F,\$+4 DESIRE RESCHEDULE U.RP3?

* NOTHING GOING ON WITH ASR 28, SO LET'S START SOMETHING
LDI,5 #4002

FLAG OUTPUT UNDERWAY

118	0080	8220	ORBMM,2	T2810	
	0081	08E0			GET CONTROL WORDS
119	0082	A450	LFM,5	CONTR	
	0083	08E1			SEND NULL CHAR
120	0084	6C99	7RR,9		
121	0085	4158	OCR,5,8		
122	0086	4169	OCR,6,9		
123	0087	4599	ODD,9,9		START OUTPUT
124	0088	4178	OCR,7,8		
125	0089	A410	RETRN LFM,1	SAVRS	RESTORE REGISTERS
	008A	08E5			AND RETURN
126	008B	FF0E	BRX,14		


```

146 *****
147 * TABLE IS PACKED WITH 1 CHAR PER BYTE. THE MSB
148 * OF A BYTE IS SET IF THE CHAR IS IN THE IFIGSI
149 * MODE. THE BITS ARE STORED IN THE 5 LEAST
150 * SIGNIFICANT BITS OF THE BYTE, IN REVERSE ORDER.
151 * FOR EXAMPLE, THE LETTER JAJ IS NORMALLY #18.
152 * IT IS STORED IN THE TABLE AS #03.
153 BAUDOT DFC #0000,#0000,#0000,#0000,#0000,#0000

```

009E	A	0000	DFC	
009F	A	0000		
00A0	A	0000		
00A1	A	0000		
00A2	A	0000		
00A3	A	0200	DFC	#0200,#0008,#0000,#0000,#0000
00A4	A	0008		
00A5	A	0000		
00A6	A	0000		
00A7	A	0000		
00A8	A	0000	DFC	#0000,#0000,#0000,#0000,#1B00
00A9	A	0000		
00AA	A	0000		
00AB	A	0000		
00AC	A	1B00		
00AD	A	001F	DFC	#001F,#048D,#9194,#899D,#9A8B
00AE	A	048D		
00AF	A	9194		
00B0	A	899D		
00B1	A	9A8B		
00B2	A	8F92	DFC	#8F92,#8B9A,#8C83,#9C9D,#9697
00B3	A	8B9A		
00B4	A	8C83		
00B5	A	9C9D		
00B6	A	9697		
00B7	A	9381	DFC	#9381,#8A90,#9587,#8698,#8E9E
00B8	A	8A90		
00B9	A	9587		
00BA	A	8698		
00BB	A	8E9E		
00BC	A	8F8D	DFC	#8F8D,#9299,#0003,#190E,#0901
00BD	A	9299		
00BE	A	0003		
00BF	A	190E		
00C0	A	0901	DFC	#0D1A,#1406,#0B0F,#121C,#0C18
00C1	A	0D1A		
00C2	A	1406		
00C3	A	0B0F		
00C4	A	121C		
00C5	A	0C18		
00C6	A	1617	DFC	#1617,#0A05,#1007,#1E13,#1D15
00C7	A	0A05		
00C8	A	1007		

MDDCOMP ASSEMBLY ASCII TO BAUDOT CONVERSION TABLE.....

162	00C9 A 1E13		
	00CA A 1D15		
	00CR A 118F	DFC	#118F,#8C92,#0083,#8C03,#190E
	00CC A 8C92		
	00CD A 0083		
	00CE A 8C03		
	00CF A 190E		
163	00D0 A 0901	DFC	#0901,#0D1A,#1406,#080F,#121C
	00D1 A 0D1A		
	00D2 A 1406		
	00D3 A 080F		
	00D4 A 121C		
164	00D5 A 0C18	DFC	#0C18,#1617,#0A05,#1007,#1E13
	00D6 A 1617		
	00D7 A 0A05		
	00D8 A 1007		
	00D9 A 1E13		
165	00DA A 1D15	DFC	#1D15,#118F,#8C92,#0085
	00DB A 118F		
	00DC A 8C92		
	00DD A 0085		

```

167 *****
168 * TWO POINTERS ARE USED TO ACCESS THE CHARACTER *****
169 * BUFFER:
170 *
171 * PNTIN INDICATES THE NEXT LOCATION A CHAR MAY BE
172 * ENTERED IN.
173 * PNTOT INDICATES THE NEXT LOCATION A CHAR WILL
174 * BE TAKEN FROM FOR TRANSMISSION TO THE ASR 28.
175 * IF THEY POINT TO THE SAME LOCATION WHILE FILLING
176 * THE BUFFER, IT IS FULL AND ADDITIONAL CHARACTERS
177 * IN THAT MESSAGE WILL BE LOST.
178 * IF THEY POINT TO THE SAVE LOCATION WHILE DUMPING
179 * THE BUFFER, IT IS EMPTY AND THE TRANSMISSION
180 * IS TERMINATED.
181 * CHRBF IS A 4096 CHARACTER BUFFER.
182 * FOR YOU WHO CAN'T DIVIDE BY 2, THAT'S A WHOPPING
183 * 2048 WORDS. THIS THING HAS TO BE BIG ENOUGH FOR
184 * SEVERAL OF THE MANY TABLES, ETC. THAT COULD BE
185 * GENERATED AT ONE TIME.
186 *
187 * PNTIN DFC 0
188 * PNTOT DFC 0
189 * CHRBF RES 2048,0
190 *****
191 * T2810 IS A STATUS FLAG FOR THE ASR 28 *****
192 * BIT 0=0 IDLE
193 * =1 OUTPUT UNDERWAY
194 * PIT 1=0 NO REPORT MESSAGES WAITING
195 * =1 REPORT MESSAGES WAITING
196 * BIT 2=0 NORMAL
197 * =1 FIRST CHAR COMING UP
198 * E=280 EQU $
199 * T2810 DFC 0
200 *****
201 * CONTR HAS THE CONTROL WORDS NECESSARY TO MAKE THE *****
202 * ASR 28 OUTPUT #6002,#A020,#0200 *****
203 * CONTR DFC
204 *****
205 * E=28F IS A REQUEST FLAG FOR EST28 ACTIONS..... *****
206 * BIT 0=0 THIS IS A INOPMAL MESSAGE *****
207 * =1 THIS INITIATES A REPORT SEQUENCE. *****
208 * F=28F DFC 0 *****
209 * SAVRS RES 7 *****
210 * SAVE REGISTERS *****
211 * SHOWS MODE *****
212 * FLAG DFC 0 *****
213 * END *****

```

TOTAL ASSEMBLY ERRORS = 0 SIZE = 2285


```

1      PGM          E$T35
2      INT          E:T35,E:T35,E:035,E:35I
3      INT          E:3IC,E:10F,E:35S,E:350
4      EXT          E:T12,E:T06,E:T15,E:SKD
5      *           *
6      *           *
7      *           *
8      *           *
9      *           *
10     *           *
11     *           *
12     *           *
13     *           *
14     *           *
15     *           *
16     *           *
17     *           *
18     *           *
19     *           *
20     *           *
21     *           *
22     *           *
23     *           *
24     *           *
25     *           *
26     *           *
27     *           *
28     *           *
29     0000         8200      E:T35 0BMM,0      E:T12      SET FLAG BIT FOR E:T35 IN E$SKD
30     0001         0001      X
31     0002         2500
32     0003         8600      E:T35 TBMB,0      T35IO,OUTPT  BRANCH IF OUTPUT UNDERWAY
33     0004         0122      R
34     0005         007C      R
35     0006         4C3A      IDA,3,#A      READ CHARACTER
36     0007         6238      ZAR,3,8      CLEAR PARITY BIT
37     0008         8610      TBMB,1      BRANCH IF PAST FIRST CHAR. NOW
38     0009         0122      R
39     000A         002D      R
40     000B         8620      TBMB,2      T35IO,RESPN
41     000C         0122      R
42     000D         0028      R
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** CHECK FOR VALID FIRST CHARACTER OF OPERATOR INITIATED REQUEST
** SUI,3 #2F #2F FIRST CHAR SHOULD BE I/I
TRRB,3,3 \$+4 GOODY IT IS!!!

42	0012	E700	BRU	VALID	
43	0013	001C	ZRR,3	E.IOF	ZERO E.IOF.....
44	0014	6C33	STM,3	E:035	LOG INVALID INITIAL CHAR
45	0015	E630	BLM,14	FOULI	
46	0016	R 0296	DFC	E:SKD	
47	0017	E7E0	BRU		
48	0018	R 00C8	VALID ZRP,2	E.31C	SET INPUT COUNTER TO 0
49	0019	R 0123	STM,2	E.IOF	RESET E.IOF.....
50	001A	E700	STM,2	E:035	SEND VALID INITIATE RESPONSE
51	001B	X 0004	BLM,14	GOODI	
52	001C	6C22	DFC	T3510	SET FLAG FOR MSG. PAST FIRST CHAR
53	001D	E620	OBMM,1	E:SKD	
54	001E	R 0183	BRU	T3510	2ST CHAR OF RESPONSE ANYTHING GOES
55	001F	E620	RESPN OBMM,1	E.31C	RESET OUTPUT COUNTER
56	0020	R 0296	ZRR,2	E.31C	INPUT CHARACTER COUNTER
57	0021	R 00C8	STM,2	E.35I	STORE INPUT CHAR AWAY
58	0022	R 00C8	LDI,5	#7F	IS CHAR RUB OUT (#10) ?
59	0023	R 0130	GONGI LDM,4	\$+4	BRU IF CHAR IS INDEED A RUB OUT
60	0024	R 8210	STM,3,4	RUBCT	
61	0025	R 0122	LDI,5	#18	IS CHAR CANCEL (#18)
62	0026	E700	SURB,5,3	\$+4	BRU IF CHAR IS INDEED A CANCEL
63	0027	X 0004	BRU	CANCL	
64	0028	R 8210	LDI,5	#0D	IS CHAR C/R (#0D) ?
65	0029	R 0122	SURB,5,3	\$+4	BRU IF CHAR IS INDEED A C/R
66	002A	6C22	BRU	DCNE	
67	002B	E620	ABR,4,15	604F	MOVE POINTER/COUNTER
68	002C	R 0183			
69	002D	F540			
70	002E	P 0183			
71	002F	E634			
72	0030	R 0133			
73	0031	ED50			
74	0032	A 007F			
75	0033	7953			
76	0034	R 0037			
77	0035	E700			
78	0036	R 0073			
79	0037	ED50			
80	0038	A 0018			
81	0039	7953			
82	003A	R 003D			
83	003B	E700			
84	003C	R 0069			
85	003D	ED50			
86	003E	A 000D			
87	003F	7953			
88	0040	R 0043			
89	0041	E700			
90	0042	R 004A			
91	0043	604F			

70	0044	E640	STM,4	E:3IC	
0045	R	0183			
71	0046	E940	SUI,4	80	
0047	A	0050			
72	0048	7D44	TRR,4,4	E:SKD	RETURN AND AWAIT NEXT CHARACTER
0049	X	0004			
73	004A	6C33	ZRR,3	E:IOF	RESET E:IOF.....
74	004B	E630	STM,3		
004C	R	0296			
75	004D	E7E0	BLM,14	E:035	OUTPUT CR/LF TO ASR 35
004E	R	00C8			
76	004F	R 0184	DFC	CRLF	
77	0050	E540	LDM,4	E:3IC	
0051	R	0183			
78	0052	E634	STM,3,4	E:35I	STORE MSG TERMINATOR
0053	R	0133			
79	0054	8110	ZBMM,1	T35IO	CLEAR MSG. UNDERWAY FLAG BIT
0055	R	0122			
80	0056	8620	TRMR,2	T35IO,RSPNS	IS MSG. REQUEST OR RESPONSE
0057	R	0122			
81	0058	R 005C	OBMM,0	E:T06REQUEST, SO SCHED. E:OMI
0059	R	8200			
82	005A	X 0002	HOP,\$+5		
005B	R	F705			
83	005C	8120	RSPNS ZBMM,2	T35IO	RESET REQUEST/RESPONSE FLAG BIT
005D	R	0122			
84	005E	8200	ORMM,0	E:T15	AND SCHEDULE BACKGROUND SYSTEM
005F	X	0003			
85	0060	8630	TBMR,3	T35IO,\$+5	IS OUTPUT WAITING? BRU IF IT IS.
0061	R	0122			
0062	R	0065			
86	0063	E700	BRU	E:SKD	NO, RETURN TO SYSTEM
0064	X	0004			
87	0065	E770	BLM,7	T0TST	BEGIN TTY OUTPUT
0066	R	0102			
88	0067	E700	BRU	E:SKD	AND THEN RETURN TO SYSTEM
0068	X	0004			
89	0069	8110	CANCL ZBMM,1	T35IO	CANCEL MSG, RESET MSG UNDERWAY FLAG
006A	R	0122			
90	006B	6C22	ZRR,2		
91	006C	E620	STM,2	E:IOF	CLEAR E:IOF....
006D	R	0296			
92	006E	E7E0	PLM,14	E:035	SEND CR,LF TO ASR 35
006F	R	00C8			
93	0070	R 0184	DFC	CRLF	
94	0071	E700	BRU	E:SKD	
0072	X	0004			
95	0073	7D44	RUBCT TRR,4,4	\$+4	
0074	R	0077			
96	0075	E700	BRU	CANCL	RUB OUT FIRST CHAR SAVE AS CANCEL

127	00A6	F140	SUM,4	PNTIN	BUFFER EMPTY IF PNTOT = PNTIN
	00A7	0286			
128	00A8	7D44	TRRB,4,4	\$+4	BRU IF BUFFER IS EMPTY
	00A9	00AC			
129	00AA	E700	BRU	EMPTY	
	00AB	00B3			
130	00AC	AE82	LBX,8,2		GET CHAR FROM MEMORY INTO R8
131	00AD	ED40	LDI,4	#A000	INITIALIZE OUTPUT OF NEXT CHAR
	00AE	A000			
132	00AF	404A	OCA,4,#A		
133	00B0	448A	ODA,8,#A		
134	00B1	E700	BRU	E:SKD	RETURN TO SENDER
	00B2	0004			
135	00B3	ED20	EMPTY LDI,2	#9800	
	00B4	9800			
136	00B5	C120	ETMM,2	T3510	RESET FLAG BITS 0, 3, 4
	00B6	0122			
137	00B7	8650	TBMB,5	T3510,\$+5	
	00B8	0122			
	00B9	00BC			
138	00BA	E700	BRU	EMPTY	
	00BB	00C0			
139	00BC	8200	ORMM,0	E.T15	SCHEDULE BACKGROUND SYSTEM
	00BD	0003			
140	00BE	8150	ZRMM,5	T3510	CLEAR RESCHEDULE FLAG BIT
	00BF	0122			
141	00C0	ED40	EMPTY LDI,4	#4400	
	00C1	4400			
142	00C2	404A	OCA,4,#A		RESET INTERRUPT FOR INPUT CHARS
143	00C3	ED40	LDI,4	#AC00	
	00C4	AC00			
144	00C5	404A	OCA,4,#A		
145	00C6	E700	BRU	E:SKD	RETURN TO SENDER
	00C7	0004			

```

147 *
148 * E:035 IS THE MEANS FOR SYSTEM AND APPLICATION PROGRAMS TO OUTPUT
149 * INFORMATION TO THE ASR 35 TELETYPEWRITER.
150 * CALLING SEQUENCE IS.....
151 *
152 *      BLM,14      E:035
153 *      ( ADDR OF FIRST WORD OF STUFF TO BE PRINTED ON ASR 35 )
154 *
155 * WHERE THE FIRST WORD AFTER THE MESSAGE IS #0000 TO TERMINATE OUTPUT
156 *
157 * E:035 SFM,1      SAVRS      SAVE REGISTERS
158 *      LDX,2,14      GET START ADDRESS
159 *      ABR,14,15      MOVE RETURN ADDRESS AROUND PARAM.
160 *      LDM,5          PNTIN      GET INPUT BUFFER POINTER
161 *
162 *      RLS,5,1        CONVERT COUNTER FROM BYTES TO WORDS
163 *      NXTWD LDX,3,2  GET NEXT WOTPUT WORD (2 CHARS.)
164 *      TRRB,3,3      $$$+4      DONE IF WORD IS #0000
165 *
166 *      BRU           FINI
167 *
168 *      STM,3,5      OUTBF      STORE IN OUTPUT BUFFER
169 *      ABMM,14      PNTIN      MOVE POINTER TWO BYTES IN MEMORY
170 *      ABR,5,15     PNTIN,$+5  MOVE POINTER ONE WORD IN R5
171 *      TBMB,6       IS POINTER OVER TOP OF BUFFER
172 *
173 *      BRU           $$$+5
174 *
175 *      ZRR,5        YES, RESET TO TOP
176 *      STM,5        IN MEMORY, AND R5
177 *
178 *      ABR,2,15     MOVE NEXT WORD POINTER
179 *      LDM,4        GET OUTPUT POINTER
180 *
181 *      RLS,4,1      LAST OUTPUT WORD MOVED TO BUFFER
182 *      SURB,4,5     NXTWD
183 *
184 *      FINI TBMB,1  E.IOF,$+4
185 *
186 *      HOP,$+3     NEXT INPUT WILL BE OPERATOR RESPONSE
187 *      OBMM,2      T35IO
188 *
189 *      TBMB,0      E.ICF,$+4
190 *
191 *      A510 R 00C8
192 *      0288 R 00C9
193 *      FD2E R 00CA
194 *      60EF R 00CB
195 *      E550 R 00CC
196 *      0286 R 00CD
197 *      2951 R 00CE
198 *      FD32 R 00CF
199 *      7D33 R 00D0
200 *      00D4 R 00D1
201 *      E700 R 00D2
202 *      00E7 R 00D3
203 *      E635 R 00D4
204 *      0186 R 00D5
205 *      80E0 R 00D6
206 *      0286 R 00D7
207 *      605F R 00D8
208 *      8660 R 00D9
209 *      00DA R 00DA
210 *      00DE R 00DB
211 *      F700 R 00DC
212 *      00E1 R 00DD
213 *      6C55 R 00DE
214 *      E650 R 00DF
215 *      0286 R 00E0
216 *      602F R 00E1
217 *      E540 R 00E2
218 *      0287 R 00E3
219 *      2941 R 00E4
220 *      7945 R 00E5
221 *      00CF R 00E6
222 *      8610 R 00E7
223 *      0296 R 00E8
224 *      00ER R 00E9
225 *      F703 R 00EA
226 *      8220 R 00EB
227 *      0122 R 00EC
228 *      8600 R 00ED
229 *      0296 R 00EE
230 *      00F1 R 00EF

```

E:035.....

MODCOMP ASSEMBLY

```

180 00F0 F703
181 00F1 8250
182 00F2 R 0122
183 00F3 8610
184 00F4 R 0122
185 00F5 R 00FE
186 00F6 8600
187 00F7 R 0122
188 00F8 R 00FB
189 00F9 F770
190 00FA R 0102
191 00FB A410
192 00FC R 0288
193 00FD FFOE
194 00FE 8230
195 00FF R 0122
196 0100 F700
197 0101 R 00FB

198 0102 8200
199 0103 R 0122
200 0104 8240
201 0105 R 0122
202 0106 8130
203 0107 R 0122
204 0108 ED20
205 0109 A 5000
206 010A 402A
207 010B ED20
208 010C A 4400
209 010D 402A
210 010E FF07

211 010F A510
212 0110 R 028F
213 0111 483A
214 0112 7637
215 0113 R 0120
216 0114 ED20
217 0115 A A000
218 0116 6C33
219 0117 402A
220 0118 443A

HDP,$+3
OBMM,5
TBMB,1
TBMB,0
BLM,7
RETRN LFM,1
BRX,14
UNDER OBMM,3
BRU
RETRN
TOTST
OBMM,4
ZBMM,3
LDI,2
OCA,2,#A
LDI,2
OCA,2,#A
BRX,7
E:35S SFM,1
ISA,3,#A
TBRB,3,7
LDI,#2
ZRP,3
OCA,2,#A
ODA,3,#A

RESCHEDULE BACKGROUND AFTER OUTPUT
IS INPUT UNDERWAY INHIBITING OUTPUT
IF OUTPUT IS UNDERWAY, NEW STUFF RDY
RESTORE REGISTERS
RETURN
SET OUTPUT WAIT
AND RETURN
SET FLAG TO SHOW OUTPUT
SET FLAG TO SHOW OUTPUT UNDERWAY
CLEAR OUTPUT WAITING FLAG BIT
ENABLE SI
TERMINATE
RETURN, WAIT FOR SI
SAVE REGISTERS
READ 35 STATUS
NO POWER TO ASR 35
XFER INIT.
OUTPUT NULL CHARACTER

```


MODCOMP ASSEMBLY

£:035.....

213	0119	8260			
	011A	0122	ORBMM,6	T3510	SET FOR ANOTHER NULL
214	011R	8270			
	011C	0122	ORBMM,7	T3510	FLAG FIPST CHAR. OUTPUT
215	011D	A410	LFM,1	E35SV	RESTORE REGISTERS
	011E	028F			
216	011F	2500			
217			CIP		
218			*****		
219			*		
220			* A POWER LOSS HAS BEEN DETECTED TO THE ASR 35.		
221			* FOR LACK OF ANYTHING BETTER TO DO, SET OFF		
222			* A SONALERT.		
223	0120	0100	PERR	RMI	
224	0121	2500	CIR		
225			*		

T35IO.....

* T35IO IS THE WORD USED TO FLAG VARIOUS ACTIVITIES AND CONDITIONS FOR OPERATIONS ON THE ASR 35. THE BITS ARE INTERPRETED AS FOLLOWS.

- * BIT 0=0 INPUT EXPECTED
- =1 OUTPUT UNDERWAY
- * 1=0 FIRST CHAR. OF INPUT MESSAGE
- =1 INPUT PAST FIRST CHARACTER
- * 2=0 INPUT IS OPERATOR INITIATED REQUEST TO SYSTEM
- =1 INPUT IS OPERATOR RESPONSE TO SYSTEM INQUIRY
- * 3=0 NO OUTPUT SCHEDULED DURING INPUT
- =1 OUTPUT SCHEDULED DURING INPUT OPERATION
- * 4=0 ASR 35 NOT BUSY WITH OUTPUT
- =1 ASR 35 OUTPUT UNDERWAY
- * 5=0 NO BACKGROUND RESCHEDULED AFTER OUTPUT
- =1 RESCHEDULE BACKGROUND SYSTEM AFTER OUTPUT COMPLETED
- * 6=0 NORMAL OUTPUT MODE
- =1 NEED ANOTHER NULL OUTPUT
- * 7=0 NORMAL RUN
- =1 OUTPUT FIRST CHARACTER

R	0122	E.350 EQU	\$	
	0123	T35IC DFC	0	
	0124	FOUL1 DFC	#0D0A,1	INVALID SYSTEM CALL 1,#0D0A,0
	0125	A 5641		
	0126	A 4C49		
	0127	A 4420		
	0128	A 5359		
	0129	A 5354		
	012A	A 454D		
	012B	A 2043		
	012C	A 414C		
	012D	A 4C20		
	012E	A 0D0A		
	012F	A 0000		
	0130	A 0D5C	#0D5C,#2020,#0000	
	0131	A 2020		
	0132	A 0000		
	0133	A 0000	80,0	INPUT BUFFER
	0183	A 0000	0	INPUT CHAR. COUNTER
	0184	A 0D0A	#0D0A,0	
	0185	A 0000		

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MODCOMP ASSEMBLY

T3510.....

PAGE 10

262

*

* OUTRF IS THE OUTPUT BUFFER, PACKED AT TWO CHARS PER WORD. BYTE
 * ADDRESSING IS USED TO RETRIEVE THE OUTPUT CHARACTERS IN TURN.
 * PNTIN IS THE BUFFER POINTER USED TO STORE DATA IN THE BUFFER.
 * PNTOT IS THE BUFFER POINTER USED TO REMOVE DATA FROM THE BUFFER.
 * BFLIM IS THE LIMIT FOR THE BUFFER POINTERS BEFORE RECYCLING.

264						
265						
266						
267						
268						
269						
270	0186			256		512 CHARACTER OUTPUT BUFFER (#100)
271	0286	A	0000	0		
272	0287	A	0000	0		
273	0288			7		
274	028F			7		
275						
276						

*

```

278 * E.IOF TRANSMITS DATA ABOUT CALL FROM B: IOR TO E:035.
279 * BIT 0=0 DO NOT RESCHEDULE BACKGROUND AFTER I/O DONE
280 * =1 RESCHEDULE BACKGROUND AFTER I/O DONE
281 * I=0 NO RESPONSE EXPECTED
282 * =1 OPERATOR RESPONSE IS EXPECTED
283 *
284 0296 A 0000 E.IOF DFC 0
285 0297 END

```

TOTAL ASSEMBLY ERRORS = 0 SIZE = 663

```

1      PGM          E$MAG
2      INT          E:$MAG,E:MTI,E:MGS
3      EXT          E:$KID,E:115,E:IOF,E:035,B:STT
4      EXT          E:111,F:MTP
5      CAT_NO.400000-018
6      * E$MAG IS THE MAGNETIC TAPE UNIT CONTROL PROGRAM.
7      * IT CONSISTS OF THREE PARTS:
8      * 1. E:MGS IS CALLED BY A BACKGROUND ROUTINE
9      * VIA THE B:IOR TO INIATE A DATA TRANSFER.
10     * 2. E:MTI RESPONDS TO THE SI FOLLOWING THE
11     * COMPLETION OF THE DATA TRANSFER AND SCHEDULES
12     * E:MAG IN THE SYSTEM SCHEDULER.
13     * 3. E:MAG CHECKS FOR ERRORS IN THE DATA TRANSFER
14     * AND INFORMS THE OPERATOR OF THEM. IF ALL
15     * WENT WELL, THE BACKGROUND PROCESSOR IS
16     * RESCHEDULES.

```

MODCOMP ASSEMBLY

MAG TAPE SI HANDLER.....

PAGE 2

18
19
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21

0000
0001 X
0002

8200
0006
2500

CIR

* A MAG TAPE SI CAUSES ENTRY TO E:MTI
E:MTI QBMM,0 E.T11 SCHEDULE E:MAG

```

23 *****
24 * AFTER EACH MAG TAPE SI, E:MAG IS ENTERED FROM THE
25 * SCHEDULER.
26 * E:MAG CHECKS THE STATUS OF THE MAG TAPE UNIT AND
27 * LOGS ANY ERRORS FOR THE OPERATOR.
28 E:MAG ISA,1,4
29 TBPB,1,0 NOERR
30
31 * WE HAVE A MAG TAPE ERROR, PINPOINT THE CUASE#
32 TBRB,1,2 PAPER PARITY ERROR
33 MAGO1 TBPB,1,6 DEFER TAPE DEFECT
34 TBRB,1,9 EOTER EOT FOUND
35 TBRB,1,3 INOPE INOPERATIVE
36 BRU NOERR
37 PARER TBPB,0,2 MAGO1
38 LDI,1 M02
39 HOP,EROUT M03
40 DEFER LDI,1
41 HOP,EROUT M04
42 EOTER LDI,1
43 HOP,EROUT M05
44 INOPE LDI,1
45 EROUT ZRR,2 E.IOF
46 STM,2 $+4
47 STM,1 E:O35
48 BLM,14
49 DFC
50 ALM,14 O
51 E:O35
52 DFC M06
53 ZRR,2
54 STM,2 B.STT
55 LDI,1 14080
56 STM,1 F.MTP
57
58
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62
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```

ABORT BACKGROUND

MODCOMP ASSEMBLY SI PROCESSOR.....

55	002F	E700	BRU	E::SKD
	0030	X 0001		
56	0031	8200	* LAST MAG TAPE REAC WAS GOOD, RESCHEDULE BACKGROUND.	
57	0032	X 0002	NOERR DBMM,0	E.T15
	0033	E700	BRU	E::SKD
58	0034	X 0001		

MODCOMP ASSEMBLY

MAG TAPE I/O INIATOR.....

005F R 004E

MAG TAPE MESSAGES.....

MDDCOMP ASSEMBLY

```

96
97
*****
#000A,IMAG TAPE NOT READY, I
M01 DFC
0D0A
A 0D41
A 4D41
A 4720
A 5441
A 5045
A 204E
A 4F54
A 2052
A 4541
A 4459
A 2C20
A 4341
A 4C4C
A 494E
A 4720
A 5441
A 534B
A 2041
A 424F
A 5254
A 4544
A 2E20
A 0D0A
A 0000
A 0D0A
A 4D41
A 4720
A 5441
A 5045
A 2050
A 4152
A 4954
A 5920
A 4552
A 524F
A 522C
A 2020
A 0000
A 0D0A
A 5441
A 5045
A 2044
A 4546
A 4543
A 5420
A 4445
A 5445
A 4354
A 4544

98
DFC
ICALLING TASK ABORTED. I,#0D0A
M02 DFC
M03 DFC
0
#0C0A,IMAG TAPE PARITY ERROR, I
0D0A
A 0D41
A 4720
A 5441
A 5045
A 2050
A 4152
A 4954
A 5920
A 4552
A 524F
A 522C
A 2020
A 0000
A 0D0A
A 5441
A 5045
A 2044
A 4546
A 4543
A 5420
A 4445
A 5445
A 4354
A 4544

99
100
DFC
DFC
M02 DFC
M03 DFC
0
#0D0A,IMAG TAPE DEFECT DETECTED, I
0D0A
A 0D41
A 4720
A 5441
A 5045
A 2050
A 4152
A 4954
A 5920
A 4552
A 524F
A 522C
A 2020
A 0000
A 0D0A
A 5441
A 5045
A 2044
A 4546
A 4543
A 5420
A 4445
A 5445
A 4354
A 4544

```



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3
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5
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10
11
12
13
14
15
16
17
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PGM
INT
EXT
EXT
EXT
EXT
EXT
EXT
EXT
EXT
EXT
CAT NO. 400100-002
*$RTA PERFORMS THE FOLLOWING FUNCTIONS
* 1. MONITORS CHANNEL AMPLITUDE TO DETERMINE NEED FOR GAIN CHANGES.
* 2. MONITORS CHANNEL PHASE TO DETERMINE NEED FOR TRACKING RATE CHANGES
* 3. MONITORS PERCENT BLANKED SIGNAL TO DETERMINE NEEDED LEVEL CHANGES
* 4. CONTROLS CHANNEL R.F. GAIN AND LOGS CHANGES
* 5. CONTROLS CHANNEL BLANKING LEVEL AND LOGS CHANGES
* 6. COMPUTES STANDARD DEVIATION OF INPUT PHASE SIGNAL
* 7. CONTROLS CHANNEL TRACK RATE BASED ON STD DEV AND LOGS CHANGES
* 8. PERFORMS DATA COMPRESSION
* 9. CONTROLS CHANNEL DOWN SEQUENCE.
* (BUSY LITTLE BASTARD ISN'T IT????)

*$RTA
U:RTA,U:CNI,U:SIG,U:TM3,U:TM4
U:CYC,U:WRK,U:RTY,U:IHB,U:AST
U:FAC,U:CCN,U:LST,U:LSP,E:DIF
E:SRT,U:LTF,U:DIU,E:MIT,U:BLL
U:TRK,U:LSQ,U:CPC,U:CMP,U:CTM
U:TRF,E:035,E:TM1,E:TM2,E:FCT
U:CST,E:LOG,U:GAN,U:AMP,E:FMF
U:CPP,E:IOF,M:STA

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23 *****
24 * AMPLITUDE MONITORING SECTION.....
25 U:RTA ZRR,6
26 RTA1 LDI,13 44
27 ZRR,5 MUBAV
28 STM,5
29 STM,5 SATTY
30 STM,5 DRPTY
31 RTA2 LDI,3 #3
32 MPR,3,6
33 TRR,2,3
34 ADR,2,13
35 E532 LDM,3,2
36 U.WRK
37 AMPMN
38 LOVAL
39 AMPMX
40 NXTSM
41 HIVTY
42 SATUR
43 NXTSM
44 ABMM,15
45 LDM,3
46 SUI,3
47 TRRB,3,3
48 LBR,3,15
49 STM,3
50 BRU
51 LOVAL ABMM,15,6
ADM,3
6C66
EDDO 0000
0001 A 002C
6C55
E650 R 0314
0005 R 0312
F650 R 0009
ED30 R 0008
A 0003
2036
6D23
682D
E532
0002 X
E130
02F2 R
7630
002C R
E130
02F3 R
7630 R
004A
80F6 R
0304
E130 R
02F5
7630 R
004A
80F0 R
0312
E530
0312 R
E930
0006 A
7D33
004A R
653F
E630 R
0314
E700
004A R
80F6
02F6 R
E030
02F4 R

```

R6 CHANNEL POINTER (C)
R13 SAMPLE POINTER (S)

RESET BAD AVERAGE FLAG
RESET SATURATION TALLY
RESET DROP TALLY

INDEX = S + C. (R2)
GET AMPLITUDE VALUE INTO R3

GO TALLY VAL IF ITS LESS THAN AMPMN

CHECK NEXT SAMPLE IF POINT IS O.K.
TALLY HI AMP VALUE ON THIS CHAN

GO ON IF POINT NOT SATURATED
TALLY SATURATED AMP VALUE
?
IS SATURATION TALLY FULL?

SET MUBAV=+1 TO FLAG SATURATED CHAN
GO TO NEXT SAMPLE
TALLY LO AMP VALUE ON THIS CHAN

AMPLITUDE MONITOR.....

MODCOMP ASSEMBLY

52	0030	7630	TBRB,3,0	\$+4	
	0031	0034	BRU	NXTSM	NOT DROP VALUE, CHECK NEXT SAMPLE
53	0032	E700			TALLY SIGNAL DROP-OUT VALUE
	0033	004A	ABM,15	DRPTY	
54	0034	80F0			
	0035	0313	LDM,3	DRPTY	
55	0036	E530			
	0037	0313	SUI,3	#3	IS DROP TALLY FULL
56	0038	F930			
	0039	0003	TRPB,3,3	\$+7	
57	003A	7D33			
	003B	0041	GMR,3,15		SET MURAV=-1 TO FLAG SIGNAL LOST
58	003C	673F	STM,3	MUBAV	
59	003D	F630			
	003E	0314	PRU	NXTSM	
60	003F	E700			
	0040	004A	TRR,3,2		SUBSTITUTE PREVIOUS PHASE VALUE FOR CURRENT
61	0041	6D32	ABR,3,15		
62	0042	603F	TRR,4,3	44	
63	0043	6D43	SUI,4		
64	0044	E940			
	0045	002C	LDM,5,4	U.WRK	GET PREVIOUS PHASE VALUE
65	0046	E554			
	0047	0002	STM,5,3	U.WFK	STORE FOR CURRENT ONE
66	0048	E653			
	0049	0002	NXTSM ADI,13	44	MOVE POINTER TO NEXT ROW (NEXT SAMP)~
67	004A	F8D0			
	004B	002C	LDI,3	484	
68	004C	ED30			
	004D	01E4	SURR,3,13	RTA2	
69	004E	793D			
	004F	000A	LDM,3,2	U.WPK	RETAIN CURRENT AMP VALUE
70	0050	E532			
	0051	0002	STM,3,6	U.AMP	
71	0052	E636			
	0053	001D			

* PHASE MONITOR SECTION.....
LDM,3 MURAV

73	0054	R	E530	
74	0055	R	0314	
75	0056	R	7D33	
76	0057	R	020A	
77	0058	X	E536	
78	0059	X	001A	
79	005A	R	713F	
80	005B	R	005E	
81	005C	R	E700	
82	005D	R	007D	
83	005E	R	8606	
84	005F	X	0004	
85	0060	R	00DF	
86	0061	X	84F6	
87	0062	X	0003	
88	0063	R	007D	
89	0064		0956	
90	0065	X	E740	
91	0066	X	001B	
92	0067	X	E556	
93	0068	X	001A	
94	0069	X	654F	
95	006A	X	E646	
96	006B	X	001A	
97	006C	R	765D	
98	006D	R	0070	
99	006E	R	E700	
00	006F	R	007D	
01	0070		6C44	
02	0071	X	E646	
03	0072	X	0015	
04	0073	R	E646	
05	0074	R	0315	
06	0075	R	E646	
07	0076	R	0323	
08	0077	X	E646	
09	0078	X	0012	
10	0079	X	E550	
11	007A	X	000C	
12	007B	X	E656	
13	007C	X	0006	
14	007D	X	EDF0	
15	007E	X	0002	
16	007F	X	E8F0	
17	0080	A	0029	
18	0081	A	EDE0	
19	0082	A	002D	

IF MURAV IS NOT 0, BEGIN DOWN SEQ.

IS CHANNEL STATUS UP?

YES GO TO NORMAL PROCESSING

TRRB,3,3 DNSEQ

LDM,3,6 U.CST

SBPB,3,15 \$+4

BRU MFAZI

TBMB,0,6 U.IHB,NXTCH

ABMB,15,6 U.RTY,MFAZI

MRL,5,6 E:LOG

BLM,4 U.CST

LBR,4,15 U.CST

STM,4,6 \$+4

TBRB,5,13 MFAZI

ZRR,4 U.TRF

STM,4,6 UBADT

STM,4,6 UBADS

STM,4,6 U.CPC

LDM,5 U.LTF

STM,5,6 U.FAC

MFAZI LDI,15 U.WRK

ADI,15 41

LDI,14 45

R15 POINTS TO FIRST TIME ENTRY
(U.WRK+41)

R14 SAMPLE POINTER

PHASE MONITOR.....

MODCOMP ASSEMBLY

100	0083	6CAA	ZRR,10	R10,R11 SERVE AS UACC
101	0084	6C9B	ZRP,11	
102	0085	6CCC	ZRP,12	R12,R13 SERVE AS U2ACC
103	0086	6CDD	ZRP,13	
104	0087	ED30	* BEGIN LOOP BY COMPUTING LOCATION OF FAZ POINT IN R2	
105	0088	A 0003	MFAZ3 LDI,3	GET CHANNEL
106	0089	2036	MPR,3,6	
107	008A	6D23	TRR,2,3	
108	008B	682E	ADR,2,14	GET SAMPLE POINTER, SUM INTO R2
109	008C	F552	LDM,5,2	GET PHASE POINT
110	008E	7650	TBRB,5,0	U.WRK
111	008F	R 0091		\$+3
112	0090	F703	HOP,\$+3	
113	0091	6250	ZRR,5,0	CLEAR CYCLE SHIFT BIT
114	0092	6380	GR,8,0	SET FLAG BIT IN R8
115	0093	E056	ADM,5,6	ADD CYCLE SHIFT COMPENSATION
116	0094	X 0007		U.CCN
117	0095	6D45	TRP,4,5	
118	0096	E146	SUM,4,6	IS POINT LOWER THAN WINMN?
119	0097	R 03A1		WINMN
120	0098	7640	TBRB,4,0	FINDN
121	0099	R 009F		
122	009A	6D45	TRP,4,5	IS POINT GREATER THAN WINMX?
123	009B	E146	SUM,4,6	WINMX
124	009C	R 0393		
125	009D	7640	TBRB,4,0	MFAZ2
126	009E	R 00A3		
127	009F	E740	FINDN BLM,4	COMPR
128	00A0	R 0290		
129	00A1	E740	BLM,4	NEWIN
130	00A2	R 02AF		
131	00A3	7650	MFAZ2 TBPB,5,0	\$+4
132	00A4	R 00A7		
133	00A5	6C44	ZRR,4	POSITIVE VALUE
134	00A6	F702	HOP,\$+2	NEGATIVE VALUE
135	00A7	674F	GMR,4,15	DOUBLE PRECISION:VACC+CUR.POINT
136	00A8	22A4	DAR,10,4	(PHASE - XBAR)
137	00A9	E156	SUM,5,6	XBAR
138	00AA	R 0385		
139	00AB	2045	MPP,4,5	SQUARED
140	00AC	22C4	DAR,12,4	DBL.PREC:U2ACC=U2ACC+(CURPOINT)**2
141	00AD	7680	TBPB,8,0	\$+3
142	00AE	R 0080		
143	00AF	F709	HOP,\$+9	CLEAR FLAG BIT
144	00B0	6280	ZRP,8,0	
145	00B1	E546	LDM,4,6	U.CYC
146	00B2	X 0001		
147	00B3	C046	ADMM,4,6	U.CCN=U.CCN+U.CYC;UPDATE CYCLE COUNT~

MODCOMP ASSEMBLY PHASE MONITOR.....

Address	OpCode	OpType	OpData	Comment
136	00B4	X	0007	
137	00B5		6C44	
138	00B6	X	E646	
139	00B7	X	0001	
140	00B8	A	ERE0	
141	00B9	A	002C	
142	00BA	A	01E5	
143	00BC	R	793E	
144	00BD	R	0087	
145	00BE		6D16	
146	00BF		2F11	
147	00C0	R	F541	
148	00C1	R	0331	
149	00C2	R	E551	
150	00C3	R	0332	
151	00C4	R	224A	
152	00C5	R	E641	
153	00C6	R	0331	
154	00C7	R	E651	
155	00C8	R	0332	
156	00C9	R	E541	
157	00CA	R	034D	
158	00CB	R	E551	
159	00CC	R	034E	
160	00CD	R	224C	
161	00CE	R	E641	
162	00CF	R	034D	
163	00D0	R	F651	
164	00D1	R	034E	
165	00D2	A	ED40	
166	00D3	A	000A	
167	00D4	X	21A4	
168	00D5	X	F510	
169	00D6	X	0009	
170	00D7	X	6816	
171	00D8	X	E6B1	
172	00D9	X	0008	
			ZRR,4	
			STM,4,6	
			ADI,14	
			LDI,3	
			SURB,3,14	
			TRR,1,6	
			LAS,1,1	
			LDM,4,1	
			LDM,5,1	
			DAR,4,10	
			STM,4,1	
			STM,5,1	
			LDM,4,1	
			LDM,5,1	
			DAR,4,12	
			STM,4,1	
			STM,5,1	
			LDI,4	
			DVR,10,4	
			LDM,1	
			ADR,1,6	
			STM,11,1	
			U.CYC	CLEAR CYCLE SHIFT FLAG FOR CHAN
			44	
			485	
			MFAZ3	LOOP THRU 10 SAMPLES
			UACCI	GET DOUBLE PRECISION DATA POINTER
			UACCI+1	DBL.PREC.:UACCI=UACCI+UACC
			UACCI+1	
			UACCI	
			UACCI+1	
			UZACC	DBL.PREC:UZACC=UZACC+CUR.UZACC
			UZACC+1	
			UZACC	
			UZACC+1	
			10	
			U.LSP	GET 10 SECOND AVERAGE PHASE FIND STORAGE POINTER
			U.LST	ADD ROW POINTER AND CHAN POINTER STORE AVER

159	00DA	60ZF	ABR,2,15			MOVE DATA POINTER TO BLANKING VALUE
160	00DB	F542	LDM,4,2	U.WRK		GET DATA BLANK VALUE
161	00DC	0002				
162	00DD	C046	ADMM,4,6	UBACC		UBACC=UBACC+CUR,VALUE
163	00DE	03RF				
164	00DF	606F	NXTCH ABR,6,15			MOVE POINTER TO NEXT CHANNEL
165	00E0	ED40	LDI,4	14		
166	00E1	000E				
167	00E2	7946	SURB,4,6	RTAI		LOOP THROUGH 14 CHANNELS
168	00E3	0001				
169	00E4	ED10	LDI,1	14		
170	00E5	000E				
171	00E6	C010	ADMM,1	U.LSP		MOVE POINTER TO NEXT ROW
172	00E7	0009				
173	00E8	ED10	LDI,1	140		
174	00E9	008C				
175	00FA	E110	SUM,1	U.LSP		SEE IF TIME TO RESET POINTER
176	00FB	0009				
177	00FC	7D11	TRRB,1,1	\$+4		NO, CONTINUE.
178	00FD	00F0				
179	00FE	F610	STM,1	U.LSP		RESET TO TOP OF BUFFER
180	00FF	0009				

* GAIN AND BLANKING LEVEL CONTROL SECTIONS.....

* ENTRY TO THIS SECTION IS CONTROLLED BY THE GAIN CLOCK, WHICH PERMITS EXECUTION EVERY TWO MINUTES.

* GAIN ADJUSTMENTS ARE MADE BASED ON THE NUMBER OF HIGH AND LOW VALUES TALLIED IN THE MONITOR SECTION.

* BLANKING LEVEL ADJUSTMENTS ARE MADE BASED ON THE AVERAGE BLANKING LEVEL IN UBACC RELATIVE TO MAXIMUM AND MINIMUM PERMITTED VALUES.

* ABMR,15 GCLCK,CTCLK

180	00F0	84F0	ZRR,6			RESET CHANNEL POINTER TO FIRST CHAN
181	00F1	03D0	RTLOP RLM,15	CANI		SEE IF PARM CHANGE O.K.
182	00F2	016C				
183	00F3	6C66				
184	00F4	E7F0				
185	00F5	0268	BRU	RSTAL		NO, GO ON AROUND CHANGE.
186	00F6	E700				
187	00F7	0152				

* O.K. WE'VE ESTABLISHED THAT IT'S ALL RIGHT TO ADJUST THE GAIN OR BLANKING LEVEL AS NEEDED

188	00F8	EDA0	LDI,10	90		LIMITER FOR LOW VALUE TALLY
189	00F9	005A				
190	00FA	E1A6	SUM,10,6	LOVTV		
191	00FB	02F6				
192	00FC	76A0	TRRB,10,0	LOGAN		BRU IF GAIN IS TOO LOW
193	00FD	0106				
194	00FE	EDA0	LDI,10	90		
195	00FF	005A				
196	0100	E1A6	SUM,10,6	HIVTV		
197	0101	0304				

BLANKING LEVEL CONTROL.....

MODCOMP ASSEMBLY

211	0127	E5A6	CKBL	LDM,10,6	UBACC					
212	0128	R 038F	SUM,10		BLKMN				CHECK AGAINST MIN. VALUE.....	
213	0129	R 03CE	TBR,10,0		LORLK				IF R10<0, THEN LEVEL IS TOO LOW	
214	012A	R 76A0	SUM,10		BLKMX				CHECK AGAINST MAX. VALUE.....	
215	012B	R 013F	TBR,10,0		RSTAL				IF R10<0, THEN NO CHANGE NEEDED	
216	012C	R 03CF	LDM,10,6		U.BLL				CHECK TO SEE IF AT MAX SETTING	
217	012D	R 76A0	SUI,10		63					
218	012E	R 0152	TRR,10,10		\$+4					
219	012F	R 0152	BRU		RSTAL				BLANK LEVEL AT MAX SETTING	
220	0130	R E5A6	ABMM,15,6		U.BLL				INCREMENT SETTING	
221	0131	X 000F	LDM,10,6		U.BLL					
222	0132	R 0148	BRU		RCHNG					
223	0133	R E5A6	LORLK	LDM,10,6	U.BLL				CHECK TO SEE IF AT MIN SETTING	
224	0134	X 000F	TRRR,10,10		\$+4				?	
225	0135	R 0145	BRU		RSTAL				BLANK LEVEL AT MIN SETTING	
226	0136	R 0152	SBR,10,15		U.BLL					
227	0137	R 61AF	STM,10,6							
228	0138	X 000F	RCHNG	STM,10	RPARM				PASS NEW BLANK LEVEL TO E\$LOG	
229	0139	R E6A0	ORMM,0		CFLAG				SET CHANGE MADE FLAG	
230	0140	R 03CD	MBL,5,6						SFT UP R5 FOR E\$LOG CALL	
231	0141	R 0956	ADI,5		#05				MSG NO. FOR IBCIMESSAGE	
232	0142	A 0005	BLM,4		E:LOG					
233	0143	X 001B	BPARM	DFC	0				NEW BLANK LEVEL	
234	0144	A 0000	RSTAL	ZRR,4	HIVTY				RESET COUNTERS	
235	0145	R 6C44	STM,4,6		LOVTY				HI VALUE GAIN COUNTER	
236	0146	R E646	STM,4,6						LO VALUE GAIN COUNTER	
237	0147	R 02F6	STM,4,6		UBACC				BLANK LEVEL AVERAGER	
238	0148	R E646								
239	0149	R 038F								
240	0150	R 038F								
241	0151	R 038F								
242	0152	R 038F								
243	0153	R 038F								
244	0154	R 038F								
245	0155	R 038F								
246	0156	R 038F								
247	0157	R 038F								
248	0158	R 038F								

249 *****
 250 * TRACK RATE CONTROL SECTION.....
 251 * ENTRY TO THIS SECTION IS CONTROLLED BY THE TRACK CLOCK, WHICH
 252 * PERMITS EXECUTION EVERY 1 1/2 MINUTES.
 253 * TRACK RATE ADJUSTMENTS ARE MADE BASED ON THE STANDARD DEVIATION OF
 254 * THE PHASE INPUT, SO NATURALLY WE HAVE TO COMPUTE IT FIRST.
 255 CTCLK ABMB,15 TCCLK,=INI

256 ZRR,6 RESET CHANNEL POINTER TO FIRST CHAN
 257 * DO STANDARD DEVIATION COMPUTATION.....
 258 STDEV TRP,5,6 R6 HAS CHANNEL POINTER
 259 LLS,5,1 R5 HAS D.P. DATA CHANNEL POINTER
 260 * SIGMA=SQRT(U2ACC/89)
 261 LDM,2,5 U2ACC R2,R3 HAS U2ACC

262 LDM,3,5 U2ACC+1 R10=8900
 263 LDI,10 8900 R13 HAS REMAINDER
 264 DVR,2,10 R4=100
 265 TRR,13,2 100 R7=89
 266 LDI,4 100 R12,R13=REMAINDER

267 LDI,7 89 R13=REM*100/89
 268 ZRR,12 R2,R3=U2ACC/8900*100
 269 DVR,12,7 MPR,2,4 P12,R13=U2ACC(D.P.)/89
 270 ZRR,12 TAKE SQUARE ROOT
 271 DAR,12,2 E:SFT SAVE LAST SIGMA
 272 BLM,14 SIGMA STORE SIGMA AWAY
 273 E7F0

274 LDM,2,6 SIGMA
 275 STM,2,6 LSIGM
 276 STM,13,6 SIGMA
 277 * NOW UPDATE XBAR:
 278 XBAR=(UACCI/90)
 279 LDI,7 90

280 LDM,12,5 UACCI
 281 LDM,13,5 UACCI+1
 282 DVR,12,7
 283 SUI,12 45 R12=UACCI/90

016C R 84F0
 016D R 0301
 016E R 01FB
 016F 6C66
 0170 6D56
 0171 2D51
 0172 E525
 0173 R 034D
 0174 E535
 0175 R 034E
 0176 EDA0
 0177 A 22C4
 0178 212A
 0179 6DD2
 017A ED40
 017B A 0064
 017C ED70
 017D A 0059
 017E 6CCC
 017F 21C7
 0180 6CCC
 0181 2024
 0182 22C2
 0183 E7F0
 0184 X 000B
 0185 E526
 0186 R 0369
 0187 F626
 0188 R 0377
 0189 F606
 018A R 0369
 018R ED70
 018C A 005A
 018D E5C5
 018E R 0331
 018F E5D5
 0190 R 0332
 0191 21C7
 0192 E9C0
 0193 A 002D

284	0194	76C0	TBRB,12,0	\$+3
285	0195	0197	ABR,13,15	
286	0196	60DF	STM,13,6	XBAR
287	0197	E6D6		
288	0198	0385		
289			* CLEAR ACCUMULATORS,	
290			* UACCI=0	
291			* U2ACC=0	
292	0199	6C44	ZRR,4	
293	019A	E645	STM,4,5	UACCI
294	019B	0331	STM,4,5	UACCI+1
295	019C	E645	STM,4,5	U2ACC
296	019D	0332	STM,4,5	U2ACC+1
297	019E	E645		
298	019F	034D		
299	01A0	E645		
300	01A1	034E		
301	01A2	E536	* DO NOT CHANGE DELTA IF PROCESSING A TRIGGER	
302	01A3	0005	LDM,3,6	U.AST
303	01A4	613E	SBR,3,14	
304	01A5	7630	TBRB,3,0	\$+4
305	01A6	01A9	BRU	ADJTR
306	01A7	E700		
307	01A8	01BE		
308	01A9	E506	* THE DATA COMPRESSION IS 2*DELTA WIDE, WHERE:	
309	01AA	0377	DELTA=LSIG**K	
310	01AB	E530	LDM,13,6	LSIGM
311	01AC	038E	LDM,3	K+1
312	01AD	202D	MPP,2,13	R2=FRACTION PART
313	01AE	2E21	LAD,2,1	
314	01AF	E530	LDM,3	K
315	01B0	038D	MPR,12,3	R13=INTEGER PART
316	01B1	20C3	ADR,13,2	
317	01B2	68D2	STM,13,6	DELTA
318	01B3	E6D6		
319	01B4	03AF		
320	01B5	61DB	* SET MINIMUM WINDOW WIDTH OF 2 MICROSECONDS,	
321	01B6	76D0	* THAT IS, A MINIMUM DELTA = 1 MICROSECOND.	
322	01B7	01B9	TBRB,13,0	\$+3
323	01B8	F704	HOP,\$+4	HOP IF VAL 0.K..
324	01B9	650B	LBR,13,11	SET TO 1 MICROSECOND
325	01BA	E6D6	STM,13,6	DELTA
326	01BB	03AF	BLM,4	NEWIN
327	01BC	E740		
328	01BD	02AF		

MODCOMP ASSEMBLY STANDARD DEVIATION COMPUTATION.....

MODCOMP ASSEMBLY		TRACKING RATE CONTROL.....	
346	01EE A	0000	
347	01FF	6D36	
348	01F0	E750	
	01F1 X	000E	
349	01F2	606F	
350	01F3	ED50	
	01F4 A	000F	
351	01F5	7956	
	01F6 R	0170	
352	01F7	ED50	
	01F8 A	FFF7	
353	01F9	E650	
	01FA R	03D1	
354			
355			
356	01FB	ED50	
	01FC X	0002	
357	01FD	6D65	
358	01FE	E860	
	01FF A	01F8	
359	0200	ED70	
	0201 A	FFD4	
360	0202	FD46	
361	0203	FE45	
362	0204	605F	
363	0205	606F	
364	0206	707F	
	0207 R	0202	
365	0208	E700	
	0209 X	0011	
	TPARM	DFC	
		TRR,3,6	0
		BLM,5	E:MIT
	NEXTC	ABR,6,15	14
		LDI,5	
		SURB,5,6	STDEV
		LDI,5	-9
		STM,5	TCLCK
	* NOW MOVE LAST ROW OF U.WRK (MOST CURRENT DATA) TO THE FIRST		
	* TO SET UP FOR NEXT TEN SECONDS ENTRY TO U\$RTA.		
	FINI	LDI,5	U.WRK
		TRR,6,5	440
		ADI,6	
		LDI,7	-44
		LDX,4,6	
		STX,4,5	
		ABR,5,15	
		ABR,6,15	
		ABRB,7,15	\$-4
	BRU		U:LSQ
			LOOP THRU 44 POINTS
			GO ON TO LEAST SQUARES PGM
			R6=U.WRK+440
			MOVE TO NEXT CHANNEL
			SET CHANNEL IN R3
			GO MAKE CHANGE
			LOOP THRU 14 CHANNELS, THEN
			RESET TR CLOCK TO -9

367 * WHEN MUBAV IS SET OTHER THAN 0, INDICATING IBADI DATA,
 368 * THE CHANNEL IS PUT INTO A DOWN SEQUENCE WHICH EVENTUALLY
 369 * PUTS THE CHANNEL INTO A DOWN STATE IF THE LOW QUALITY
 370 * DATA CONTINUES.
 371 * ENTER WITH MUBAV IN R3, CHANNEL IN R6
 372 DNSEQ TBMB,0,6 U.IHB,STREC BRU IF PROC INHIBITED

020A B606
 020B X 0004
 020C R 0215
 020D E556
 020E X 001A
 020F E70D
 0210 R 0210

LDM,5,6 U.CST
 BRU*,5 JMPTB-1

375 * THE NEXT STEP DEPENDS ON WHAT THE CURRENT STATUS IS...
 376 JMPTR DFC ISUP U.CST=1, UP
 377 0211 R 021B DFC ISCRN U.CST=2, COAST,NO-REPORT
 378 0212 R 0224 DFC ISCR U.CST=3, COAST,REPORT
 379 0213 R 0250 DFC STREC U.CST=4, DOWN
 380 0214 R 0215 DFC -IB
 380 0215 ED50 STREC LDI,5

381 0216 A FFEE STM,5,6 U.RTY RESET RECOVERY TALLY
 381 0217 E656 STM,5,6 U.RTY RESET RECOVERY TALLY
 382 0218 X 0003 BRU NXTCH AND EXIT
 382 0219 E700 BRU NXTCH AND EXIT

383 021A R 00DF ISUP ABMM,15,6 U.CST SET STATE = COAST NO REPORT
 383 021B 80F6 ISUP ABMM,15,6 U.CST SET STATE = COAST NO REPORT
 384 021C X 001A LBR,5,15 URADT SET BAD TALLY = 1
 384 021D 655F STM,5,6 URADT SET BAD TALLY = 1
 385 021E E656 STM,5,6 URADT SET BAD TALLY = 1
 385 021F R 0315 STM,3,6 URADS ADD MUBAV TO BAD SUM
 386 0220 E636 STM,3,6 URADS ADD MUBAV TO BAD SUM
 386 0221 R 0323 BRU STREC ADD MUBAV TO BAD SUM
 387 0222 E700 BRU STREC ADD MUBAV TO BAD SUM

388 0223 R 0215 ISCRN ABMM,15,6 UBACT INCRM BAD TALLY
 388 0224 80F6 ISCRN ABMM,15,6 UBACT INCRM BAD TALLY
 389 0225 R 0315 ADM,3,6 UBADS ADD MUBAV TO BAD SUM
 389 0226 C036 ADM,3,6 UBADS ADD MUBAV TO BAD SUM
 390 0227 F 0323 LDM,5,6 UBADT ADD MUBAV TO BAD SUM
 390 0228 E556 LDM,5,6 UBADT ADD MUBAV TO BAD SUM
 391 0229 R 0315 SUI,5 3

392 022A F950 SUI,5 3
 392 022B A 0003 TRPB,5,5 STREC CHECK OF TALLY=3
 392 022C R 0215 TRPB,5,5 STREC CHECK OF TALLY=3
 393 022D R 0215 ABMM,15,6 U.CST YES, UP STATE TO COAST REPORT
 393 022E 80F6 ABMM,15,6 U.CST YES, UP STATE TO COAST REPORT
 394 022F X 001A LDM,5,6 UBADS

394 0230 E556 LDM,5,6 UBADS
 394 0231 R 0323 SUI,5 3
 395 0232 E950 TRRB,5,5 \$+4
 395 0233 A 0003 TRRB,5,5 \$+4
 396 0234 7D55 TRRB,5,5 \$+4
 396 0235 R 0238 TRRB,5,5 \$+4

397	0236	E700	BRU	CMSMSG	MSG NO. FOR ICNI
	0237	R 0248			
398	0238	E556	LDM,5,6	UBADS	SET UP CHAN FOR LOG MSG.
	0239	R 0323	ADI,5	3	ADD IN MSG NO.
399	023A	E850	TRRB,5,5	\$+4	LOG COASTING AND REASON
	023B	A 0003	BRU	CFMSG	
400	023C	7D55	LDI,7	15	
	023D	R 0240			
401	023E	E700	LOGCR MBL,5,6		
	023F	R 024C	ADR,5,7		
402	0240	E700	BLM,4	E:LCG	
	0241	A 000F	BRU	STREC	
403	0242	0956	CMSMSG LDI,7	16	MSG NO FOR ICSI
404	0243	6857	BRU	LOGCR	
405	0244	E740	CFMSG LDI,7	14	MSG NO FOR CF
	0245	X 001B	BRU	LOGCR	
406	0246	E700	ISCR ABMM,15,6		INCREM BAD TALLY
	0247	R 0215	LDM,5,6	UBADT	
407	0248	E700	SUI,5	30	IS BAD TALLY = 30?
	0249	A 0010	TRPB,5,5	STREC	
408	024A	E700	ABMM,15,6	U.CST	YES, GO DOWN
	024B	R 0242	MBL,5,6		MSG NO FOR IDI MESSAGE
409	024C	E700	ADI,5	17	LOG DOWN CHANNEL
	024D	A 000E	BLM,4	E:LOG	RESET
410	024E	E700	ZRR,4	URADT	BAD TALLY
	024F	R 0242	STM,4,6	UBADS	BAD SUM
411	0250	80F6	STM,4,6	U.CCN	CYCLE COUNTER
	0251	R 0315	BRU	STREC	
412	0252	E556			
	0253	R 0315			
413	0254	E950			
	0255	A 001E			
414	0256	7D55			
	0257	R 0215			
415	0258	80F6			
	0259	X 001A			
416	025A	0956			
417	025R	E850			
	025C	A 0011			
418	025D	E740			
	025E	X 001R			
419	025F	6C44			
420	0260	E646			
	0261	R 0315			
421	0262	E646			
	0263	R 0323			
422	0264	E646			
	0265	X 0007			
423	0266	E700			
	0267	R 0215			

MODCOMP ASSEMBLY

CHANNEL DOWN SEQUENCER.....

PAGE 18

424

*****END MAINLINE OF U\$RTA*****

* CANI IS A SUBROUTINE TO DETERMINE IF I CAN MAKE A PARAMETER
 * ADJUSTMENT. ADJUSTMENTS ARE NOT PERMITTED IF VAVEFORM ANALYSIS
 * INDICATES A REPORTABLE SIGNAL IS PROBABLY IN PROGRESS. THE
 * FOLLOWING CRITERIA ARE USED TO DETERMINE IF THERE MIGHT BE A
 * SIGNAL UNDERWAY:
 * 1. U:AST, THE ALARM STATUS, SHOWS A PEAK HAS BEEN FOUND (>4)
 * 2. THE ELAPSED TIME FROM ONSET IS LONGER THAN 50 SECONDS.
 * CALLING SEQUENCE:

* BLM,15 CANI (R6=CHAN)
 * (RETURN IF NOT PERMITTED)
 * (RETURN IF CHANGES O.K.)
 * CANI SFM,12 THOLD SAVE R4

LDI,12 #3
 SUM,12,6 U:AST
 TBRB,12,0 CANRT BRU IF DOWN OR COAST REPORTED
 TBMB,15,6 M:STA,CANRT NO CHANGE IN MANUAL

CANI1 LDI,12 #4
 SUM,12,6 U:AST
 TBRB,12,0 CANRT BRU IF SIGNAL PAST PEAK
 BLM,14 E:DI# GET TIME SINCE ONSET
 DFC U:CTM LOC. OF CURRENT TIME
 HDP,5,7
 LDI,14 50

SUP,14,13
 TBRB,14,0 CANRT BRU IF > 50 SEC PAST PEAK
 * IF YOU MAKE IT THIS FAR, YOU MAY MAKE A PARM ADJUSTMENT.
 ABMM,14 THOLD+3 ADJ. RETURN ADDRESS

CANRT LFM,12 THOLD
 BRX,15
 THOLD RES 4 RETURN
 U:CNI SFM,12 THOLD
 BRU CANI1

426		A5C0	R	0268
427		0288		
428		EDC0		
429		026A		
430		A 0003		
431		E1C6		
432		026C		
433		X 001A		
434		026D		
435		026E		
436		R 0285		
437		0270		
438		0271		
439		X 0021		
440		0272		
441		R 0285		
442		EDC0		
443		A 0004		
444		0273		
445		E1C6		
446		0274		
447		X 0005		
448		0275		
449		0276		
450		0277		
451		R 0285		
452		E7E0		
453		0278		
454		X 000A		
455		027A		
456		X 0014		
457		F707		
458		027C		
459		6600		
459		EDE0		
459		027E		
459		A 0032		
459		027F		
459		0280		
459		69ED		
459		76E0		
459		R 0285		
459		0282		
459		80E0		
459		0283		
459		F 028R		
459		0284		
459		A4C0		
459		R 0288		
459		FF0F		
459		0286		
459		0287		
459		0288		
459		A5C0		
459		R 0288		
459		E700		
459		028E		
459		R 0273		

MODCOMP ASSEMBLY

CANI AND U:CANI SUBROUTINES.....

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461 *****
462 * COMPR ESTABLISHES A NEW COMPRESSED POINT, IT:
463 *
464 * 1. SETS A NEW WINDOW CENTER
465 * 2. SAVES POINT VALUE AND TIME
466 * NEWIN ESTABLISHES A NEW DATA WINDOW WHEN:
467 * 1. A NEW CENTER HAS BEEN FOUND
468 * 2. A NEW DELTA HAS BEEN COMPUTED
469 * CALLING SEQUENCE.....
470 *   BLM,4   COMPR
471 *   (CUR. VAL IN R5, TIME IN LOC. AT R14,R15)
472 *   BLM,4   NEWIN
473 *   TREGS   SAVE REGISTERS
474 *   STM,5,6 CENTR   SET NEW COUNTER
475 *   LDI,3   3
476 *   MPM,2,6 U.CPC   ENTRY COUNTER
477 *   ADM,3,6 U.CPP   BUFFER POINTER
478 *   STX,5,3 STORE DATA IN BUFFER
479 *   ABR,3,15 STORE LOC. FOR TIME
480 *   TRP,7,14
481 *   ADR,7,15 TIME POINTER
482 *   LDX,5,7 GET TIME
483 *   STX,5,3 STORE IN BUFFER
484 *   STM,5   E.TM1
485 *   ABR,7,15
486 *   ABR,3,15
487 *   LDX,5,7 SECOND TIME WORD
488 *   STX,5,3 AND STORE
489 *   ABMM,15,6 BUMP COMP. POINT COUNTER
490 *   TRF8,0,11 CMPCT
491 *   COMRT LFM,1 TREGS   RESTORE REGISTERS
492 *   BRX,4
493 *   NEWIN SFM,1 TREGS   AND RETURN
494 *   LDM,5,6 CENTR   MAX=CENTER + DELTA
495 *   ADM,5,6 DELTA
496 *   STM,5,6 WINMX
497 *   A510
498 *   R 02C0
499 *   E656
500 *   R 03D4
501 *   ED30
502 *   A 0003
503 *   A026
504 *   X 0012
505 *   E036
506 *   X 001F
507 *   FE53
508 *   R 603F
509 *   6D7E
510 *   R 687F
511 *   FD57
512 *   FE53
513 *   E650
514 *   X 0018
515 *   R 20F6
516 *   X 0012
517 *   A410
518 *   R 02C7
519 *   A410
520 *   P 02C0
521 *   FF04
522 *   A510
523 *   R 02C0
524 *   E556
525 *   R 03D4
526 *   E056
527 *   R 03AF
528 *   E656
529 *   R 0393

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528 *****
529 * GAIN CONTROL PARAMETERS.....
530 * AMPMN...MINIMUM ACCEPTABLE AMPLITUDE VALUE.
531 * AMPMX...DIFF. BETWEEN MAX ACCEPTABLE AND MIN.
532 * AMPDP...VALUE BELOW WHICH PHASE CONSIDERED WDRTHLESS.
533 * SATUR...SATURATION POINT, VALUE ABOVE WHICH PHASE CONSIDERED WORTHLESS
534 AMPMN DFC          614
535 AMPMX DFC          666
536 AMPDP DFC          563
537 SATUR DFC          707
538 * LOVTV .. IS COUNTER OF POINTS BELOW AMPMN FOR EACH CHANNEL
539 * H1VTV .. IS COUNTER OF POINTS ABOVE AMPMX FOR EACH CHANNEL
540 LOVTV RES          14,0
541 H1VTV RES          14,0
542 0312 A 0000
543 0313 A 0000
544 * MURAV IS THE AMPLITUDE BAD SAMPLE FLAG. IT'S SET TO +1 IF THE CHAN
545 * IS SATURATED AND TO -1 IF THE SIGNAL DROPS OUT.
546 MUBAV DFC          0
547 * THE CHANNEL DOWN SEQUENCE USES TWO PARAMETERS:
548 * UBADT IS THE TALLY OF BAD DATA BLOCKS AND DETERMINES WHEN CHANNEL
549 * CHANGES STATE.
550 * UBADS IS A SUM OF U.BAV AND IS USED TO DETERMINE WHY CHANNEL IS
551 * CHANGING STATUS.
552 UBACT RES          14,0
553 UBADS RES          14,0
554 * THE STANDARD DEVIATION OF THE INPUT IS COMPUTED USING THE FOLLOWING:
555 * SIGMA=SQRT(SUM OF (FAZ-XBAR)**2
556 * WHERE:
557 * UACCI = SUM OF CURRENT POINTS
558 * UZACC = SUM OF (CURRENT POINTS - XBAR)**2/89
559 * XBAR = ESTIMATE OF AVERAGE INPUT
560 UACCI RES          28,0
561 UZACC RES          28,0
562 U.SIG EQU          $
563 SIGMA RES          14,0
564 LSIGM RES          14,0
565 XBAR RES          14,0
566 * COMPRESSED DATA POINTS ARE MAINTAINED IN THE US$DAT AREA, BUT THE
567 * FOLLOWING ARE USED IN COMPRESSED DATA COMPUTATION.....
568 * WINMX AND WINMN ARE THE UPPER AND LOWER ROUNDS FOR THE CURRENT WINDOW~
569 * DELTA IS SIGMA**K
570 WINMX RES          14, #7FFF
571 WINMN RES          14, #8000
572 DELTA RES          14,0
573 K                #0001, #4000      =1.5
574 * UBACC IS USED TO DETERMINE THE APPROXIMATE PERCENTAGE BLANKED.
575 * THE LAST BLANKED PERCENTAGE VALUE FROM EACH 10 SECOND PERIOD IS
576 * AVERAGED OVER TWO MINUTES.

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MODCOMP ASSEMBLY PARAMETERS, CONSTANTS, ETC.....

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577 03BF A 0000 UBACC RES 14,0
578 03CD A 0000 CFLAG DFC 0
579 * BLANKING LEVEL CONTROL PARAMETERS: MIN AND MAX ALLOWED VALUES
580 03CE A 00B9 #R9
581 03CF A 00A9 #A9
582 * GCLOCK IS THE GAIN CLOCK....
583 * TCLOCK IS THE TRACK RATE CLOCK....
584 R 03D0 $ -12
585 03D0 A FFF4 GCLOCK DFC
586 R 03D1 $ -9
587 03D1 A FFF7 TCLOCK DFC
588 *****
589 * TRACKING RATE CONTROL PARAMETERS.....
590 * TRMAX MAXIMUM STANDARD DEVIATION PERMITTED
591 * TRMIN MINIMUM STANDARD DEVIATION PERMITTED
592 * WHEN SIGMA FALLS OUTSIDE THESE BOUNDS, TRY TO BRING IT BACK
593 03D2 A 0014 #14
594 03D3 A 0008 #08
595 *****
596 * CENTR IS THE COMPRESSED DATA POINT AT THE WINDOW CENTER
597 03D4 A 0000 14,0
598 *****
599 END

```

TOTAL ASSEMBLY ERRORS = 0 SIZE = 994

```

1 0000 6C44 PGM U$LSQ
2 0001 ED60 INT U:LSQ
3 0002 FFE4 EXT U.LST,U.LSP,U:TGR,U.CST,U.SLP
4 0003 E646 CAT NO. 400100-003
5 0004 R 006F * U$LSQ IS THE LEAST SQUARES SLOPF ESTIMATOR.
6 0005 706F * THE LAST 10 OF THE 10 SECOND PHASE AVERAGES ARE ANALYZED.
7 0006 R 0003 U:LSQ ZRR,4 CLEAR SUMS
8 0007 E550 LDI,6 -28
9 0008 X 0002 STM,4,6 SUM+28
10 0009 R 006F ABRB,6,15 $-2
11 0010 E550 LDM,5 U.LSP R5=ROW POINTER
12 0011 X 0002 LDM,4 TUMAX
13 0012 R 0071 TTR,4,4 TUMAT INIT. TUMAT=-TUMAX
14 0013 E640 STM,4 TUMAT R6=CHANNEL POINTER(B)
15 0014 E640 NEXTA ZRR,6 D.P.POINTER
16 0015 E00F LOOP1 TRR,4,6 TBMB,13,6 U.CST,LASTR BYPASS IF CHANNEL DOWN
17 0016 2D41 LLS,4,1
18 0017 86D6
19 0018 X 0004 TRR,7,6 POINT=A+B
20 0019 R 0024 ADR,7,5
21 0020 6D76 * SUM=SUM+2**4*TUMAT*U.LST
22 0021 6875 TUMAT
23 0022 E5D0 LDM,13
24 0023 R 006F MPM,12,7 U.LST R12,13=U.LST*TUMAT
25 0024 X 0001 RAD,12,12 *2**4 {CLR MPY BY FRACTION}
26 0025 E5A4 LDM,10,4 SUM +SUM
27 0026 E5R4 LDM,11,4 SUM+1
28 0027 R 0054 DAR,10,12 INTO SUM!
29 0028 E6A4 STM,10,4
30 0029 R 0053 STM,11,4 SUM+1
31 0030 R 0054 LASTB ABR,6,15 14 NEXT CHAN
32 0031 606F ED40 SURB,4,6 LOOP1 LOOP THRU 14 CHANNELS
33 0032 A 000E ADI,5 14 NEXT ROW
34 0033 R 000F E850
35 0034 A 000E

```

34	002B	ED40	LDI,4	140	
	002C	008C			
35	002D	7945	SURB,4,5	\$+3	
	002E	0030			
36	002F	6D54	TRP,5,4	U.SLP	RECYCLE ROW POINTER
37	0030	E540	LDM,4		
	0031	0002			
38	0032	7945	SURB,4,5	\$+4	LOOP THRU 10 ROWS
	0033	0036			
39	0034	E700	RRU	DONEA	THEN EXIT
	0035	003C			
40	0036	E560	LDM,6	TUWCF	
	0037	0070			
41	0038	C060	ADMM,6	TUWAT	TUWAT=TUWAT+TUNDF
	0039	006F			
42	003A	E700	RRU	NEXTA	AND DO NEXT ROW
	003B	000E			
43					
44	003C	6C66			
45	003D	6D46	DOONEA ZRR,6		REINIT CHANNEL POINTER
46	003E	2D41	LOOP2 TRP,4,6		
47	003F	F536	LLS,4,1	U.CST	D.P. POINTER FOR CHANNEL
	0040	0004	LDM,3,6		
48	0041	E930	SUI,3	1	CHECK IF CHANNEL IS UP
	0042	0001			
49	0043	7D33	TRRB,3,3	NEXTB	...NO, DO NEXT CHANNEL
	0044	004C			
50	0045	F5D4	LDM,13,4	SUM+1	
	0046	0054			
51	0047	A0C0	MPM,12	TUFAC	SUM*TUFAC
	0048	0072			
52	0049	2EC1	LAD,12,1		INTO U.SLP
53	004A	E6C6	STM,12,6	U.SLP	NEXT CHANNEL
	004B	0005			
54	004C	606F	NEXTB ARR,6,15		
55	004D	ED50	LDI,5	14	
	004E	000E			
56	004F	7956	SURB,5,6	LOOP2	LOOP THRU 14 CHANNELS
	0050	003D	BRU	U:TGR	DO TRIGGER DETECTION
57	0051	E700			
	0052	0003			

* SLOPE=TUFAC*SUM FOR EACH CHAN.

MODCOMP ASSEMBLY PARAMETERS, CONSTANTS, ETC.....

```

59
60
61
62
63
64
65
66

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0053	A	0000	
006F	A	3999	
0070	A	0CCC	
0071	A	3999	
0072	A	5D17	
0073			

```

* SUM IS A DOUBLE PRECISION ACCUMULATOR FOR THE COMPUTATION
SUM RES 28.0
TUMAT DFC #3999 .45
TUMDF DFC #0CCC .10
TUMAX DFC #3999 .45
TUFAC DFC #5D17 .727272
END

```

TOTAL ASSEMBLY ERRORS = 0 SIZE = 115

```

1  U$TGR
2  U:TGR,U:GET,U:TRF,U:TRL
3  U:CPC,U:CPP,U:AST,U:CDT,E:DIF
4  U:WFA,U:CDT,U:CDM,U:AMP,U:OAM
5  U:PIU,U:ODI,U:OFZ,U:OTM,U:OTB
6  U:CST,U:OSG,U:SIG,U:DTM,U:END
7  U:RP2,U:CLS,U:LTF,U:FAC,U:LFZ
8  U:LTM
9  CAT NO. 400100-005
10 * U$TGR IS THE U-WAVEFORM TRIGGER DETECTION ROUTINE.
11 * U:TGR ZRR,6 INIT CHANNEL COUNTER
12 NEXTP LDM,4,6 U:CST
13 S8PB,4,15 NEXTC
14 LDI,2 1 IS ALARM STATUS = TO 1?
15 SUM,2,6 U:AST
16 TRRB,2,2 $+3 ..NO, GO TO NEXT CHAN
17 HOP,LOOK
18 ADI,2 #6 NEW ONSET?
19 TRRB,2,2 NEXTC
20 LOOK BLM,14 U:GET
21 BRU NEXTC NO POINTS, NEXT CHANNEL
22 TPR,5,6
23 LLS,5,1
24 LDM,2,6 U:TRF
25 BRU*,2 JMPTB
26 JMPTB DFC U:TRF=0
27 TRF1 DFC =1
28 TRF2 DFC =2
29 TRF3 DFC =3
30 DFC NEXTC =4
31 * U:TRF=0, SO WE'RE LOOKING FOR THE FIRST POINT, AND SINCE
32 * U:CPC ISN'T ZERO, WE HAVE IT IN R11, TIME IN R12,13.
33 * NOW, SOME HOUSEKEEPING, THEN ON TO THE INTERESTING STUFF.
34 TRFO ZPR,2 TRCNT RESET TRCNT=0
35 STM,2,6
36 BRU INCTR
37 * U:TRF=1, SO WE HAVE THE FIRST POINT IN TRBUF AND THE SECOND IN

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38 * R12. WE'LL COMPARE THESE TO DETERMINE THE DIRECTION OF THE
39 * POSSIBLE ONSET.
40 * IF THE SECOND PCINT > FIRST, SET U.TRF=3 (RETARD)
41 * OTHERWISE, SET U.TRF=2 (ADVANCE).
42 TRF1 BLM,4 STPON          STDR E POSSIBLE ONSET
43   0024 R E740
44   0025 R 0129
45   0026 6D1B TRR,1,11      GET CURRENT VALUE
46   0027 E11E SUM*,1,6     SUBTRACT ONSET VALUE
47   0028 R 0307 TRBF      TRBFP
48   0029 7610 TBRB,1,0    INCTR
49   002A R 002D ABMM,15,6  SET TRFLAG=3
50   002B 80F6 U.TRF
51   002C R 0147 INCTR ABMM,15,6 U.TRF
52   002D 80F6 U.TRF
53   002E R 0147          SET TRFLAG=2
54   002F ED30          * STORE CURRENT POINT AND TIME IN TRBUF....
55   0030 0030 STRTR LDI,3 3
56   0031 A 0033 MPM,2,6   TRCNT
57   0032 R 0155 ADM,3,6   TRBFP
58   0033 E036          TRCNT
59   0034 R 0307 STX,11,3  STORE VALUE
60   0035 FEB3 ABR,3,15   STORE HI-TIME WORD
61   0036 603F STX,12,3
62   0037 FEC3 ABR,3,15   INCRM TRCNT
63   0038 603F STX,13,3
64   0039 FED3 ABMM,15,6
65   003A 80F6          TRCNT
66   003B R 0155          INCRM TRCNT
67   003C E526          * ANOTHER POINT IN OUR POSSIBLE TRIGGER HAS BEEN SAFELY TUCKED IN.
68   003D R 0155          * NOW, LET'S SEE IF WE HAVE TRIM OF THEM YET....
69   003E E126          LDM,2,6   TRCNT
70   003F R 0315 SUM,2,6   U.TRL
71   0040 7022 TRRB,2,2  NEXTP          NDT ENOUGH YET, GET ANOTHER
72   0041 R 0001          * ONSET IS GETTING MORE LIKELY. WE HAVE TRIM POINTS IN THE SAME
73   0042 ED30          * DIRECTION. NOW, IF THEY FALL WITHIN TRIM SECONDS, WE HAVE
74   0043 X 0013          * A REAL HONEST-TC-GOODNESS TRIGGER.
75   0044 E525          * FIRST, GET THE POSSIBLE ONSET TIME INTO U.DTM
76   0045 R 0323          LDI,3    U.DTM
77   0046 FE23          LDM,2,5   PONST
78   0047 603F STX,2,3
79   0048 E525          ABR,3,15
80   0049 R 0324          LDM,2,5   PONST+1
81   004A FE23          STX,2,3          SET U.DTM WITH POSS. ONSET TIME

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73	0048	E6C0	STM,12	TEMP	
	004C	R 033F			
74	004D	E6D0	STM,13	TEMPI	
	004E	R 0340			
75	004F	6046	TRR,4,6		HOLD CHANNEL REGISTER
76	0050	ED60	LDI,6	14	POINT TO U.DTM
	0051	A 000E			
77	0052	E7E0	BLM,14	E:DI F	RETURN WITH
	0053	X 0005			
78	0054	R 033F	DFC	TEMP	R12,13=TIME DIFFERENCE
79	0055	6600	NOP		
80	0056	6600	NOP		
81	0057	6064	TRR,6,4		RESTORE CHANNEL REGISTER
82	0058	E1D0	SUM,13	TRTIM	
	0059	R 0341			
83	005A	76D0	TBRB,13,0	GUOTR	BRU IF TIME<TRTIM
	005B	R 007C			
84	005C	E536	LDM,3,6	TRCNT	
	005D	R 0155			
85	005E	613F	SBR,3,15		DECRIMENT COUNT
86	005F	E636	STM,3,6	TRCNT	
	0060	R 0155			
87	0061	ED40	LDI,4	3	
	0062	A 0003			
88	0063	2024	MPR,2,4		WORDS TO BE TRANSFERED
89	0064	6F43	TTR,4,3		R4=WORD COUNT
90	0065	E526	LDM,2,6	TRBFP	R2=STORE LOCATION
	0066	R 0307			
91	0067	6D32	TRR,3,2		
92	0068	E830	ADI,3	3	R3=LOAD LOCATION
	0069	A 0003			
93	006A	FD13	LDX,1,3		MOVE FIRST VALUE OUT
94	006B	FE12	STX,1,2		AND REST UP ONE
95	006C	602F	ABR,2,15		
96	006D	603E	ABR,3,15		
97	006E	704F	ABR,4,15	LOOP1	
	006F	R 006A			
98	0070	E536	LDM,3,6	TRBFP	SET UP FOR NEW PONST
	0071	R 0307			
99	0072	E830	ADI,3	3	
	0073	A 0003			
100	0074	603F	ABR,3,15		
101	0075	FDC3	LDX,12,3		
102	0076	603F	ABR,3,15		
103	0077	FDD3	LDX,13,3		
104	0078	E740	BLM,4	STPCN	GET NEW PONST
	0079	R 0129			
105	007A	E700	BRU	NEXTP	
	007B	R 0001			

* HOLY COW (SAINTLY BOVINE?), WE HAVE AN ONSET ON THE CHAN IN R6

107	007C	E546	GUDTR	LDM,4,6	U.AST		
108	007E	714F	SBRB,4,15	\$+4			
109	0080	F700	BRU	GOODY		INITIAL ONSET	
110	0081	R 0092					
111			* THE ONSET WE HAVE JUST FOUND TERMINATES A SIGNAL				
112			* ALREADY IN PROGRESS, BUT PAST ITS HALF RECOVERY				
113			* POINT.				
114			* WE'LL HAVE TO TERMINATE PROCESSING ON THAT SIGNAL				
115			* BEFORE WE CAN TAKE UP THIS NEW POSSIBILITY.				
116	0082	6542	LBR,4,2				
117	0083	F646	STM,4,6	U.END		FLAG END TYPE	
118	0084	X 0014					
119	0085	F7F0	RLM,15	U:RP2		CUT OLD SIG OFF	
120	0086	X 0015					
121	0087	6C44	ZRR,4				
122	0088	E646	STM,4,6	U.END			
123	0089	X 0014				CLEAR PARMS FOR NEW SIGNAL	
124	008A	E646	STM,4,6	U.CLS			
125	008B	X 0016					
126	008C	F546	LDM,4,6	U.LTF			
127	008D	X 0017					
128	008E	E646	STM,4,6	U.FAC			
129	008F	X 0018					
130	0090	F700	BRU	GOOD1			
131	0091	R 009A					
132	0092	F546	GOODY	LDM,4,6	U.DIU		
133	0093	X 000B				ONSET DIURNAL=U.DIU	
134	0094	E646	STM,4,6	U.ODI			
135	0095	X 000C					
136	0096	F546	LDM,4,6	U.SIG			
137	0097	X 0012					
138	0098	F646	STM,4,6	U.OSG			
139	0099	X 0011					
140	009A	E546	LDM,4,6	U.TRF			
141	009B	R 0147				SET U.AST=U.TRF	
142	009C	F646	STM,4,6	U.AST			
143	009D	X 0003					
144	009E	654D	LBR,4,13				
145	009F	E646	STM,4,6	U.TPF		RESET U.TRF=4	
146	00A0	R 0147					
147	00A1	F546	LDM,4,6	U.AMP			
148	00A2	X 0009					
149	00A3	F646	STM,4,6	U.OAM		ONSET AMP=U.AMP	
150	00A4	X 000A					
151	00A5	F546	LDM,4,6	TRRFP			
152	00A6	R 0307					
153	00A7	FD34	LDX,3,4				
154	00A8	F636	STM,3,6	U.OFZ		ONSET PHASE	

Line	Code	Label	Address	Value	Description
137	00A9	X	000D		
	00AA	E536			
138	00AB	X	0019		
	00AC	E636			
	00AD	X	0007		
139	00AE	604D			
140	00AF	FD34			
141	00B0	E635			
	00B1	X	000F		
142	00B2	E535			
	00B3	X	001A		
143	00B4	E635			
	00B5	X	0008		
144	00B6	E535			
	00B7	R	0323		
145	00B8	E635			
	00B9	X	000E		
146	00BA	604F			
147	00BB	605F			
148	00BC	FD34			
149	00BD	E635			
	00BE	X	000F		
150	00BF	E535			
	00C0	X	001A		
151	00C1	E635			
	00C2	X	0008		
152	00C3	E535			
	00C4	R	0323		
153	00C5	E635			
	00C6	X	000E		
154	00C7	606F			
155	00C8	ED50			
156	00C9	A	000E		
157	00CA	7956			
	00CB	R	0001		
158	00CC	E700			
	00CD	X	0006		
159					
160					
161					
162					
163	00CE	E7E0			
	00CF	R	0139		
164	00D0	F700			
	00D1	R	00D4		
165	00D2	E700			
	00D3	R	002F		
166	00D4	F546			
	00D5	R	0307		

Code	Address	Value	Description
U.LFZ	LDM,3,6		CANDIDATE PEAK
U.CDT	STM,3,6		
U.OTB	ABP,4,13		ONSET TIME, BCD
	LDX,3,4		
	STM,3,5		
U.LTM	LDM,3,5		
U.CDM	STM,3,5		
PONST	LDM,3,5		
U.OTM	STM,3,5		ONSET TIME, BINARY
	ABR,4,15		
	ABR,5,15		
	LDX,3,4		
	STM,3,5		
U.OTB	LDM,3,5		
U.LTM	STM,3,5		
U.CDM	LDM,3,5		
PONST	STM,3,5		
U.OTM	STM,3,5		

Code	Address	Value	Description
* GC ON TO NEXT CHANNEL			
NEXTC	ABR,6,15		
LDI	,14		
NEXTP	SURB,5,6		LOOP THROUGH 14 CHANNELS
U:WFA	BRU		THEN DO WAVE FORM ANALYSIS

Code	Address	Value	Description
* IF U.TRF=2 WE ARE WORKING ON AN ADVANCE ONSET. IF THE CURRENT POINT CONTINUES THE TREND, C.EST GRAND. OTHERWISE, SAVE THE LAST POINT AS THE POSSIBLE START OF A NEW ONSET AND TAKE IT FROM THERE.			
TRF2	BLM,14	FZDIF	
\$+4	BRU		CUR>LAST VAL, CONTINUE
STRTR	BRU		CUR<LAST VAL
TRRFP	LDM,4,6		RESET TRIGGER

167	00D6	E7D0	LDI,7	-3	MOVE LAST VALUE.....
168	00D7	FFFD			
169	00D8	FD23	LOOP2		TO FIRST:
170	00D9	FE24	LDX,2,3		
171	00DA	603F	STX,2,4		
172	00DB	604F	ABR,3,15		
	00DC	707F	ABR,4,15	LOOP2	
	00DD	00D8	ABRB,7,15		
173	00DE	652F	LBR,2,15		RESET
174	00DF	E626	STM,2,6	U,TRF	U,TRF=1
175	00E0	0147	STM,2,6	TRCNT	TRCNT=1
176	00E1	E626	BRU	TRF1	
177	00E2	0155			
178	00E3	E700			
179	00E4	0024			
180					
181	00E5	E7E0	TRF3	BLM,14	FZDIF
182	00E6	0139	BRU	STRTR	GOOD POINT:
183	00E7	E700	BRU	TRRST	BAD POINT:
	00E8	002F			
	00E9	E700			
	00EA	00D4			

* IF U,TRF=3 WE ARE WORKING ON A RETARD ONSET. IF THE CURRENT
 * POINT CONTINUES IN THAT DIRECTION, C'EST MAGNIFIQUE.
 * OTHERWISE, SAVE THE LAST POINT AS THE POSSIBLE ONSET AND
 * TAKE IT FROM THERE.

* U:GET IS A DUAL-RETURN SUBROUTINE WHICH:
* 1. CHECKS TO SEE IF ANY COMPRESSED POINTS ARE HELD
* 2. IF ANY ARE, IT RETURNS THE NEXT ONE.
* CALLING SEQUENCE:
* RLM,14 U:GET (CHAN IN R6)
* (RETURN IF NO COMPRESSED POINTS)
* (RETURN IF THERE ARE COMPRESSED POINTS)
* RETURN WITH POINTS IN R11, ITS TIME IN R12,13
* SAVE REGISTERS
U:GET SFM,1

```

185 00EB A510
186 00EC R 0114
187 00ED E526
188 00EE X 0001
189 00EF 7D22
190 00F0 R 00F3
191 00F1 E700
192 00F2 R 0110
193 00F3 ED30
194 00F4 A 0003
195 00F5 A026
196 00F6 R 011B
197 00F7 E036
198 00F8 X 0002
199 00F9 FDB3
200 00FA 603F
201 00FB FDC3
202 00FC 603F
203 00FD FDD3
204 00FE 60EE
205 00FF 80F6
206 0100 R 011B
207 0101 ED30
208 0102 A FFFF
209 0103 C036
210 0104 X 0001
211 0105 E686
212 0106 X 0019
213 0107 2D61
214 0108 E6C6
215 0109 X 001A
216 010A 606F
217 010B E6D6
218 010C X 001A
219 010D A410
220 010E R 0114
221 010F FFOE
222 0110 F626
223 0111 R 011B
224 0112 E700
225 0113 R 010D

LDM,2,6 U,CPC GET COMP. POINT COUNTER
TRRB,2,2 $+4 EXIT IF COUNT=0
BRU GETO1
LDI,3 3 TAKE OUT COUNTER
MPM,2,6 GETCN *3
ADM,3,6 U,CPP +OFFSET
LDX,11,3 GET POINT
ABR,3,15 GET HI-TIME WORD
LDX,12,3 GET LOW-TIME WORD
ABR,3,15 MOVE RETURN ADDRESS
LDX,13,3 MOVE OUT COUNTER
ABR,14,14
ABMM,15,6
LDI,3 -1
ADMM,3,6 U,CPC DECREM. COMP. POINT COUNTER
STM,11,6 U,LFZ SAVE LAST POINT
LLS,6,1 U,LTM
STM,12,6 U,LTM
ABR,6,15 U,LTM
STM,13,6
GETRT LFM,1 GETSV RESTORE REGISTERS
RRX,14 AND RETURN
GETO1 STM,2,6 GETCN RESET OUT-COUNTER
BRU GFTRT

```

MDDCOMP ASSEMBLY

U:GET SUBROUTINE.....

219
220

0114
011B A

0000

GETSV RES
GETCN RES

7

14,0

```

222          E6C0
223          0129 R 012A R 033F
224          012B R 06D0
225          012C R 0340
          012D X 07E0
          012E X 0004
228          012F R 033F
229          0130 R 06C5
          0131 R 0323
230          0132 R 06D5
          0133 R 0324
231          0134 R 05C0
          0135 R 033F
232          0136 R 05D0
          0137 R 0340
233          0138 R FF04

          E6C0
          STM,13
          BLM,14
          DFC
          STM,12,5
          STM,13,5
          LDM,12
          LDM,13
          BRX,4

          TEMP
          PONST
          PONST+1
          TEMP
          TEMPI

          TIME IN TEMP STORAGE
          E:COT
          RINARY POSSIBLE ONSET TIME
          RESTORE R12,13

```

* STPON TAKES THE INPUT FORMATTED TIME IN R12,13 AND STORES
* THE BINARY EQUIVALENT IN PONST FOR THE CURRENT CHANNEL
STPON STM,12
TEMP
TIME IN TEMP STORAGE
E:COT
RINARY POSSIBLE ONSET TIME
RESTORE R12,13

```

235 *****
236 * FZDIF COMPUTES THE DIFFERENCE IN PHASE BETWEEN THE CURRENT *****
237 * VALUE IN (R11) AND THE LAST VALUE STORED IN TRBUF. *****
238 * IF CUR.VAL->LAST VAL., USE 1ST RETURN *****
239 * OTHERWISE USE THE 2ND RETURN *****
240 * CALLING SEQUENCE..... *****
241 * BLM,14 FZDIF (R6=CHAN,R11=CURRENT VALUE) *****
242 * (RETURN LOCATION IF CUR.VAL->LAST VAL.) *****
243 * (RETURN OTHERWISE) *****
244 FZDIF LDM,3,6 TRCNT
      E536
0139 R 0155
013A R 0155
013B 613F
0138 613F
013C FD40
013D A 0003
013E 2024
013F E036
0140 R 0307
0141 FD23
0142 692B
0143 7620
0144 R 0146
0145 60EE
0146 FF0E
      SBR,3,15
      LDI,4 3
      MPR,2,4
      ADM,3,6 TRBFP
      LDX,2,3
      SUP,2,11 $+3
      TBRB,2,0
      ABR,14,14
      BRX,14
R3=ADDP OF LAST PHASE VALUE
R2=LAST PHASE VALUE
R2=LAST VAL.-CUR.VAL.
BRU IF CUR>LAST VALUE
INC. RETURN IF LAST>CUR VAL
RETURN

```

```

255 *****
256 * U-TYPE IS THE TRIGGER STATUS FLAG. IT IS INTERPRETED: *****
257 * 0 LOCKING FOR ONSET (FIRST POINT) *****
258 * 1 LOCKING FOR SECOND POINT IN EITHER DIRECTION *****
259 * 2 LOCKING FOR ADDITIONAL POINTS (ADVANCED DIRECTION) *****
260 * 3 LOCKING FOR ADDITIONAL POINTS (RETARD DIRECTION) *****
261 * 4 VALID ONSET *****
262 0147 A 0000 14,0 *****
263 ***** TRCNT IS A COUNTER OF POINTS IN THE TRIGGER BUFFER *****
264 0155 A 0000 14,0 *****
265 *****
266 *****
267 * TRBUF IS A 14 CHANNEL * 3 WORD/ENTRY * 10 ENTRY BUFFER OF POSSIBLE *****
268 * POINTS LEADING TO A VALID TRIGGER. *****
269 * TRBFP IS A CHAN: BY CHAN TABLE OF STARTING POINTS. *****
270 TRBUF RES 420,0 *****
271 TRBFP DFC *****
272 0163 A 0000 *****
273 0307 R 0163 TRBUF *****
274 0308 R 0181 TRBUF+30 *****
275 0309 R 019F TRBUF+60 *****
276 030A R 01BD TRBUF+90 *****
277 030B R 01DB TRBUF+120 *****
278 030C R 01F9 TRBUF+150 *****
279 030D R 0217 TRBUF+180 *****
280 030E R 0235 TRBUF+210 *****
281 030F R 0253 TRBUF+240 *****
282 0310 R 0271 TRBUF+270 *****
283 0311 R 028F TRBUF+300 *****
284 0312 R 02AD TRBUF+330 *****
285 0313 R 02CB TRBUF+360 *****
286 0314 R 02E9 TRBUF+390 *****
287 *****
288 * TPLIM IS THE NUMBER OF POINTS REQUIRED FOR A VALID TRIGGER *****
289 U.TRL RES 14,5 *****
290 *****
291 * PONST IS THE BINARY TIME FOR THE FIRST POINT IN TRBUF (EACH CHAN) *****
292 0323 A 0000 28,0 *****
293 *****
294 033F A 0000 0 *****
295 0340 A 0000 0 *****
296 *****
297 * TRTIM IS THE MAXIMUM PERMISSIBLE TIME BETWEEN FIRST AND LAST POINT *****
298 * IN THE TRIGGER SEQUENCE. (TIME IN SECONDS). *****
299 TRTIM DFC 30 *****
*****
END

```

TOTAL ASSEMBLY ERRORS = 0 SIZE = 834

```

*****WARNING*** **0** 015P
*****WARNING*** **0** 0184
*****WARNING*** **0** 0188
*****WARNING*** **0** 018A

```

```

CURRENT JOURNAL TO ONSET VALUE.
PASS, SET CLASS = 2
PASS, SET CLASS = 2
REASN

```

CK. CLASS 2 OFFSET CRITERIA).*****

```

1      PGM          U$WFA
2      INT          U:WFA,U.LFZ,U.LTM,U.CDT,U.CDM
3      INT          U.CCL
4      EXT          U.CST,U.AST,U.SLP,U.DIU,U.FAC
5      EXT          U:RPI,U:RP2,U:GET,E:DIF,U.CTM
6      EXT          U.CLS,W.PEZ,W.PTM,W.PAM,W.OST
7      EXT          W.ONR,W.LPK,W.LTM,W.VPK,W.VTM
8      EXT          W.RFZ,W.RTM,W.RAM,E:SKD,U.END
9      EXT          U.OFZ,U.OOI,U.BGF,U.TRK,U.ODR
10     EXT          E:COT,U.OTM,E.IOF,U.LTF,E:O35
11     EXT          E:FRI,U.AMP,U.DTM,W.LAM,W.VAM
12     EXT          U.FRQ,U.TRF,U.TPL
13
14     * CAT NO. 400100-006
15     * U$WFA PERFORMS WAVEFORM ANALYSIS ON A POSSIBLE SIGNAL WHICH
16     * WAS DETECTED BY U$TGR.
17     U:NFA ZRR,6      R6=CHANNEL
18     LOOP1 TBMB,13,6  U.CST,DIED  IS CHAN DOWN?
19
20     LDM,2,6          U.AST          NO, CHECK U.AST
21     SBR,2,15
22     TRRB,2,0        WFA1          U.AST=0
23     TRRB,2,2        WFA2          U.AST>1
24     BRU              UPDIU        U.AST=1
25     WFA1 LBP,2,15   U.AST
26     STM,2,6         U.SLP
27     STM,2,6         U.DIU        U.DIU=U.SLP
28
29     * THE DIURNAL SLOPE ESTIMATE IS UPDATED THUS:
30     * U.DIU=U.DIU+U.FAC*(U.SLP-U.DIU)
31     UPDIU LDM,3,6   U.SLP
32     SUM,3,6         U.DIU
33     MPM,2,6         U.FAC
34     LAD,2,1         U.DIU
35     ADMM,2,6
36     NEXTC RLM,15   SCMPD          CLEAR C.P. BUFFER
37     ABF,6,15       LDI,5          NEXT CHANNEL
38     ED50

```

MODCOMP ASSEMBLY

37	0021	A	000E		
	0022		7956		
	0023	R	0001	SURB,5,6	LDOP1
38					
39					
40					
41	0024		E700		
	0025	X	0018		

* ALL U-ANALYSIS IS DONE FOR THIS TEN SECOND PERIOD,
* RETURN TO THE SCHEDULER AND WAIT, WE'LL BE BACK.
*
SEE Y'ALL LATER
BRU
E:SKD

```

43      0026      E526
44      0027      X 0002
45      0028      E920
46      0029      A 0006
47      002A      R 7620
48      002B      R 0047
49      002C      R 7D22
50      002D      R 0043
51      002E      E536
52      002F      R 0337
53      0030      E636
54      0031      X 0015
55      0032      E536
56      0033      X 0025
57      0034      F636
58      0035      X 0017
59      0036      6D56
60      0037      2D51
61      0038      E535
62      0039      R 0345
63      003A      E635
64      003B      X 0016
65      003C      605F
66      003D      E535
67      003E      R 0345
68      003F      E635
69      0040      X 0016
70      0041      E7F0
71      0042      X 0006
72      0043      8206
73      0044      X 0019
74      0045      E7F0
75      0046      X 0007
76      0047      6C22
77      0048      E626
78      0049      X 0002
79      004A      6D56
80      004B      2D51
81      004C      E625
82      004D      X 0020
83      004E      605F
84      004F      E625
85      0050      X 0020
86      0051      E700
87      0052      R 001D

```

* THE CHANNEL IS DOWN. IF WE HAVE A WAVEFORM IN THE ADVANCED STAGES, I.E. U.AST IS 7 OR 8, REPORT THE WAVE, QUALIFYING THE REPORT WITH A CHANNEL DOWN STATEMENT. IN ANY CASE, ENSURE THAT U.AST=0 AND GO TO THE NEXT CHANNEL.

DIEC LDM,2,6 U.AST

SUI,2 6

TBRB,2,0 RSTAS U.AST<6

TRRB,2,2 DAST7 U.AST=7

LDM,3,6 U.LFZ SET UP CURRENT VALUES

STM,3,6 W.RFZ AS HALF RECOVERY

LDM,3,6 U.AMP VALUES.

STM,3,6 W.RAM

TRR,5,6

LLS,5,1

LDM,3,5 U.LTM

STM,3,5 W.RTM

ABR,5,15

LDM,3,5 U.LTM

STM,3,5 W.RTM

BLM,15 U:RP1 U.AST=6,END SIGNAL THERE

DAST7 GRMM,0,6 U.END SET ABNORMAL END BIT

BLM,15 U:RP2 END SIGNAL COMPLETELY

RSTAS ZRR,2 U.AST RESET U.AST=0

STM,2,6

TRP,5,6

LLS,5,1

STM,2,5 U.OTM D.P. CHAN POINTER

ABP,5,15

STM,2,5 U.OTM IN TWO PARTS

BRU NEXTC

* U.AST INDICATES THE STATE OF THE WAVEFORM.

* GO TO THE ROUTINE CORRESPONDING TO THAT STATE.....

WFA2 LDM,2,6 U.AST

74

75

76

77

0053 E526

0054 X 0002

0055 E70A

0056 R 0055

0057 R 005D

0058 R 0099

0059 A 0000

005A R 01A5

005B R 01F0

005C R 02A2

BRU*,2 JMPTB-2

JMPTB DFC

DFC

DFC

DFC

DFC

UAST2

UAST3

0

UAST5

UAST6

UAST7

(ILLEGAL VALUE)


```

86 *****
87 * WHEN U.AST=2, WE HAVE FOUND AN ONSET IN THE ADVANCE DIRECTION.
88 * AS LONG AS PHASE VALUES DECREASE, WE KEEP TRACKING. WHEN
89 * TWO SUCCESSIVE COMPRESSED POINTS SHOW A CHANGE, THE MINIMAL
90 * POINT IS CONSIDERED PEAK.
91 * IF NO PEAK IS FOUND WITHIN T1 SECONDS, THE ONSET AND SIGNAL
92 * ARE REJECTED.
93 UAST2 TRR,5,6
94 LLS,5,1
95 BLM,14
96 BRU
97 LDM,2,6
98 SUR,2,11
99 SBR,2,15
100 TBRB,2,0
101 STM,11,6
102 STM,12,5
103 STM,13,5
104 ZRR,2
105 STM,2,6
106 AST23 STM,12
107 STM,13
108 BLM,14
109 DFC
110 NOP
111 NOP
112 SUM,13
113 TBRB,13,0
114 SREAL LBF,2,15
115 STM,2,6
116 BRU
117 AST22 ARMM,15,6
118 LDI,2

```

```

6D56
2D51
E7E0
0008 X
0061 R
0062 R
0063 R
0064 R
0065 692B
0066 612F
0067 7620
0068 R
0069 E686
006A R 0361
006B E6C5
006C R 036F
006D E6D5
006E R 0370
006F 6C22
0070 E626
0071 R 0399
0072 E6C0
0073 R 03A7
0074 E6D0
0075 R 03A8
0076 E7E0
0077 X 0009
0078 R 03A7
0079 6600
007A 6600
007B F1D0
007C R 03AA
007D 76D0
007E R 005F
007F 652F
0080 E626
0081 R 03D4
0082 E700
0083 R 02EF
0084 80F6
0085 R 0399
0086 ED20
0087 A 0004

```

```

U:GET
AST21 NO COMP. POINTS
U.CDT IS CUR.POINT<CDATE
(DON'T UPDATE ON =)
NO,CHECK DELAY COUNT
YES,NEW CANDIDATE FOR PEAK
AND ITS TIME
CFIRM RESET DELAY COUNTER
TEMP CURRENT POINT TIME
TEMP+1
E:DIF
TEMP ONSET TIME SHOULD
NEVER = 0
T1
UAST2+2 NEXT POINT IF TIME<T1
REASN SET REASN=1
FAIL
CFIRM INC. CONFIRMATION
4

```

119	0088	E126							
	0089	R 0399	SUM,2,6	CFIRM					
120	008A	7D22	TRRB,2,2	AST23				IF CONFIRMATION IS MADE	
	008R	R 0072							
121	008C	E700	BRU	PVALC				GO CHECK PEAK VALIDITY	
	008D	R 00DF							
122	008E	E7E0	AST21 BLM,14	E:DIF				NO POINTS AVAILABLE	
	008F	X 0009							
123	0090	X 000A	DFC	U.CTM				HOW LONG SINCE ONSET?	
124	0091	6600	NOP					ONSET NEVER=0 HERE	
125	0092	6600	NOP					?	
126	0093	E100	SUM,13	T1					
	0094	R 03AA							
127	0095	76D0	TBRB,13,0	UPDIU				NOT > T1 YET.	
	0096	R 0014							
128	0097	E700	BRU	SREAL					
	0098	R 007F							

```

130 *****
131 * WHEN U.AST=3, WE HAVE FOUND AN ONSET IN THE RETARD DIRECTION.
132 * PEAK IS OBTAINED THE SLOPE BETWEEN ONSET AND A CANDIDATE
133 * PEAK VALUE EXCEEDS THREE SUCCESSIVE COMPUTED SLOPES FROM
134 * ONSET TO THE THEN CURRENT POINT.
135 * IF NO PEAK IS FOUND WITHIN T1 SECONDS, THE ONSET AND SIGNAL
136 * ARE REJECTED.
137 UAST3 TRR,5,6
138 LLS,5,1
139 BLM,14
140 BRU
141 AST21
142 STM,12
143 TEMP
144 STM,13
145 TEMP+1
146 * CUR. SLOPE=(CUR. FAZ-U.OFZ)/(CUR. TIME-U.OTM)
147 BLM,14
148 E:DIF
149 DFC
150 NOP
151 NOP
152 TRR,3,11
153 SUM,3,6
154 ZRR,2
155 DVR,2,13
156 * FIND THE MAXIMUM SLOPE AS (SLOPE)
157 * COMPARE CURRENT SLOPE FROM ONSET TO CURRENT MAX.
158 TRR,2,3
159 SUM,2,6
160 SLOPE
161 TBRB,2,0
162 AST35
163 NOT NEW MAX
164 * WE HAVE A NEW MAXIMUM SLOPE
165 STM,3,6
166 SLOPE
167 BRU
168 AST32
169 * WE'LL CALL IT A PEAK WHEN THE CURRENT SLOPE RELAXES
170 * BY 25 PERCENT FROM THE MAX.
171 AST35 LDM,15,6
172 SLOPE
173 LDI,2
174 #6000
175 =.75
176 MPR,14,2
177 LAD,14,1
178 SUR,3,14
179 TBRB,3,0
180 AST33
181 BRU IF CONFIRMING PEAK

```

```

0099 6056
009A 2D51
009B E7E0
009C X 0008
009D E700
009E R 008E
009F E6C0
00A0 R 03A7
00A1 E6D0
00A2 R 03A8
00A3 E7E0
00A4 X 0009
00A5 R 03A7
00A6 6600
00A7 6600
00A8 6D3B
00A9 6136
00AA X 001A
00AB 6C22
00AC 212D
00AD 6023
00AE E126
00AF R 038B
00B0 7620
00B1 R 00B6
00B2 E636
00B3 R 038B
00B4 E700
00B5 R 00BF
00B6 E5F6
00B7 R 038B
00B8 ED20
00B9 A 6000
00BA 20E2
00BB 2EE1
00BC 693E
00BD 7630

```

LOOKING FOR RETARD DIRECTION PEAK.....

MODCOMP ASSEMBLY

LINE	CODE	OPER	PARAMETER	VALUE	UNIT	DESCRIPTION
168	00RE	R	00D7			
169	008F	R	E686			
170	00C0	R	0361			
	00C1	R	E500			
171	00C2	R	03A7			
	00C3	R	E6D5			
172	00C4	R	036F			
	00C5	R	E5D0			
173	00C6	R	03A8			
	00C7	R	E6D5			
174	00C8	R	0370			
175	00C9	R	6C0D			
	00CA	R	E6D6			
176	00CB	R	0399			
	00CC	R	E7E0			
177	00CD	X	0009			
178	00CE	R	03A7			
179	00CF	R	6600			
180	00D0	R	6600			
	00D1	R	E1D0			
	00D2	R	03AA			
181	00D3	R	76D0			
182	00D4	R	0098			
	00D5	R	E700			
	00D6	R	007F			
183	00D7	R	80F6			
184	00D8	R	0399			
	00D9	R	E536			
185	00DA	R	0399			
	00DB	R	E930			
	00DC	A	0004			
186	00DD	R	7D33			
	00DE	R	00CC			

* NOT YET CONFIRMING PEAK SAVE NEW CANDIDATE
 AST32 STM,11,6 U.CDT NEW PEAK CANDIDATE

LDM,13 TEMP
 STM,13,5 U.CDM ITS TIME
 LDM,13 TEMP+1
 STM,13,5 U.CDM+1

ZRR,13 CFIRM
 STM,13,6 RESET CFIRM=0

AST34 BLM,14 E:DIF

DFC TEMP
 NOP
 NOP

SUM,13 T1

TBRB,13,0 UAST3+2 NEXT POINT IF TIME<T1

BRU SREAL

AST33 ABMM,15,6 CFIRM INC CFIRM COUNTER

LDM,3,6 CFIRM

SUI,3 4

TRRB,3,3 AST34 IF CFIRM NOT=3, CONTINUE

PEAK VALIDITY CHECKER.....

MODCOMP ASSEMBLY

 * NOW WE HAVE BOTH AN ONSET AND A PEAK. LET'S FIND OUT IF THE
 * PEAK IS VALID. IF IT IS, WE'LL START LOOKING FOR RECOVERY
 * TO BEGIN, OTHERWISE WE'LL DROP THE WHOLE THING AND START
 * LOOKING FOR ANOTHER ONSET.
 PVALC ZRR,2 CFIRM RESET CFIRM

188	00DF	6C22	STM,2,6	CFIRM	RESET CFIRM
189	00E0	E626	LDM,3	U.BGF	SET:
190	00E1	R 0399	STM,3,6	U.FAC	U.FAC=U.BGF
191	00E2	E530	LDM,3,6	U.CLS	
192	00E3	X 001C	SUI,3	3	
193	00E4	E636	TRRB,3,3	PVAL1	CLASS NOT 3, BRU
194	00E5	X 0005	LDI,3	5	CLASS=3
195	00E6	E536	STM,3,6	U.AST	SET U.AST=5
196	00E7	X 000B	LDM,3,6	U.ODI	U.DIU=U.ODI
197	00E8	E930	STM,3,6	U.CDT	REAL PEAK VALUES
198	00E9	A 0003	LDM,3,6	W.PFZ	W.PFZ=U.CDT
199	00EA	7D33	LDM,3,5	U.CDM	W.PTM=U.CDM
200	00EB	R 0103	ABR,5,15		
201	00EC	E30	LDM,3,5	U.CDM	
202	00ED	A 0005	STM,3,5	W.PTM	
203	00EE	E636	BRU	WFA2	MOVE ALONG
204	00EF	X 0002			
205	00F0	E536			
206	00F1	X 001B			
207	00F2	E636			
208	00F3	X 0004			
209	00F4	E536			
210	00F5	R 0361			
211	00F6	E636			
212	00F7	X 000C			
213	00F8	E535			
214	00F9	R 036F			
215	00FA	E635			
216	00FB	X 000D			
217	00FC	605F			
218	00FD	E535			
219	00FE	R 036F			
220	00FF	E635			
221	0100	X 000D			
222	0101	E700			
223	0102	R 0053			
224	0103	E535			
225	0104	R 036F			

 * CLASS NOT 3, WHICH IMPLIES NORMAL ADVANCE OR RETARD
 * SIGNAL. WE WILL NOW COMPUTE THE OFFSET AND ONSET
 * RATE.
 * WHEN COMPUTING THE OFFSET, TAKE INTO ACCOUNT THE DIURNAL
 * AT ONSET. TO DO THIS, USE THE FOLLOWING:
 * W.OST=U.CDT-(U.CDM-U.OTM)*U.ODI-U.OFZ
 * W.ONR=W.OST/(U.CDM-U.OTM)
 PVAL1 LDM,3,5 U.CDM

PEAK VALIDITY CHECKER.....

MODCOMP ASSEMBLY

220	0105	E630	STM,3	TEMP	
	0106	03A7			
221	0107	E535	LDM,3,5	U.CDM+1	
	0108	0370			
222	0109	E630	STM,3	TEMP+1	
	010A	03A8			
223	010B	E7E0	BLM,14	E:DIF	
	010C	0009			
224	010D	03A7	DFC	TEMP	
225	010E	6600	NOP		
226	010F	6600	NOP		
227	0110	E6D0	STM,13	TEMP2	TEMP=(PEAK TIME-ONSET TIME)
	0111	03A9			
228	0112	E536	LDM,3,6	U.CDI	
	0113	001B			
229	0114	20C3	MPR,12,3		O.DIU*(U.CDM-U.OTM)
230	0115	ED30	LDI,3	480	SCALE
	0116	A 01E0			
231	0117	21C3	DVR,12,3		
232	0118	E536	LDM,3,6	U.CDT	
233	011A	693D	SUR,3,13	U.OFZ	
234	011B	E136	SUM,3,6		
	011C	001A			
235	011D	7630	TBRB,3,0	\$+3	
	011E	0120			
236	011F	F702	HOP,\$+2		
237	0120	6F33	TTR,3,3		
238	0121	E636	STM,3,6	W.OST	
	0122	X 000F			
239	0123	EDC0	LDI,12	60	SCALE
	0124	A 003C			
240	0125	202C	MPR,2,12		
241	0126	A120	DVM,2	TEMP2	
242	0127	R 03A9			
	0128	E636	STM,3,6	W.ONR	
	0129	X 0010			

* NOW THAT WE HAVE THE OFFSET AND ONSET RATE, WE'LL DETERMINE
 * WHETHER THEY MEET REPORTING CRITERIA:
 * CLASS 1 SIGNAL:
 * W.OST>PCFF
 * W.ONR>PONHI IF TRACK RATE>4
 * >PONMD IF TRACK RATE=2, 3, OR 4
 * >PCNLO IF TRACK RATE=0 OR 1
 * CLASS 2 SIGNAL (ADVANCE ONLY):
 * W.OST>SOFF
 * W.ONR>SONRT
 * LDM,4,6 U.TRK
 * SBR,4,14

243	012A	E546			
244	012B	X 001D			
245	012C	614E			

PEAK VALIDITY CHECKER.....

MODCOMP ASSEMBLY

255	012D	R	7640	TBRB,4,0	LOTRK	CHECK HI TRACK RATE (CLASS1)
	012E	P	0199			
256	012F	E940	E940	SUI,4	3	
257	0130	A	0003			
	0131	R	7640	TBRB,4,0	MDTRK	
258	0132	R	019F	SUM,3	PONHI	
	0133	R	E130			
259	0134	R	03AF	TBRB,3,0	CKSEC	
	0135	R	7630			
260	0136	R	016C	CKPOF	LDM,2,6	CHECK OFFSET RATE (CLASS1)
	0137	X	E526			
261	0138	X	000F	SUM,2	POFF	
	0139	R	E120			
262	013A	R	03AE	TBRB,2,0	CKSEC	
	013B	R	7620			
263	013C	R	016C	LRR,2,15		
264	013D	X	652F	STM,2,6	U.CLS	PASS, SET CLASS=1
	013E	X	E626			
	013F	X	000B			

* SIGNAL PASSES CLASS 1 CRITERIA.....

265	0140	R	E526	PASS	LDM,2,6	U.CDT
266	0141	R	0361			
267	0142	X	E626	STM,2,6	W.PFZ	
268	0143	X	000C	STM,2,6	W.VPK	
	0144	X	E626			
	0145	X	0013	LDM,2,5	U.CDM	
269	0146	R	E525			
	0147	R	036F	STM,2,5	W.PTM	
270	0148	X	E625			
	0149	X	000D	STM,2,5	W.VTM	
271	014A	X	E625			
	014B	X	0014			
272	014C		605F	ABR,5,15		
273	014D		E525	LDM,2,5	U.CDM	
	014E	R	036F			
274	014F	X	E625	STM,2,5	W.PTM	
	0150	X	000D			
275	0151	X	E625	STM,2,5	W.VTM	
	0152	X	0014			
276	0153	X	E526	LDM,2,6	U.AMP	
	0154	X	0025			
277	0155	X	E626	STM,2,6	W.PAM	
	0156	X	000E			
278	0157	X	E626	STM,2,6	W.PAM ONLY	OR IERIA:(U.CDM-U.OTM)ET TIME)***** +
279	0158	X	000E			
280	0159	X	E626	STM,2,6	W.PAM ONLY	OR IERIA:(U.CDM-U.OTM)ET TIME)***** +
	015A	X	000E			
281	015B	0	0000			CURRENT DIURNAL TO ONSET VALUE.
	015C	A	0000			

PEAK VALIDITY CHECKER.....

MODCOMP ASSEMBLY

282	015D	E526	LDM,2,6	U.ODI
	015E	001B		
283	015F	E626	STM,2,6	U.DIU
	0160	0004		
284	0161	E526	LDM,2,6	U.AST
	0162	0002		
285	0163	612F	SBR,2,15	
286	0164	E626	STM,2,6	U.ODR
	0165	001E		
287	0166	ED20	LDI,2	5
	0167	0005		
288	0168	E626	STM,2,6	U.AST
	0169	0002		
289	016A	E700	BRU	WFA2
	016B	R 0053		
290	016C	E546	CKSEC LDM,4,6	U.AST
	016D	0002		
291	016E	714E	SBRB,4,14	SREA2
	016F	A 0000		
292	0170	E526	LDM,2,6	W.OST
	0171	X 000F		
293	0172	E120	SUM,2	SOFF
	0173	R 0382		
294	0174	7620	TRPB,2,0	SREA3
	0175	R 018E		
295	0176	E526	LDM,2,6	W.ONR
	0177	X 0010		
296	0178	E120	SUM,2	
	0179	A 0000		
297	017A	E526	LDM,2,6	W.ONR
	017B	X 0010		
298	017C	E526	LDM,2,6	W.ONR
	017D	X 0010		
299	017E	E120	SUM,2	SONRT
	017F	R 0383		
300	0180	7620	TBRB,2,0	SREA4
	0181	R 0194		
301	0182	E120	SUM,2	SONRT
	0183	R 0383		
302	0184	0000	PASS, SET CLASS = 2	
	0185	A 0000		
303	0186	E700	BRU	
	0187	A 0000		
304	0188	0000	PASS, SET CLASS = 2	
	0189	A 0000		
305	018A	0000	REASN	
	018B	A 0000		
306	018C	E700	BRU	FAIL
	018D	R 02EF		
307	018E	ED20	SREA3 LDI,2	3

SET U.AST=5
CONTINUE

NO CLASS 2 ON RETARD SIGNAL

CK. CLASS 2 OFFSET CRITERIA

CK. CLASS 2 OFFSET CRITERIA).*****

CK. CLASS 2 ONSET RATE CRITERIA

CK. CLASS 2 ONSET RATE CRITERIA*** +

CK. CLASS 2 OFFSET CRITERIA).*****


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320 *****
321 * WHEN U.AST=5, WE HAVE A VALID PEAK TO OUR LITTLE SIGNAL.
322 * THE NEXT STEP IS TO LOOK FOR IT TO BEGIN RECOVERING.
323 * AT PEAK WE RESET THE CURRENT DIURNAL ESTIMATE TO THE
324 * ONSET ESTIMATE. AT THE SAME TIME WE SET U.FAC TO THE
325 * BIG VALUE. THIS REDUCES THE TIME CONSTANT IN THE
326 * DIGITAL FILTER FOR DIURNAL UPDATES. NOW, WHEN THE
327 * CURRENT DIURNAL SLIPS AROUND PAST THE ONSET VALUE
328 * BY ALPHA, RECOVERY HAS BEGUN.
329 * ONLY ONE CATCH, THIS MUST OCCUR WITHIN T2 SECONDS OF
330 * ONSET.
331 UAST5 BLM,15 UAST4 GET RID OF COMPRESSED DATA.
332 LDM,2,6 U.DIU IS.....
333 SUM,2,6 U.OOI (U.DIU-U.OOI)
334 SUM,2 ALPHA -ALPHA>0?
335 TBRB,2,0 AST51 BRU IF NOT
336 * RECOVERY HAS BEGUN
337 LDI,2 6
338 STM,2,6 U.AST SET U.AST=6
339 LDM,2,6 U.ODR
340 SBRB,2,15 AST52
341 * IF DIRECTION=1 (ADVANCE SIGNAL),
342 * SET MINIMUM RECOVERY (RCMIN)= 1/2 OFFSET
343 * IF DIRECTION=2 (RETARD SIGNAL)
344 * SET RCMIN=1/2 (CYCLE-OFFSET)
345 * WHERE CYCLE = 1/FREQUENCY
346 * OTHERWISE,
347 * SET RCMIN = 7 MICROSECONDS
348 LDM,3,6 W.OST
349 RAS,3,1
350 BRU AST53+2
351 AST52 SBRB,2,15 AST53
352 * CCNVERT FREQUENCY TO BINARY
353 TRR,7,6
354 LLS,7,1
355 LDM,5,7 U.FRQ
356 SUI,5 #3030

```

* WHEN U.AST=6, RECOVERY HAS BGUN. THE MOST IMPURTANT
 * STEP FOLLOWS: DETERMINING A VALID RECOVERY.
 * IF RECOVERY IS CONSIDERED COMPLETE (U.DIU RETURNS TO
 * WITHIN BETA OF U.OOI) IN LESS THAN T3, THE SIGNAL
 * IS REJECTED. IF WE LAST T3 SECONDS INTO THE SIGNAL
 * WITHOUT COMPLETION, WE CHECK THE PROGRESS OF THE
 * RECOVERY. IF WE RECOVER BY PCMIN FROM PEAK AND THE
 * RECOVERY RATE IS LESS THAN THE ONSET RATE, RECOVERY
 * IS VALID AND THE PEAK IS REPORTABLE.

U.AST6 TBR,5,6
 LLS,5,1
 LDM,2,6
 SUM,2,6
 SUM,2
 TBRP,2,0
 BRU
 BRU
 U.DIU
 U.OOI
 BETA
 \$\$4
 AST61
 IS.....
 (U.OIU-U.OOI)
 -BETA>0?
 BRU IF NOT

* IF AN ADVANCE SIGNAL WITH THE ONSET RATE PASSING
 * PRIMARY CRITERIA FAILS HERE, IT MAY WELL BE
 * A CCC (CLASS=3) TYPE SIGNAL.

LDM,2,6 U.OOR
 SRRB,2,15 SREF6 NOT ADVANCE
 LDM,2,6 W.CMP
 LDM,4,6 U.TFK
 SBP,4,14
 TBRP,4,0 TLC6
 SUI,4 3
 TBRP,4,0 TMD6
 SUM,2 PONHT
 TBRP,2,0 SREF6
 HOP,MAYRE
 SUM,2 PONLO
 TBRP,2,0 SREF6
 HOP,MAYRE

390 01F0 6D56
 391 01F1 2D51
 392 01F2 E526
 393 01F3 X 0004
 394 01F4 F126
 395 01F5 X 001R
 396 01F6 F120
 397 01F7 R 03R7
 398 01F8 7620
 399 01F9 R 01FC
 400 01FA E700
 401 01FB R 0243
 402
 403
 404
 405
 406
 407
 408
 409
 410 01FC F526
 411 01FD X 001E
 412 01FE 712F
 413 01FF P 0230
 414 0200 F526
 415 0201 X 0010
 416 0202 F546
 417 0203 X 001D
 418 0204 E14F
 419 0205 7640
 420 0206 R 0210
 421 0207 F940
 422 0208 A 0003
 423 0209 7640
 424 020A R 0215
 425 020R E120
 426 020C R 03AF
 427 020Q 7620
 428 020E R 0230
 429 020F F70A
 430 0210 F120
 431 0211 P 03R1
 432 0212 7620
 433 0213 P 0230
 434 0214 F705

LOOKING FOR VALID RECOVERY.....

MODCOMP ASSEMBLY

424	0215	F120	TMID6	SUM,2	PCNMD	
425	0216 R	0380		TBR,2,0	SRA6	
426	0217	7620				
427	0218 R	0230				
428	0219	652E				
429	021A	E626				
430	021B X	0002				
431	021C	E526				
432	021D P	0337				
433	021E R	E626				
434	0220	0361				
435	0221 X	E526				
436	0222	000C				
437	0223 X	F626				
438	0224	0011				
439	0225 X	F526				
440	0226	000E				
441	0227 X	E626				
442	0228	0027				
443	0229 A	ED10				
444	022A	FFFE				
445	022B R	F525				
446	022C	0345				
447	022D X	F625				
448	022E	000A				
449	022F X	E525				
450	0230	000D				
451	0231 X	E625				
452	0232	0012				
453	0233	605F				
454	0234 R	701F				
455	0235	022A				
456	0236 A	ED20				
457	0237	0003				
458	0238 X	E626				
459	0239	000R				
460	023A X	E626				
461	023B	001E				
462	023C P	E700				
463	023D	0053				
464	023E A	ED20				
465	023F	0006				
466	0240 P	E626				
467	0241	03D4				
468	0242 R	E700				
469	0243	02EF				
470	0244	E7E0				

* MAYBE WE HAVE A CLASS 3 SIGNAL HERE.....

MAYBE LRP,2,14
STM,2,6 U.AST RESET U.AST=2

* RESET IPEAKI VALUES TO FLOW PEAKI AND ESTABLISH
* A NEW PEAK CANDIDATE;
LDM,2,6 U.LFZ

STM,2,6 U.CDT

LDM,2,6 W.PF7

STM,2,6 W.LPK

LDM,2,6 W.PAM

STM,2,6 W.LAM

LDI,1 -2

LPKST LDM,2,5 U.LTM

STM,2,5 U.CTM

LDM,2,5 W.PTM

STM,2,5 W.LTM

ABR,5,15

ABPR,1,15 LPKST

LDI,2 3

STM,2,6 U.CLS

STM,2,6 U.OOR

BRU WFA2

SREA6 LDI,2 6

STM,2,6 PEASN

BRU FAIL

AST61 FLN,14

E:DF

YES....

SET REASON=6

CHECK TIME SINCE ONSET

LOOKING FOR VALID RECOVERY.....

MDCOMP ASSEMBLY

482	0272	7022	TRRB,2,2	\$+4			
483	0273 R	0276	BRU	AST64			
484	0274 R	E700					
	0275 R	029C	SUM,11,6	W.PFZ			R11=CUR. PEAK PHASE
485	0276	E1B6					
	0277 X	000C	LDI,2	W.PTM			
	0278	ED20					
	0279 X	000D					
486	027A	6825	ADR,2,5				
487	027B	E620	STM,2	\$+4			
	027C R	027F					
488	027D	E7E0	BLM,14	E:COT			
	027E X	001F					
489	027F A	0000	DFC	0			
490	0280	E6C0	STM,12	U.DTM			
	0281 X	0026					
491	0282	657F	LBR,7,15				
492	0283	E6D7	STM,13,7	U.DTM			
	0284 X	0026					
493	0285	6D76	TRR,7,6				SAVE R6
494	0286	ED60	LDI,6	14			
	0287 A	000E					
495	0288	E7E0	BLM,14	E:DIF			
	0289 X	0009					
496	028A X	000A	DFC	U.CTM			R13=TIME FROM PEAK
497	028B	6600	NOP				
498	028C	6600	NOP				
499	028D	6D67	TRR,6,7				RESTORE R7
500	028E	EDE0	LDI,14	480			SCALE
	028F A	01E0					
501	0290	20AE	MPR,10,14				
502	0291	21AD	DVR,10,13				
503	0292	E1B6	SUM,11,6	W.CNR			R10=RECOVERY RATE IS RECT<W.ONR?
	0293 X	0010					
504	0294	7680	TBRB,11,0	AST64			
	0295 R	029C					
505	0296	ED20	LDI,2	7			NO, FAILED AGAIN
	0297 A	0007	STM,2,6	REASN			SET REASON=7
506	0298	F626	BRU				
	0299 R	03D4					
507	029A	E700		FAIL			
	029B R	02EF					
508	029C	E7F0	AST64 BLM,15	U:RPI			GENERATE MSG. ON SIGNAL
	029D X	0006					
509	029E	E7F0	AST62 BLM,15	UA5T4			
	029F R	02D8					
510	02A0	E700	BRU	UPDIU			
	02A1 R	0014					

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512 ***** WE HAVE A VALID SIGNAL AND ARE WAITING OUT
513 * WHEN U.AST=7 WE HAVE A VALID SIGNAL AND ARE WAITING OUT
514 * THE END OF THE SIGNAL.
515 * FOR ALL SIGNALS, THE CURRENT DIURNAL (U.DIU) MUST RETURN
516 * TO WITHIN BETA OF THE ONSET DIURNAL (U.ODI). ADVANCE
517 * SIGNALS (U.ODR=1) MUST HAVE THE PHASE RETURN TO WITHIN
518 * TREC OF THE ONSET PHASE.
519 * IF THE SIGNAL DOESN'T END OF IT'S OWN VOLITION WITHIN
520 * T4 SECONDS, THE PROCESSING IS AUTOMATICALLY TERMINATED.
521 UAST7 LDM,2,6 U.DIU IS.....
522 SUM,2,6 U.ODI (U.DIU-U.ODI)
523 SUM,2 RETA -BETA>0?
524 TBRB,2,0 AST71 NO, GOOD END MAYBE
525 BLM,15 SCMPD YES, KEEP LOOKING
526 AST72 BLM,14 E:DIF HAVE WE WAITED
527 DFC U.CTM LONG ENOUGH?
528 NOP
529 NOP
530 SUM,13 T4
531 TBRB,13,0 UPDIU NO, STILL WAITING
532 DBMM,1,6 U.END FLAG TIME-OUT ENDING
533 BLM,15 U:RP2 END OF MESSAGE SIGNAL
534 BRU FAIL
535 AST71 LDM,2,6 U.ODR
536 SBRB,2,15 $+3
537 HOP,AST74
538 SBRB,2,15 AST73
539 AST74 LDM,10,6 U.C#Z
540 TBM,15,6 U.ODR,$+5
541 ADM,10,6 CYCLE
542 SUM,10,6 U.LFZ IS.....
E526
02A2 X 0004
02A3 X 0004
02A4 E126
02A5 X 001B
02A6 E120
02A7 R 03B7
02A8 7620
02A9 R 02B8
02AA E7F0
02AB R 032F
02AC E7E0
02AD X 0009
02AE X 000A
02AF 6600
02B0 6600
02B1 E1D0
02B2 R 03AD
02B3 76D0
02B4 R 0014
02B5 8216
02B6 X 0019
02B7 E7F0
02B8 X 0007
02B9 E700
02BA R 02EF
02BB E526
02BC X 001E
02BD 712F
02BE R 02C0
02BF F703
02C0 712F
02C1 R 02D1
02C2 E5A6
02C3 X 001A
02C4 86F6
02C5 X 001E
02C6 R 02C9
02C7 E0A6
02C8 R 03C6
02C9 E1A6
02CA R 0337

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MODCOMP ASSEMBLY LOOKING FOR END OF SIGNAL.....

543	02CB	E1A0	SUM,10	TREC	(U.OFZ-CUR.PHASE)
	02CC	03B5			
544	02CD	76A0	TBPB,10,0	AST73	-TREC<0? BRU IF YES
	02CE	02D1			
545	02CF	E700	BRU	AST72	NO, CHECK NEXT POINT
	02D0	02AC			
546	02D1	6C22	AST73 ZRR,2		
547	02D2	E620	STM,2	U.END	
	02D3	0019			
548	02D4	E7F0	BLM,15	U:RP2	END MESSAGE GENERATOR
	02D5	0007			

POSSIBLE VALLEY LOCATER.....

MODCOMP ASSEMBLY

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550 02D6 E700 BRU FAIL
02D7 R 02EF

551 *****
552 * UAST4 IS A SUBROUTINE TO LOOK FOR A POSSIBLE
553 * VALLEY IN A CLASS 3 WAVE.
554 * THE MAXIMUM VALUE (LOW POINT IN THE WAVE) IS
555 * SAVED. IF WE LATER DECIDE WE HAVE A CLASS 3
556 * SIGNAL, WE'RE SET.
557 * CALLING SEQUENCE:
558 * RLM,15 UAST4
559 * (CHAN IN R6)
560 UAST4 TRR,5,6
561 02D9 2D51 LLS,5,1
562 02DA E7E0 AST41 BLM,14 U:GET

563 02DB 0008 BRX,15 W.VPK
564 02DC FFOF NOP
565 02DE E526 LDM,2,6
566 02DF X 0013 SUR,2,11 $+4
567 02E0 692B TBRB,2,0 UAST41
568 02E1 7620 BRU
569 02E2 R 02E5 STM,11,6 W.VPK
570 02E3 E700 STM,12,5 W.VTM
571 02E4 R 02D8 ABR,5,15
572 02E5 E686 STM,13,5 W.VTM
573 02E6 X 0013
574 02E7 E6C5 SBP,5,15
575 02E8 X 0014 BRU
576 02E9 605F
577 02EA E6D5
578 02EB X 0014
579 02EC 615F
580 02ED E700 UAST41
581 02EE R 02D8

```


FAILURE MESSAGE GENERATOR.....

MODCOMP ASSEMBLY

```

603 *****
604 * WRITE.. IF BIT 10 OF THE SWITCH REGISTER IS SET,
605 * THE REASON FOR ALL SIGNAL REJECTIONS ARE DISPLAYED
606 * ON THE ASR 35.
607 WRITE LDM,2,6 REASN
      E526
608 TRRR,2,2 $+4 NORMAL END
      03D4
609 BRU FAIL1
      0317 7D22
      0318 031R
      0319 E700
      031A R 030E
      031B E820
      031C A 5230
      031D E620
      031E R 03E9
      031F 0956
      0320 E850
      0321 A 4120
      0322 E650
      0323 R 03E8
      0324 E7E0
      0325 X 0024
      0326 R 03E2
      0327 6CEE
      0328 E6E0
      0329 X 0021
      032A E7E0
      032B X 0023
      032C R 03E2
      032D E700
      032E R 030E
      0316 R
      0317 R
      0318 R
      0319 R
      031A R
      031B A
      031C A
      031D R
      031E R
      031F R
      0320 A
      0321 A
      0322 R
      0323 R
      0324 X
      0326 R
      0327 R
      0328 X
      0329 X
      032A X
      032B X
      032C R
      032D R
      032E R
      RRRR,2,2 $+4
      BRU FAIL1
      ADI,2 #5230
      STM,2 FLMMSG+7
      MBL,5,6
      ADI,5 #4120
      STM,5 FLMMSG+6
      BLM,14 E:FRT
      DFC FLMMSG
      ZRR,14 E.IOF
      STM,14
      BLM,14 E:035
      DFC FLMMSG
      BRU FAIL1
      SEND TO ASR 35.
      RETURN
      GET CURRENT TIME
  
```

```

623
624
625
626
627
628
629
630
631
632
633

032F      6D56
0330      2D51
0331      E7E0
0332      X 0008
0333      FF0F
0334      6600
0335      E700
0336      R 0331

          6D56
          2D51
          E7E0
          BRX,15
          NOP
          BRU
          $-4

          U:GET

          D.P. CHANNEL POINTER

          RETURN WHEN NONE LEFT

          LOOP THRU ALL POINTS

*****
* SCMPD IS A ROUTINE WHICH PASSES OVER COMPRESSED DATA
* POINTS DURING THOSE PARTS OF WAVEFORM ANALYSIS THAT
* DON'T USE THEM. THE LAST ONE AND ITS TIME ARE HELD
* IN U.LFZ AND U.LTM
SCMPD TRR,5,6
      LLS,5,1
      BLM,14
      BRX,15
      NOP
      BRU
      $-4
      U:GET
      D.P. CHANNEL POINTER
      RETURN WHEN NONE LEFT
      LOOP THRU ALL POINTS
*****

```



```

635 *****
636 * SINCE WE DON'T KEEP EVERY POINT, WE'LL SAVE THE LAST
637 * VALUE AND ITS TIME HERE.
638 U.LFZ RES      14,0
639 U.LTM RES      28,0
640 *****
641 * WHILE SEARCHING FOR THE PEAK, A LIKELY CANDIDATE MUST BE KEPT
642 * WITH ITS TIME. ALSO, FOR RETARD SIGNALS, THE SLOPE TO THE
643 * POSSIBLE PEAK MUST BE RETAINED
644 U.CDT RES      14,0      PEAK CANDIDATE
645 U.CDM RES      28,0      ITS TIME
646 SLOPE RES      14,0      SLOPE
647 CFIRM RES      14,0      PEAK CONFIRMATION COUNTER
648 *****
649 TEMP RES      2,0      WORKING STORAGE
650 TEMP2 RES      1,0
651 *****
652 * WAVEFORM TIMING PARAMETERS
653 * T1 MAX ALLOWED TIME FROM ONSET TO PEAK
654 * T2 MAX ALLOWED TIME TO BEGIN RECOVERY
655 * T3 MIN ALLOWED TIME FROM ONSET TO FULL RECOVERY
656 * T4 MAX DURATION OF SIGNAL, ONSET TO FULL RECOVERY
657 T1 DFC          600      (10 MINUTES)
658 T2 DFC          1200     (20 MINUTES)
659 T3 DFC          600      (10 MINUTES)
660 T4 DFC          3600     (60 MINUTES)
661 *****
662 * WAVEFORM EVALUATION CRITERIA
663 * POFF PRIMARY OFFSET CRITERIA
664 * PONHI PRIMARY ONSET RATE, HI TRACK RATES
665 * PONMD PRIMARY ONSET RATE, MED TRACK RATES
666 * PONLO PRIMARY ONSET RATE, LO TRACK RATES
667 * SOFF SECONDARY OFFSET CRITERIA
668 * SONRT SECONDARY ONSET RATE CRITERIA, ALL TRACK RATES
669 * LOPEK PERMISSIBLE OFFSET FOR FIRST OF MULTIPLE PEAKS
670 * ( ADVANCE DIRECTION ONLY )
671 * RCMIN MIN RECOVERY FROM PEAK FOR VALID RECOVERY
672 * TREC POINT TO WHICH PHASE MUST APPROACH ONSET TO END RECOVERY
673 * ALPHA DIFFERENCE TWIXT U.DIU AND U.ODI TO BEGIN RECOVERY
674 * BETA DIFFERENCE TWIXT U.DIU AND U.ODI TO END RECOVERY
675 * CYCLE IS THE MICROSECOND LENGTH OF ONE CYCLE (RET SIGS ONLY)
676 PUFF DFC       160      10 MICROSEC
677 PONHI DFC      #140     20 MICROSEC/MIN
678 PONMD DFC      #0A0     10 MICROSEC/MIN
679 PONLO DFC      #060     6 MICROSEC/MIN
680 SOFF DFC       32      2 MICROSEC
681 SONRT DFC      #030     3 MICROSEC/MIN
682 LOPEK DFC      80      5 MICROSEC
683 TREC DFC       48      3 MICROSEC
684 ALPHA DFC      38      .3 MICROSEC/MIN

```

```

685      03B7  A  001A      BETA  DFC      26      .02 MICROSEC/MIN
686      03R8  A  0030      RCMIN RES  14,48
687      03C6  R  03C6      U-CCL EQU  $
688      03C6  A  0000      CYCLE RES  14,0
689      *****
690      * FAILURE MESSAGE:
691      * FORMAT: 1123 123456 C RNI
692      * WHERE:
693      * C IS THE CHANNEL (A,B,C,,,,,N)
694      * R INDICATES SIGNAL REJECTION
695      * N IS A CODED VALUE (1,2,.....7)
696      * 1 FOR EXCESSIVE TIME TO PEAK
697      * 2 RETARD SIGNAL FAILS OFFSET CRITERIA
698      * 3 ADV SIGNAL FAILS SECONDARY OFFSET CRITERIA
699      * 4 ADV SIGNAL FAILS SECONDARY ONSET RATE CRITERIA
700      * 5 EXCESSIVE TIME TO BEGIN RECOVERY
701      * 6 SLOPE RECOVERY TOO SOON AFTER ONSET
702      * 7 RECOVERY RATE GREATER THAN ONSET RATE
703      REASN RES  14,0
704      FLMMSG DFC  1123 123456 C RNI, #0D0A, #0000

```

```

705      *****

```

```

TOTAL ASSEMBLY ERRORS = 7      SIZE = 1004      U$DAT
*****WARNING*** **0**      GM

```

```

END

```

```

1 0 0000 0000 U$DAT
0001 A 0000
2 INT U.WRK,U.TRK,U.GAN,U.BLL,U.IHB
3 INT U.CST,U.DTU,U.LTF,U.BGF,U.FAC
4 INT U.CMP,U.CPC,U.CTM,U.OTM,U.DTM
5 INT U.CPP,U.SLP,U.RTY,U.AST,U.CGN
6 INT U.LST,U.AMP,U.OTB,U.OFZ,U.ODR
7 INT U.ODI,U.OAM,U.END,U.CLS,H.PFZ
8 INT W.PTM,W.PAM,W.OST,W.ONR,W.LPK
9 INT W.LTM,W.VPK,W.VTM,W.RFZ,W.RTM
10 INT W.RAM,U.CYC,U.LSP,U.OSG,W.LAM
11 W.VAM
12 *****
13 *** CAT NO. 400100-001 *****
14 *****
15 * U$DAT IS THE COMMON DATA STORAGE AREA FOR THE
16 * U-DATA ANALYSIS PROGRAMS.

```

MODCOMP ASSEMBLY

PARAMETER SETTINGS.....

```

18 * U. TRK IS THE CHANNEL TRACKING RATE
19 *   (BETWEEN 0 AND 9)
20 * U. GAN IS THE CHANNEL GAIN SETTING
21 *   (BETWEEN 0 AND 24)
22 * U. BLL IS THE CHANNEL BLANKING LEVEL
23 *   (BETWEEN 0 AND 63)
24 * U. TRK RES      0002 A 0005
25 * U. GAN RES     0010 A 000C
26 * U. BLL RES    001E A 0028
27

```

```

14,5
14,12
14,40

```

```

29 *****
30 * U.DIU IS THE PROGRAM'S ESTIMATE OF THE SLOPE IN *****
31 * THE LONG-TERM TREND OF THE DATA. ACTUALLY, *****
32 * IT IS THE RESULT OF A DIGITAL FILTER APPLIED *****
33 * TO THE LEAST SQUARES SLOPES..... *****
34 * WHERE: *****
35 *   U.FAC=U.LTF NORMALLY *****
36 *   =U.BGF WHILE LOOKING FOR RECOVERY *****
37 002C A 0000      14,0 *****
38 003A A 0666      #0666      =.05 *****
39 003B A 0CCC      #0CCC      =.10 *****
40 003C A 0666      14, #0666 *****
41 ***** *****
42 * U.LST IS A TABLE OF 10 SECOND AVERAGE POINTS *****
43 * USED FOR THE LEAST SQUARES SLOPE ESTIMATES. *****
44 * U.LSP IS A ROW POINTER FOR U.LST *****
45 004A A 0000      140,0 *****
46 00D6 A 0000      0 *****
47 ***** *****
48 * U.SLP IS THE LEAST SQUARES SLOPE ESTIMATE. *****
49 00D7 A 0000      14,0 *****

```

```

51 *****
52 * U.WRK IS THE WORK AREA FOR THE VARIOUS U ANALYSIS
53 * PROGRAMS. AFTER 10 SECONDS OF DATA IS COLLECTED
54 * IN E.UBF BY E$SEC (E$CLK) AND E$UIN, THAT BUFFER
55 * IS FLUSHED INTO ROWS 2-11 OF U.WRK AND THE
56 * ANALYSIS PROGRAMS ARE SCHEDULED. ROW 1 OF U.WRK
57 * HOLDS THE LAST SECOND'S DATA FROM THE PREVIOUS
58 * 10 SECOND BLOCK.
59 * FORMAT: (A:AMP;A:PHASE;B:BLANK RATE)
60 *CHAN A B C ..... N (TIME)
61 *T-1 A P B A P B A P B ..... A P B X Y
62 *I TO A P B A P B A P B ..... A P B X Y
63 *M T1 A P B A P B A P B ..... A P B X Y
64 *E T2 .....
65 *
66 *
67 * T9 A P B A P B A P B ..... A P B X Y
68 * U.CTM IS A POINTER TO THE I CURRENT TIME
69 * U.WRK RES 484,0
70 * U.CTM EQU $-2
71 *****
72 * U.CMP IS THE COMPRESSED DATA BUFFER...
73 * EACH CHANNEL GETS 10 ENTRIES OF 3 WORDS
74 * (ONE OF PHASE, TWO OF TIME)
75 * U.CPP ID A POINTER TO THE START OF EACH
76 * CHANNEL'S AREA.
77 * U.CPC IS THE COMPRESSED DATA COUNTER, TELLING THE
78 * NUMBER OF COMPRESSED POINTS FOUND FOR EACH CHAN
79 * DURING THE CURRENT PERIOD.
80 * U.CMP RES 420,0
81 * U.CPP DFC CHAN 0
82 * U.CMP+30 CHAN 1
83 * U.CMP+60 CHAN 2
84 * U.CMP+90 CHAN 3
85 * U.CMP+120 CHAN 4
86 * U.CMP+150 CHAN 5
87 * U.CMP+180 CHAN 6
88 * U.CMP+210 CHAN 7
89 * U.CMP+240 CHAN 8
90 * U.CMP+270 CHAN 9
91 * U.CMP+300 CHAN 10
92 * U.CMP+330 CHAN 11
93 * U.CMP+360 CHAN 12
94 * U.CMP+390 CHAN 13
95 * U.CPC RES 14,0
96 *****
97 * U.CYC IS SET TO EITHER 150 OR -50 MICROSECONDS
98 * TO SHOW THE DIRECTION OF A 50 MICROSECOND
99 * CYCLE SHIFT SENT BY THE E$UIN DURING THE PREVIOUS
100 * TEN SECOND DATA BLOCK. BIT 0 OF THE PHASE INPUT

```

MODCOMP ASSEMBLY

DATA STORAGE BUFFERS.....

101	*	ON THE APPROPRIATE CHANNEL IS SET FOR THE LAST
102	*	VALUE RECEIVED BEFORE THE SHIFT WAS SENT.
103	*	U.CCN IS THE ACCUMULATION OF CYCLE SHIFTS (U.CYC)
104	*	ON EACH CHANNEL. THIS VALUE IS ADDED TO THE PHASE
105	*	TO GIVE A TRUE REPRESENTATION OF THE PHASE.
106	0489	A 0000 U.CYC RES 14,0
107	0497	A 0000 U.CCN RES 14,0


```

109 *****
110 * U.IHB; IF AN INE OR MAINTENANCE CONDITION EXISTS,
111 * U.IHB IS SET TO INHIBIT PROCESSING ON THAT CHAN.
112 * BIT 0=0 PERMITS NORMAL PROCESSING
113 * =1 INHIBITS PROCESSING ON THE CHANNEL
114 * U.IHB RES 14,0
115 *****
116 * U.URTY IS THE CHANNEL RECOVERY TALLY....
117 * WHENEVER U$RTA SENSES BAD DATA ON A CHANNEL,
118 * THAT CHAN'S RECOVERY TALLY IS SET TO -18.
119 * WHEN THE DATA IS AGAIN CONSIDERED (GOOD), THE
120 * TALLY GETS INCREMENTED ONCE EACH TEN SECONDS.
121 * IF U.URTY REACHES ZERO, THE CHANNEL IS PUT UP.
122 * U.URTY RES 14,-18
123 *****
124 * U.CST IS THE CHANNEL'S PROCESSING STATUS:
125 * =1 FOR UP
126 * =2 FOR COASTING, NO REPORT
127 * =3 FOR COASTING, REPORTED (LOGGED ON TTY)
128 * =4 FOR DOWN
129 * U.CST RES 14,4
130 *****
131 * U.AST IS THE CHANNEL'S ALARM STATUS:
132 * =1 WHEN LOOKING FOR ONSET
133 * =2 WHEN LOOKING FOR PEAK, ADV SIGNAL
134 * =3 WHEN LOOKING FOR PEAK, RET SIGNAL
135 * =4 WHEN LOOKING FOR BACKSWING PEAK, MULTIPLE PEAK SIGNAL
136 * =5 WHEN LOOKING FOR RECOVERY TO BEGIN
137 * =6 WHEN LOOKING FOR VALID RECOVERY
138 * =7 WHEN LOOKING FOR THE SIGNAL'S END.
139 * U.AST RES 14,0
140 *****
141 * U.CLS IS THE WAVEFORM CLASS:
142 * =1 FOR MEETING PRIMARY CRITERIA
143 * =2 FOR MEETING SECONDARY CRITERIA
144 * =3 FOR MULTIPLE PEAK SIGNAL
145 * U.CLS RES 14,0
146 *****
147 * U.END INDICATES THE TYPE OF SIGNAL ENDING:
148 * BIT 0=0 FOR NORMAL END TO WAVEFORM
149 * =1 FOR END DUE TO CHAN GOING DOWN
150 * BIT 1=0 NORMAL
151 * =1 TIME LIMITED TERMINATION
152 * BIT 2=0 NORMAL
153 * =1 NEW ONSET TERMINATES SIGNAL
154 * U.END RES 14,0

```

```

156 *****
157 * U.OTM IS THE ONSET TIME IN SECONDS (BINARY).
158 * SUBROUTINE E$DIF CHECKS THE TIME DIFFERENCE
159 * BETWEEN A FIXED VALUE AND THE CHANNEL'S ONSET
160 * TIME (U.OTM).
161 * U.OTM IS USED BY E$DIF WHEN A COMPARISON IS DESIRED
162 * TO SOME TIME OTHER THAN AN ONSET TIME.
163 * U.OTR IS THE ONSET TIME IN BCD
164 * U.OAM IS THE ONSET AMPLITUDE
165 * U.OFZ IS THE ONSET PHASE
166 * U.OOI IS THE ONSET DIURNAL
167 * U.ODR IS THE DIRECTION OF ONSET
168 * =1 FOR ADVANCE
169 * =2 FOR RETARD
170 * U.OSG IS THE STD. DEVIATION AT ONSET
171 04F9 A 0000 U.OTM RES 28,0
172 0515 A 0000 U.DTM RES 2,0
173 0517 A 0000 U.OTB RES 28,0
174 0533 A 0000 U.OAM RES 14,0
175 0541 A 0000 U.OFZ RES 14,0
176 054F A 0000 U.ODR RES 14,0
177 055D A 0000 U.OOI RES 14,0
178 0568 A 0000 U.OSG RES 14,0

```

```

180 *****
181 * W.PFZ IS THE PEAK PHASE
182 * W.PTM IS THE PEAK AMPLITUDE
183 * W.OST IS THE PHASE OFFSET TO PEAK
184 * W.ONR IS THE RATE OF ONSET TO PEAK
185 * W.LPK IS THE PHASE FOR A CLASS 3 LOW PEAK
186 * W.LTM IS THE TIME (BCD) OF THE W.LPK
187 * W.LAM IS THE AMP AT W.LTM
188 * W.VPK IS THE CLASS 3 VALLEY PEAK PHASE
189 * W.VTM IS THE TIME (BCD) OF THE W.VPK
190 * W.VAM IS THE AMP AT W.VTM
191 W.PFZ RES 0579 A 0000 14,0
192 W.PTM RES 0587 A 0000 28,0
193 W.PAM RES 05A3 A 0000 14,0
194 W.OST RES 05B1 A 0000 14,0
195 W.ONR RES 05BF A 0000 14,0
196 W.LPK RES 05CD A 0000 14,0
197 W.LTM RES 05DB A 0000 28,0
198 W.LAM RES 05F7 A 0000 14,0
199 W.VPK RES 0605 A 0000 14,0
200 W.VTM RES 0613 A 0000 28,0
201 W.VAM RES 062F A 0000 14,0
*****

```

```

203 *****
204 * W.RFZ IS THE PHASE AT VALID RECOVERY
205 * W.RTM IS THE BCD TIME OF VALID RECOVERY
206 * W.RAM IS THE AMPLITUDE AT VALID RECOVERY
207 063D A 0000 W.RFZ RES 14,0
208 064B A 0000 W.RTM RES 28,0
209 0667 A 0000 W.RAM RES 14,0

```

MODCOMP ASSEMBLY MISC.....

211
212
213
214
215
216

0675 A 0000
0683

TOTAL ASSEMBLY ERRORS = 1 SIZE = 1667

*U.AMP IS THE CURRENT AMPLITUDE ESTIMATE.
* ACTUALLY, IT'S THE LAST AMPLITUDE VALUE OF
* THE 10 SECOND PERIOD JUST ENDED.

U.AMP RES 14,0
END

```

1      PGM          U$REP
2      INT          U:RP1,U:RP2,U:RP3,U.UDR
3      EXT          E:FR1,E:BAD,E:BDP,U.FRQ,U. TRK
4      EXT          U.GAN,U. BL, M. STA, U. CLS, U. ODR
5      EXT          U. OSG, E: FMF, E: FAM, E: FCT, E. TMI
6      EXT          E. TM2, E. 28F, E: O28, U. AMP, U. OTB
7      EXT          U. OFZ, U. DAM, U. ODI, M. PFZ, M. PAM
8      EXT          W. RTM, W. RFZ, W. RAM, U. DIU, W. LTM
9      EXT          W. LPK, W. LAM, W. VTM, W. VPK, W. VAM
10     EXT          W. OST, W. ONR, U. LFZ, U. LTM, U. END
11     EXT          W. PTM, W. BDC, E: COT, U. DTM, U. OTM
12     EXT          E: SKD, E: BDC, U. CTM, E: DIF, E. BSG
13     EXT          U. CCL
14     CAT NO.     400100-008
15     * U$REP IS THE REPORTABLE SIGNAL MESSAGE GENERATOR.
16     * THE GENERATOR CONSISTS OF THREE SECTIONS:
17     * U:RP1 PRODUCES A MESSAGE FOR THE OPERATOR LISTING
18     * ALL AVAILABLE INFORMATION ABOUT THE SIGNAL. THIS
19     * ROUTINE IS CALLED AT HALF (OR VALID) RECOVERY.
20     * U:RP2 PRODUCES A MESSAGE FOR THE OPERATOR LISTING
21     * THE DATA ON THE SIGNAL SINCE HALF RECOVERY. THIS
22     * ROUTINE SETS UP THE CALL TO U:RP3.
23     * U:RP3 PRODUCES THE UDR FOR HQ.

```

```

25 *****
26 * U:RP1 CALLED BY U:WFA WHEN A SIGNAL HAS PASSED
27 * ALL REPORT CRITERIA AND HAS A HALF (VALID) RECOVERY.
28 * CALLING SEQUENCE:
29 *     BLM,15
30 *     U:RP1
31 * USING:-----
32 *     ONSET-----
33 *     TIME FROM U.OTB
34 *     PHASE FROM U.OFZ
35 *     AMP FROM U.GAM
36 *     DIURNAL FROM U.ODI
37 *     PEAK-----
38 *     TIME FROM W.PTW
39 *     PHASE FROM W.PFZ
40 *     AMP FROM W.PAM
41 *     HALF RECOVERY-----
42 *     TIME FROM W.RTM
43 *     PHASE FROM W.RFZ
44 *     AMP FROM W.RAM
45 *     DIURNAL FROM U.DIU
46 *     AND IF THE SIGNAL IS A CLASS 3:
47 *     1ST PEAK-----
48 *     TIME FROM W.LTW
49 *     PHASE FROM W.LPK
50 *     AMP FROM W.LAM
51 *     2ND PEAK-----
52 *     TIME FROM W.VTM
53 *     PHASE FROM W.VPK
54 *     AMP FROM W.VAM
55 *     U:RP1 SFM,1
56 *     RPSV1
57 *     SFM,10
58 *     RPSV2
59 *     ASSIGN NEXT UDR NUMBER TO THIS SIGNAL
60 *     AND UPDATE COUNT.
61 *     LDW,1
62 *     U:UDR
63 *     STM,1,6
64 *     UDRND
65 *     ABMM,15
66 *     U:UDR
67 *     LDW,1,6
68 *     U:ODR
69 *     STM,1,6
70 *     ZODR
71 *     LDW,1,6
72 *     U:CLS
73 *     STM,1,6
74 *     ZCLS
75 *     TRR,5,6

```


66	0013	2D51	LLS,5,1	U.OTM
67	0014	E515	LDM,1,5	
	0015	X 002D		
68	0016	F615	STM,1,5	ZOTM
	0017	R 072B		
69	0018	605F	ABR,5,15	U.OTM
70	0019	E515	LDM,1,5	
	001A	X 002D		
71	001B	E615	STM,1,5	ZOTM
	001C	R 072B		
72	001D	E7F0	* 00 5 LINE HEADER.....	
73	001E	R 0332	BLM,15	HEADR
74	001F	6C11	* 00 LONG ONSET LINE	
75	0020	E610	ZRR,1	LINEN
76	0021	R 0620	STM,1	
77	0022	E516	LDM,1,6	U.OFZ
	0023	X 0015		
78	0024	E610	STM,1	TPHAS
	0025	R 0632		
79	0026	E616	STM,1,6	ZOFZ
	0027	R 0819		
80	0028	E516	LDM,1,6	U.OAM
	0029	X 0016		
81	002A	E616	STM,1,6	ZOAM
	002B	R 0763		
82	002C	E610	STM,1	TAMP
	002D	R 0633		
83	002E	E516	LDM,1,6	U.OOI
	002F	X 0017		
84	0030	E610	STM,1	TDIUR
	0031	R 0634		
85	0032	6D56	TRR,5,6	
86	0033	2D51	LLS,5,1	
87	0034	E515	LDM,1,5	U.OTR
	0035	X 0014		
88	0036	E615	STM,1,5	ZOTR
	0037	R 0747		
89	0038	E610	STM,1	E.TMI
	0039	X 000F		
90	003A	605F	ABR,5,15	
91	003B	F515	LDM,1,5	U.OTR
	003C	X 0014		
92	003D	E615	STM,1,5	ZOTB
	003E	R 0747		
93	003F	E610	STM,1	E.TM2
	0040	X 0010		
94	0041	E7F0	BLM,15	LONGI
	0042	R 03FC		

U:RPI.....

MODCOMP ASSEMBLY

125	0072	E610	STM,1	E.TM1
	0073	X 000F		
126	0074	605F	ABR,5,15	
127	0075	E515	LDM,1,5	W.VTM
	0076	X 0021		
128	0077	E610	STM,1	E.TM2
	0078	X 0010		
129	0079	E7F0	BLM,15	SHRTL
	007A	R 042E		
130	007B	ED10	* DC SHORT PEAK LINE	
131	007C	A 0003	PKOUT LDI,1	3
	007D	E610	STM,1	LINEN
132	007E	R 0620		
	007F	E516	LDM,1,6	W.PFZ
133	0080	X 0018		
134	0081	E610	STM,1	TPHAS
	0082	R 0632		
135	0083	E516	LDM,1,6	W.PAM
	0084	X 0019		
136	0085	E616	STM,1,6	ZPAM
	0086	R 0787		
137	0087	E610	STM,1	TAMP
	0088	R 0633		
138	0089	6D56	TRR,5,6	
139	008A	2D51	LLS,5,1	
140	008B	E515	LDM,1,5	W.PTM
	008C	X 0029		
141	008D	E615	STM,1,5	ZPTM
	008E	R 0798		
142	008F	E610	STM,1	E.TM1
	0090	X 000F		
143	0091	605F	ABR,5,15	
144	0092	E515	LDM,1,5	W.PTM
	0093	X 0029		
145	0094	E615	STM,1,5	ZPTM
	0095	R 0798		
146	0096	E610	STM,1	E.TM2
	0097	X 0010		
147	0098	E7F0	BLM,15	SHRTL
	0099	R 042E		
148	009A	651D	* DC LONG HALF RECOVERY LINE	
149	009B	E610	LBR,1,13	
150	009C	R 0620	STM,1	LINEN
	009D	E516	LDM,1,6	W.RFZ
151	009E	X 0018		
152	009F	E616	STM,1,6	ZRFZ
	00A0	R 07EF		
153	00A1	E610	STM,1	TPHAS

MODCOMP ASSEMBLY

U:RPI.....

154	00A2	R	0632						
	00A3		E516	LDM,1,6	W. RAM				
155	00A4	X	001C	STM,1,6	ZRAM				
	00A5		E616						
156	00A6	R	07E1	STM,1	TAMP				
	00A7		E610						
157	00A8	R	0633	LDM,1,6	U. DIU				
	00A9		E516						
158	00AA	X	001D	STM,1	TDIUR				
	00AB		E610						
	00AC	R	0634						
159	00AD		6D56	TRR,5,6					
160	00AE		2D51	LLS,5,1					
161	00AF		E515	LDM,1,5	W. RTM				
	00B0	X	001A						
162	00B1		E615	STM,1,5	ZRTM				
	00B2	R	07C5						
163	00B3		E610	STM,1	E. TM1				
	00B4	X	000F						
164	00B5		605F	ABR,5,15					
165	00B6		E515	LDM,1,5	W. RTM				
	00B7	X	001A						
166	00B8		E615	STM,1,5	ZRTM				
	00B9	R	07C5						
167	00BA		E610	STM,1	E. TM2				
	00BB	X	0010						
168	00BC		E7F0	BLM,15	LONGL				
	00BD	R	03FC						
169	00BE		E5C6						
170	00BF	X	0024	LDM,12,6	W. OST				
	00C0		E6C6						
171	00C1	R	0771	STM,12,6	ZOST				
	00C2		E7F0						
172	00C3	X	000C	BLM,15	E:FMF				
	00C4	R	062D						
173	00C5		ED10	DFC	TEMP				
174	00C6	A	FFFC	LDI,1	-4				
	00C7		E521						
175	00C8	R	0632	LDM,2,1	TEMP+5				
	00C9		E621	STM,2,1	M03S1+4				
176	00CA	R	052F						
	00CB	R	701F	ABR8,1,15	\$-4				
	00CC		00C7						
178	00CD		E536	LDM,3,6	W. ONR				
	00CE	X	0025						
179	00CF		E636	STM,3,6	ZONR				
	00D0	R	078D						
180	00D1		ED10	LDI,1	#A				
	00D2	A	000A						

* SET UP OFFSET AND ONSET RATE

U:RPI.....

MODCOMP ASSEMBLY

181	00D3	2021	MPR,2,1	E:BAD
182	00D4	2A23	RAD,2,3	E.BDF
183	00D5	603F	ABR,3,15	
184	00D6	2B31	RAS,3,1	
185	00D7	6DC3	TRR,12,3	
186	00D8	E7E0	BLM,14	
187	00D9	0002	LDI,1	
188	00DA	ED10	LDX,2,1	
189	00DB	0003	ADI,2	#2E00
190	00DC	FD21	STM,2	M03S2+2
191	00DD	E820	ABR,1,14	
192	00DE	A 2E00	LDX,2,1	
193	00DF	E620	MBL,2,2	
194	00E0	0538	SBR,1,15	
195	00E1	601E	ADX,2,1	
196	00E2	FD21	STM,2	M03S2+1
197	00E3	0922	ABR,1,14	
198	00E4	611F	LDX,2,1	
199	00E5	F821	ADI,2	#2000
200	00E6	E620	STM,2	M03S2
201	00E7	0537	ZRR,2	E.28F
202	00E8	601E	STM,2	E:028
203	00E9	FD21	BLM,14	
204	00EA	E820	DFC	MSG03
205	00EB	A 2000	LFM,1	RPSV1
206	00EC	E620	LFM,10	RPSV2
207	00ED	0536	PRX,15	
208	00EE	6C22		
209	00EF	E620		
210	00F0	0011		
211	00F1	E7E0		
212	00F2	0012		
213	00F3	R 0526		
214	00F4	A410		
215	00F5	R 0635		
216	00F6	A4A0		
217	00F7	R 063C		
218	00F8	FF0F		

* RESTORE REGISTERS AND RETURN

```

211 *****
212 * U:RP2 IS CALLED WHEN THE SIGNAL ON THE CHANNEL IN
213 * R6 IS TERMINATED, THIS ROUTINE PRINTS PERTINENT
214 * INFORMATION ABOUT THE FINAL PART OF THE SIGNAL,
215 * SAVES INFORMATION FOR THE UDR, AND SETS UP THE
216 * CALL TO U:RP3 TO PREPARE THE UDR.
217 U:RP2 SFM,1 RPSVI

```

```

00F9 A510
00FA R 0635
00FB A5A0
00FC R 063C

```

```

00FD E7F0
00FE R 0332

```

```

00FF ED10
0100 A 0005
0101 E610
0102 R 0620
0103 E516
0104 X 0026
0105 E616
0106 R 0835
0107 E610
0108 R 0632
0109 E516
010A X 0013
010B E616
010C R 0827
010D E610
010E R 0633
010F E516
0110 X 001D
0111 E610
0112 R 0634
0113 6D56
0114 2D51
0115 E515
0116 X 0027
0117 E615
0118 R 0843
0119 E610
011A X 000F
011B 605F
011C E515
011D X 0027
011E E615
011F R 0843
0120 E610
0121 X 0010
0122 E7F0

```

```

* SET UP 5 LINE HEADER
BLM,15 HEADR

```

```

* SET UP LONG FULL RECOVERY LINE
LDI,1 5

```

```

STM,1 LINEN
LDM,1,6 U.LFZ
STM,1,6 ZFFZ
STM,1 TPHAS
LDM,1,6 U.AMP
STM,1,6 ZFAM
STM,1 TAMP
LDM,1,6 U.DIU
STM,1 TDIUR

```

```

TRR,5,6
LLS,5,1
LDM,1,5 U.LTM
STM,1,5 ZFTM
STM,1 E.TM1

```

```

ABR,5,15
LDM,1,5 U.LTM
STM,1,5 ZFTM
STM,1 E.TM2
BLM,15 LONGL

```

```

242 0123 R 03FC
243 0124 8606
0125 X 0028
0126 R 0130
244 0127 8616
0128 X 0028
0129 R 0133
245 012A 8626
0128 X 0028
012C R 0136
246 012D ED10
012E R 0642
247 012F F709
248 0130 ED10
0131 R 064E
249 0132 F706
250 0133 ED10
0134 R 0648
251 0135 F703
252 0136 ED10
0137 R 0654
253 0138 ED20
0139 A FFFA
254 013A FD31
255 0138 E632
013C R 054A
256 013D 601F
257 013E 702F
013F R 013A

0140 6520
0141 E620
0142 X 0011
262 0143 E7E0
0144 X 0012
263 0145 R 053A
264 0146 E516
265 0147 X 0028
266 0148 E616
0149 R 086D
267 014A 8206
014B R 085F
014C A410
269 014D R 0635
014E A4A0
270 014F R 063C

* DETERMINE HOW SIGNAL ENDED AND LOG
  TBMB,0,6 U.END,SGLST
  TBMB,1,6 U.END,TMLAP
  TBMB,2,6 U.END,NUONS
  LDI,1 ENDI
  HOP,STRND
  SGLST LDI,1 ENDS3
  TMLAP LDI,1 ENDS2
  HOP,STRND
  NUONS LDI,1 ENDS4
  STRND LDI,2 -6
  LDX,3,1 W04SI+6
  STM,3,2
  ABR,1,15
  ABRB,2,15 STRND+2

* OUTPUT LAST LINE SETTING FLAG TO HAVE U:RP3
* SCHEDULED WHEN ASR 28 BECOMES AVAILABLE.
  LBR,2,0 E.28F
  STM,2
  BLM,14 E:028
  DFC MSG04
  LDM,1,6 U.END

* SAVE ALL DATA NEEDED FOR U:RP3...
  STM,1,6 ZEND
  OBMM,0,6 INPRG
  FLAG REPORTABLE ON THIS CHAN

* NOW GET THE HELL OUT OF THIS PROGRAM
  LFM,1 RPSV1
  LFM,10 RPSV2
  
```


MODCOMP ASSEMBLY

U:RP2.....

271
272

0150

FF0F

BRX,15

MODCOMP ASSEMBLY

U:RP3.....

***** U:RP3 SETS UP ONE RSD MESSAGE AND RESCHEDULES ITSELF *****

* U:RP3 ZRR,*6 TBMB,*0,6 INPRG,*RP301 INIT CHAN

U:RP3 ZRR,*6 TBMB,*0,6 INPRG,*RP301

6C66 8606 0151 R 0152 8606 0152 R 0153 085F 0154 R 0154 0162 0155 606F 0156 ED50 0157 A 000E 0158 7956 0159 R 0152

ABR,*6,15 14 LDI,*5 SURB,*5,6 U:RP3+1

* NO MORE TO OUTPUT

ZRR,*6 E,*28F STM,*6 BLM,*14 E:028 DFC LEADR+25 BRU E:SKD

* FIRST LINE OUTPUT: LEADER/RSD ONE ONE ONE ONE/LEADER

RP301 LBR,*1,0 E,*28F STM,*1 BLM,*14 E:028 DFC LEADR INPRG ZBMM,*0,6 LDM,*12,6 UDRNO BLM,*14 E:BDC LDI,*7 3 LDI,*10 M05S1 ZRR,*11 CUTNO BLM,*14 ZRR,*5 SBX,*5,10 SBX,*11,15 ABR,*11,15 SBX,*5,10 LBR,*5,0 STM,*5 E,*28F BLM,*14 E:028 DFC MSG05

274	0151	6C66	
275	0152	8606	
276	0153	085F	R
277	0154	0162	R
	0155	606F	
278	0156	ED50	
279	0157	A 000E	A
280	0158	7956	
	0159	0152	R
281	015A	6C66	
282	015B	E660	
283	015C	0011	X
	015D	E7E0	X
284	015E	X 0012	X
285	015F	R 0569	R
286	0160	E700	
	0161	X 002E	X
287	0162	6510	
288	0163	E610	
289	0164	X 0011	X
	0165	E7E0	X
290	0166	X 0012	X
291	0167	R 0550	R
292	0168	8106	
	0169	R 085F	R
293	016A	E5C6	
	016B	R 0611	R
294	016C	E7E0	
	016D	X 002F	X
295	016E	ED70	
	016F	A 0003	A
296	0170	EDAO	
	0171	R 056F	R
297	0172	6CBB	
298	0173	E7E0	
	0174	R 0490	R
299	0175	6C55	
300	0176	AF5A	
301	0177	608F	
302	0178	AF5A	
303	0179	6550	
304	017A	E650	
	017B	X 0011	X
305	017C	E7E0	
	017D	X 0012	X
306	017E	R 056B	R

LINE	CLASS	TYPE	FRQ	CALL	LTRS	PATH	DOY
307	017F	ED50					
308	0180	0041					
309	0181	E546					
310	0182	06C9					
311	0183	714E					
312	0184	0186					
313	0185	605F					
314	0186	EDA0					
315	0187	0583					
316	0188	6C8B					
317	0189	AF5A					
318	018A	60BF					
319	018B	ED50					
320	018C	002C					
321	018D	AF5A					
322	018E	60BF					
323	018F	E546					
324	0190	06D7					
325	0191	6C55					
326	0192	ED30					
327	0193	FFFA					
328	0194	E7F0					
329	0195	0462					
330	0196	ED50					
331	0197	002C					
332	0198	AF5A					
333	0199	60BF					
334	019A	6D16					
335	019B	2D11					
336	019C	6D41					
337	019D	E840					
338	019E	06E5					
339	019F	6C55					
340	01A0	ED30					
341	01A1	FFFC					
342	01A2	E7F0					
343	01A3	0462					
344	01A4	6D41					
345	01A5	E840					
346	01A6	0701					
347	01A7	E7F0					
348	01A8	0462					
349	01A9	ED50					
350	01AA	002C					
351	01AB	AF5A					
352	01AC	60BF					

* LINE 2: CLASS, TYPE, FRQ CALL LTRS, PATH, DOY

LDI,5

LDM,4,6

SBRB,4,14

ABR,5,15

ZRR,11

SBX,5,10

ABR,11,15

LDI,5

SBX,5,10

ABR,11,15

LDM,4,6

ZRR,5

LDI,3

BLM,15

LDI,5

SBX,5,10

ABR,11,15

TRR,1,6

LLS,1,1

TRR,4,1

ADI,4

ZRR,5

LDI,3

BLM,15

TRR,4,1

ADI,4

ZRR,5

LDI,3

BLM,15

TRR,4,1

ADI,4

ZRR,5

LDI,3

BLM,15

TRR,4,1

ADI,4

ZRR,5

LDI,3

BLM,15

TRR,4,1

ADI,4

* STORE TYPE: ZTYPE

-6

TYPE

#2C

* DO FREQUENCY AND CALL LETTERS

ZFRQ

-4

FREQ

STRCH

CALLT

CALL LETTERS

#2C

SBX,5,10

ABR,11,15

* DO CALL LETTERS

TRR,4,1

ADI,4

STRCH

#2C

SBX,5,10

ABR,11,15

U:RP3.....

MODCOMP ASSEMBLY

342	01AD	ED50							
343	01AE	A 000B							
	01AF	86A6							
344	01B0	R 071D							
	01B1	R 01B3							
345	01B2	615F							
346	01B3	E7F0							
	01B4	R 0451							
347	01B5	ED50							
	01B6	A 002C							
348	01B7	AF5A							
349	01B8	60BF							
350									
	01B9	6C22							
351	01BA	E530							
352	01BB	X 0030							
	01BC	2C22							
353	01BD	6D52							
354	01BE	E7F0							
355	01BF	R 0451							
	01C0	6C22							
356	01C1	2C24							
357	01C2	6D52							
358	01C3	E7F0							
359	01C4	R 0451							
	01C5	6C22							
360	01C6	2C24							
361	01C7	6D52							
362	01C8	E7F0							
363	01C9	R 0451							
	01CA	E7F0							
364	01CB	R 049A							
365									
	01CC	ED50							
366	01CD	A 2020							
367	01CE	E650							
368	01CF	R 0583							
	01D0	ED50							
369	01D1	A 412E							
	01D2	E650							
370	01D3	R 0584							
	01D4	EDA0							
371	01D5	R 0585							
	01D6	6C8B							
372	01D7	6D76							
373	01D8	2D71							
374	01D9	E537							
375	01DA	R 0747							

* INDICATE FRONT OR BACK PATH
LDI,5

TBMB,10,6 ZSTA,\$+4

SBR,5,15 FRONT/BACK
BLM,15

LDI,5 #2C

SBX,5,10
ABR,11,15

* DO DAY OF YEAR
ZRR,2
LDM,3

U-CTM

LLD,2,2
TRR,5,2

1ST DIGIT

ABCNO

ZRR,2
LLD,2,4
TRR,5,2

2ND DIGIT

ARCNO

ZRR,2
LLD,2,4
TRR,5,2

3RD DIGIT

ABCNO

* END LINE AND OUTPUT
BLM,15 TERLN

* SET UP PARAGRAPH A.
LDI,5 | |

STM,5 M06S1

LDI,5 |A.|

STM,5 M06SI+1

LDI,10 M06SI+2

SET UP FOR OUTPUT

ZRR,11
TRR,7,6
LLS,7,1
LDM,3,7

ZOTB

U:RP3.....

MODCOMP ASSEMBLY

Line No	Code	Label	Parameter	Value	Description
376	6C22				
377	01D8		ZRR,2		
378	01DC		LLS,3,10		
379	01DD		LLD,2,2		
380	01DE		TRR,5,2		
381	01DF		BLM,15		1ST DIGIT (HRS X 10)
	01E0	R			
382	01E1		ZRR,2		
383	01E2		LLD,2,4		
384	01E3		TRR,5,2		
385	01E4		BLM,15		2ND DIGIT (HR X 1)
	01E5	R			
386	01E6		ABR,7,15		
387	01E7		LDM,3,7		
	01E8	X			U.OTB
388	01E9		ZRR,2		
389	01EA		LLD,2,5		
390	01EB		TRR,5,2		
391	01EC		BLM,15		3RD DIGIT (MIN X 10)
	01ED	R			
392	01EE		ZRR,2		
393	01EF		LLD,2,4		
394	01F0		TRR,5,2		
395	01F1		BLM,15		4TH DIGIT (MIN X 1)
	01F2	R			
396	01F3		ZRR,2		
397	01F4		LLD,2,3		
398	01F5		TRR,5,2		
399	01F6		BLM,15		5TH DIGIT (SEC X 10)
	01F7	R			
400	01F8		ZRR,2		
401	01F9		LLD,2,4		
402	01FA		TRR,5,2		
403	01FB		BLM,15		6TH DIGIT (SEC X 1)
	01FC	R			
404	01FD		LDI,5		#2C
	01FE	A			
405	01FF		SBX,5,10		
406	0200		ABR,11,15		
407	0201		LDM,12,6		ZOST
	0202	R			
408	0203		ABR,12,12		
409	0204		RLS,12,4		
410	0205		BLM,14		E:BDC
	0206	X			
411	0207		LDI,7		3
	0208	A			
412	0209		BLM,14		OUTNO
	020A	R			
413	020B		LDI,5		#2C

* DO SIX DIGIT ONSET TIME

1ST DIGIT (HRS X 10)

2ND DIGIT (HR X 1)

3RD DIGIT (MIN X 10)

4TH DIGIT (MIN X 1)

5TH DIGIT (SEC X 10)

6TH DIGIT (SEC X 1)

OFFSE

ROUND OFF

3 DIGIT OFFSET

U:RP3.....

MODCOMP ASSEMBLY

414	415	416	417	418	419	420	421	422	423	424	425	426	427	428	429	430	431	432	433	434	435	436	437	438	439	440	441	442	443	444									
020C	020D	020E	0210	0211	0212	0213	0214	0215	0216	0217	0218	0219	021A	021B	021C	021D	021E	021F	0220	0221	0222	0223	0224	0226	0227	0228	0229	022A	022B	022C	022D	022E							
A	A	R	R	A	R	R	R	R	R	R	R	R	R	R	R	R	R	R	R	R	R	R	R	R	R	R	R	R	R	R	R								
002C	AF5A	60BF	E546	ED50	0013	714E	0216	F702	615F	E7F0	0451	E7E0	04A5	E5C6	078D	60CC	99C4	E7E0	002F	ED70	0003	E7E0	0490	ED50	002C	AF5A	60BF	E556	0763	E7C0	046D	E7F0	049A						
SBX,5,10	ABR,11,15	LDM,4,6	LDI,5	SBRB,4,14	\$+3	HOP,\$+2	SBR,5,15	BLM,15	BLM,14	NXTLN	LDM,12,6	ZONP	ABR,12,12	RLS,12,4	BLM,14	E:8DC	LDI,7	3	BLM,14	OUTNO	LDI,5	#2C	SBX,5,10	ABR,11,15	LDM,5,6	BLM,12	STAMP	BLM,15	TERLN	* PEAK DATA, PARAGRAPH B.	LDI,5	IB,1							
ZODR	19							ABCNO																															
					</																																		

U:RP3.....

NODCOMP ASSEMBLY

445	023C	E557	LDM,5,7	ZOTM
446	023D R	072B	STM,5	U.OTM
447	023E X	E650	LBR,4,15	
448	023F	002C	ABR,7,15	
449	6240	654F	LDM,5,7	ZOTM
450	0241	607F	STM,5,4	U.OTM
451	0242	E557	SBR,7,15	
452	0243 R	072B	ADI,7	ZPTM
453	0244 X	E654	STM,7	\$+7
454	0245	002C	TRR,7,6	
455	0246	517F	LDI,6	14
456	0247	E870	BLM,14	E:DIF
457	0248 R	079B	DFC	0
458	0249	E670	NOP	
459	024A R	0250	NOP	
460	024B	6076	TRR,6,7	#6
461	024C	ED60	LDI,7	
462	024D A	000E	DVR,12,7	
463	024E	E7E0	TRR,12,13	
464	024F X	0031	BLM,14	E:BDC
465	0250 A	0000	LDI,7	3
466	0251	6600	BLM,14	OUTNO
467	0252	6600	LDI,5	#2C
468	0253	6067	SBX,5,10	
469	0254	ED70	ABR,11,15	
470	0255 A	0006	LDM,5,6	ZPAM
471	0256	21C7	BLM,12	STAMP
472	0257	6DCD	BLM,15	TERLN
473	0258	E7E0		
474	0259 X	002F		
475	025A A	ED70		
476	025B A	0003		
	025C	E7E0		
	025D R	0490		
	025E	ED50		
	025F A	002C		
	0260	AF5A		
	0261	60BF		
	0262	E556		
	0263 R	07B7		
	0264	E7C0		
	0265	046D		
	0266	E7F0		
	0267 R	049A		
	0268	ED50		
	0269 A	432E		
	026A	E650		
	026B R	0584		
	026C	EDAO		

THREE DIGIT TIME FROM ONSET

* HALF RECOVERY DATA, PARAGRAPH C. IC.1

U:RP3.....

MODCOMP ASSEMBLY

Address	Op	Op Code	Op Name	Op Comment	Op Comment
026D	R	0585			
026E		6C8B	ZRR,11		
026F		ED50	LDI,5	13	
0270	A	000D			
0271		E7F0	BLM,15	ABCNC	(HALF)
0272	R	0451			
* GET TIME FROM ONSET TO HALF RECOVERY					
0273		6D76	TRR,7,6		
0274		2D71	LLS,7,1		
0275		E870	ADI,7	ZRTM	
0276	R	07C5			
0277		E670	STM,7	\$+7	
0278	R	027E			
0279		6D76	TRR,7,6		
027A		ED60	LDI,6	14	
027B	A	000E			
027C		E7E0	BLM,14	E:DIF	
027D	X	0031			
027E	A	0000	DFC	0	
027F		6600	NOP		
0280		6600	NOP		
0281		6D67	TRR,6,7		
0282		ED70	LDI,7	#6	
0283	A	0006			
0284		21C7	DVR,12,7		
0285		6DCD	TRR,12,13		
0286		E7E0	BLM,14	E:BDC	
0287	X	002F			
0288		ED70	LDI,7	3	
0289	A	0003			
028A		E7E0	BLM,14	OUTNO	THREE DIGIT TIME FROM ONSET
028B	R	0490			
028C		ED50	LDI,5	#2C	
028D	A	002C			
028E		AF5A	SBX,5,10		
028F		608F	ABR,11,15		
0290		E556	LDM,5,6	ZRAM	
0291	R	07E1			
0292		E7C0	BLM,12	STAMP	
0293	R	046D			
0294		E7F0	BLM,15	TERLN	
0295	R	049A			
* DAT ABOUT SIGNAL ENDING, PARAGRAPH D.					
0296		ED50	LDI,5	ID.1	ID.1
0297	A	442E			
0298		E650	STM,5	M06SI+1	
0299	R	0584			
029A		EDA0	LDI,10	M06SI+2	
029B	R	0585			
029C		6C8B	ZRR,11		

MODCOMP ASSEMBLY

U:RP3.....

PAGE 21

597

0330

E700

BRU

E:SKD

0331 X 002E

```

599 *****
600 * HEADR SETS UP THE FIRST FIVE LINES TO THE ASR 28.
601 * THIS INCLUDES ALL DATA ABOUT THE CHANNEL:
602 * 1. CHAN DESIGNATOR (SHOWS PRIORITY)
603 * 2. FREQ AND TRANSMITTER CALL SIGN
604 * 3. TRACK RATE, GAIN, AND BLANK LEVEL
605 * 4. CAROID CONFIGURATION
606 * 5. SIGNAL TYPE
607 * 6. BACKGROUND LEVEL AT ONSET
608 HEADR BLM,14 E:FRT GET MESSAGE TIME
609 DFC MSG01+2
610 * READ DET NUMBER AND SET IT UP FOR OUTPUT
611 LDI,2 #400E
612 OCD,2,#E
613 IDD,4,#E
614 MBR,2,4
615 ADI,2 #2030
616 STM,2 M01S1+2 | 5--|
617 MLR,4,4
618 RLD,4,4
619 LLS,4,4
620 LLD,4,4
621 ADI,4 #3030
622 STM,4 M01S1+3 | 515|
623 * SET UP RSD (REPORATABLE SIGNAL DESCRIPTOR) NUMBER.
624 LDM,12,6 UDRNO
625 BLM,14 E:BAD CONVERT TO DECIMAL
626 LDI,5 E.8DF
627 LDX,4,5
628 MRL,4,4
629 ADI,4 #0020
630 STM,4 M01S2+1 | --0 |
631 ABR,5,14
632 LDX,4,5
633 MRL,4,4
634 SBR,5,15
635 ADX,4,5
636 STM,4 M01S2 | 1000 |

```

```

E7E0
0332 X 0001
0333 R 04B8
ED20
0335 A 400E
0336 432E
0337 4F4E
0338 0824
0339 0824
033A E820
033B A 2030
033C E620
033D R 04CF
033E 0C44
033F 2844
0340 2D44
0341 2C44
0342 E840
0343 A 3030
0344 E640
0345 R 04D0
E5C6
0346 R 0611
0347 E7E0
0348 X 0002
0349 ED50
034A X 0003
034B FD45
034C 0944
034D E840
034E A 0020
034F E640
0350 R 04D3
0351 605E
0352 FD45
0353 0944
0354 615F
0355 F845
0356 E640
0357 R 04D2
0358

```

637	0359	6D56	* DC CHANNEL DESIGNATOR.....		
638	035A	E850	TRR,5,6		
639	035B	A 2041	ADI,5	I AI	
640	035C	E650	STM,5	M01S2+5	
641	035D	R 04D7	* DO FREQ AND CALL LETTERS.....		
642	035E	ED10	L01,1	-56	
643	035F	A FFC8	TRR,7,6		
644	0360	6D76	LLS,7,1		
645	0362	D071	LDM,4,7	U.FRQ	R4,5=FREQUENCY
646	0363	X 0004	STM,4,7	ZFRQ	
647	0364	E647	ABR,7,15		
648	0365	R 06E5	LDM,5,7	U.FRQ	
649	0366	607F	STM,5,7	ZFRQ	
650	0367	F557	STM,4	M01S3	STORE FREQ
651	0368	E640	STM,5	M01S3+1	
652	036C	R 04DB	HED02	LDM,2,1	
653	036D	E650	LDM,3,1	CALTR+56	
654	036E	R 04DC	XORB,2,4	NXT01	
655	036F	F521	XORB,3,5	NXT01	
656	0370	R 060B	LDM,2,1	CALTR+58	
657	0371	E531	STM,2	M01S3+2	GET CALL LTRS.
658	0372	R 060C	SBR,7,15		
659	0373	7C24	STM,2,7	CALLT	
660	0374	R 0387	LDM,2,1	CALTR+59	
661	0375	7C35	ABR,7,15		
662	0376	R 0387	STM,2,7	CALLT	
663	0377	E521	STM,2	M01S3+3	
664	0378	R 060D	BRU	HED01	
665	037A	R 04DD	NXT01	ABR,1,13	
666	037B	617E		HED02	
667	037C	E627			
668	037D	R 0701			
669	037E	E521			
670	037F	R 060E			
671	0380	607F			
672	0381	F627			
673	0382	R 0701			
674	0383	E620			
675	0384	R 04DE			
676	0385	F700			
677	0386	R 0397			
678	0387	701D			
679	0388	R 036F			

SUPROUTINES.....

MODCOMP ASSEMBLY

Address	Code	Label	Operation	Comments
666	0389	ED20		
667	038A	2055		
667	038B	617F		
668	038C	F627		
669	038D	0701		
670	038E	F620		
670	038F	04DD		
671	0390	ED20		
671	0391	4E48		
672	0392	607F		
672	0393	E627		
673	0394	0701		
673	0395	E620		
674	0396	04DE		
675	0397	E556		
676	0398	0005		
676	0399	E656		
677	039A	07FD		
677	039B	F850		
678	039C	5230		
678	039D	F650		
679	039E	04E0		
679	039F	E556		
680	03A0	0006		
680	03A1	6C44		
681	03A2	FD10		
682	03A3	000A		
682	03A4	2141		
683	03A5	F850		
684	03A6	4E30		
684	03A7	E650		
685	03A8	04E2		
685	03A9	0944		
686	03AA	F840		
687	03AB	303A		
687	03AC	E640		
688	03AD	04E3		
688	03AE	E556		
689	03AF	0007		
689	03B0	6C44		
690	03B1	2141		
691	03B2	0955		
692	03B3	6854		
693	03B4	E850		
694	03B5	3030		
694	03B6	E650		
695	03B7	04E5		
696	03B8	E556		

LDI,2 I UI IF NO MATCH FOUND,
 SRR,7,15 USE I UNKI
 STM,2,7
 STM,2 MOIS3+2
 LDI,2 INKI FOR UNKNOWN
 ARP,7,15
 STM,2,7 CALLT
 STM,2 MOIS3+3

* NOW, SET UP TRACK RATE, GAIN, AND BLANK LEVEL.....
 HED01 LDM,5,6 U,TRK

STM,5,6 ZTR
 ADI,5 #5230
 STM,5 MOIS3+5 TRAKC RATE
 LDM,5,6 U,GAN
 ZRR,4
 LDI,1 10
 DVR,4,1
 ADI,5 INOI *
 STM,5 MOIS3+7 GAIN
 MBL,4,4
 ADI,4 IO:I
 STM,4 MOIS3+8
 LDM,5,6 U,BLL
 ZRR,4
 DVR,4,1
 MBL,5,5
 ADR,5,4
 ADI,5 #3030
 STM,5 MOIS3+10

* GET CARDICID PATTERN
 LDM,5,6 M,STA

SUBROUTINES.....

MODCOMP ASSEMBLY

```

759 0417 E620 STM,2 M02S2+4 | +---0 |
0418 R 0524
760 0419 601F ABR,1,15
761 041A FD21 LDX,2,1
762 041B 0922 MBL,2,2
763 041C E820 ADI,2 #2E
764 041D A 002E STM,2 M02S2+3 | +--0.0 |
041E E620
041F R 0523
765 0420 601E ABR,1,14
766 0421 FD21 LDX,2,1
767 0422 0922 MBL,2,2
768 0423 611F SRR,1,15
769 0424 F821 ADX,2,1
770 0425 E620 STM,2 M02S2+2 | +000.0 |
0426 R 0522
771 0427 6C22 LOUT ZRR,2
772 0428 E620 STM,2 F.28F
0429 X 0011
773 042A E7E0 BLM,14 E:028
042B X 0012
774 042C R 0510 DFC MSG02
775 042D FFOF BRX,15 RETURN.
776 *****
777 * SHRTL SETS UP AND OUTPUTS A SHORT LINE *****
778 SHRTL BLM,5 SHSET *****
779 ZRR,2 M02S2 TERMINATE LINE
780 0431 E620 STM,2
0432 R 0520 BRU LOUT
0433 E700
0434 R 0427 *****
782 *****
783 * SHSET SETS UP THE COMMON PART OF BOTH *****
784 * LONG AND SHORT LINES. *****
785 SHSET LDM,1 *****
786 LDM,2,1 LLABE
787 STM,2 MSG02+1
788 * CONVERT TIME ELEMENT *****
789 RLM,14 E:FCF
790 043D R 0513 DFC MSG02+3
791 043E R 05C0 LDM,12 TPHAS
792 0440 6DEF TRP,14,15 SAVE R15
793 0441 E7F0 BLM,15 E:FMF FORMAT PHASE
0442 X 000C

```

SUBROUTINES.....

MODCOMP ASSEMBLY

```

794 0443 R 0518
795 0444 6DFE
796 0445 E516
0446 X 0013
797 0447 E520
0448 R 0633
798 0449 E626
044A X 0013
799 044B E7E0
044C X 000D
800 044D R 051D
801 044E E616
044F X 0013
802 0450 FF05

```

```

DFC
TRR,15,14
LDM,1,6
LDM,2
STM,2,6
BLM,14
DFC
STM,1,6
BRX,5

```

```

MSG02+8
U.AMP
TAMP
U.AMP
E:FAM
M0ZS1+4
U.AMP

```

```

SUPPLY AMP FOR OUTPUT
RESTORE REAL AMP

```

RETURN

```

* ABCNO IS A SUBROUTINE TO CONVERT A NUMERIC DIGIT
* TO THE ALPHABETIC REPRESENTATION.
* IT HAS BEEN EXTENDED TO OUTPUT OTHER WORDS!
* AS WELL.

```

```

* CALLING SEQUENCE:
* BLM,15 ABCNO
* WITH R5= NUMBER TO BE CONVERTED
* OR CODE FOR DESIRED WORD.
* R10,R11 = BYTE ADDRESS TO BEGIN STORAGE
* EXIT WITH R10,R11 UPDATED FOR NEXT USAGE.
ABCNO SFM,4 RPSV1

```

ABCNO

LDL

A540

0451

814

RPSV1

LDL

0635

0452

815

NCTBL

LDL

E545

0453

816

ZRR,5

LDL

065A

0454

817

ABCN1

LDL

AE74

0455

818

TRR,7,7

LDL

7077

0456

819

HOP,ABCRT

LDL

045A

0457

820

SBX,7,10

LDL

F706

0458

821

ABR,5,15

LDL

AF7A

0459

822

ABR,11,15

LDL

605F

045A

823

BRU

LDL

60RF

045B

824

ABCN1

LDL

E700

045C

825

ABCRT

LDL

A440

045D

826

RPSV1

LDL

0635

045E

827

BRX,15

LDL

FF0F

045F

828

STRCH

LDL

FF0F

0460

829

```

*****
* STRCH STORES A FIXED NUMBER OF CHARACTERS IN THE OUTPUT
* LINE.

```

```

* BLM,15 STRCH
* WITH R4=LOCATION OF FIRST CHAR
* R5= ZERC
* R3=COUNT OF CHARS

```

STRCH

LDL

FF0F

0461

830

WITH R4=LOCATION OF FIRST CHAR

LDL

FF0F

0460

831

R5= ZERC

LDL

FF0F

0460

832

R3=COUNT OF CHARS

LDL

FF0F

0461

833

```

834 * REGISTERS ARE RESTORED FOR RETURN.
835 * R10,R11 BYTE ADDR FOR STORAGE,
836 * ARE UPDATED ON RETURN.
837 STRCH SFM,3 RPSV1
      A530
0463 R 0635
0464 AE74
0465 AF7A
0466 605F
0467 60RF
0468 703F
0469 R 0464
046A A430
046B R 0635
046C FFOF

      LBX,7,4
      SBX,7,10
      ABR,5,15
      ABR,11,15
      ABRB,3,15
      STRCH+2

      LFM,3
      RPSV1

      BRX,15
*****
* STAMP TAKES THE NUMERIC AMPLITUDE, CONVERTS IT TO
* DB AND PUTS IT IN THE OUTPUT BUFFER
* BLM,12 STAMP
* WITH R5= VALUE
* R10,R11 FOR STORAGE LOCATION
* UPDATED ON RETURN.
* STAMP SFM,4 RPSV2
      A540
046D R 063C
046E ED70
046F A 000A
0470 A 2047
0471 2A49
0472 2A49
0473 E950
0474 A 0014
0475 6D6C
0476 6DC5
0477 E7E0
0478 X 002F

      LDI,7 10
      MPR,4,7 AMP*100
      RAD,4,9 /2048
      SUI,5 20 -5 VOLTS

      TRR,6,12
      TRR,12,5
      BLM,14 E:BDC

* OUTPUT SIGN.....
      6DC6
      TRR,12,6
      LDI,5 20
      LDM,6 F:RSG
      SUI,6 #202D
      TRPR,6,6 $+3
      ABR,5,15
      BLM,15 ABCNO
      0451 R 0451

* 3 DIGIT OUTPUT,....
      ED70
      0486 A 0002
      0487 E7E0
      BLM,14 OUTNO
      PLUS/MINUS
  
```


SUBROUTINES.....

MODCOMP ASSEMBLY

```

0488 R 0490
0489 ED50
048A A 0016
048B E7F0
048C R 0451
048D A440
048E R 063C
048F FFOC

0490 ED40
0491 X 0003
0492 6847
0493 614F
0494 FD54
0495 E7F0
0496 R 0451
0497 717F
0498 R 0493
0499 FFOE

049A 6C55
049B AF5A
049C 60RF
049D AF5A
049E 6550
049F E650
04A0 X 0011
04A1 E7E0
04A2 X 0012
04A3 R 0582
04A4 FFOF

04A5 ED50
04A6 A 000D
04A7 AF5A
04A8 60RF
04A9 ED50
04AA A 000A
04AB AF5A
04AC 60RF
04AD ED40
04AE A FFCC
04AF ED50

0490 LDI,5 22
0491 BLM,15 ABCND (DR)
0492 LFM,4 RPSV2
0493 BRX,12
*****
* CUTNO OUTPUTS THE NUMBER OF DIGITS SPECIFIED IN
* R7 FPOM E.RDF+(R7) THRU E.BDF.
* RLM,14 OUTNO
* OUTNO LDI,4 E.BDF

0494 ADP,4,7 FIRST CHAP.
0495 SRP,4,15
0496 LDX,5,4
0497 RLM,15 ABCND
0498 SBR,7,15 $-4
0499 BRX,14
*****
* TERLN TERMINATES LINE AND OUTPUTS TO ASR 28
* TERLN ZRR,5
049A SAX,5,10
049B ABR,11,15
049C SBX,5,10
049D LPR,5,0
049E STM,5 E.28F
04A0 RLM,14 E:Q28
04A1 DFC MSG06
04A2 BRX,15
*****
* NXTLN IS A SUBROUTINE TO SET UP ANOTHER LINE IN
* THE CURRENT PARAGRAPH.
* BLM,14 NXTLN
* NXTLN LDI,5 #0D C/R
04A5 SBX,5,10
04A6 ABR,11,15 L/F
04A7 LDI,5 #0A
04A8 SBX,5,10
04A9 ABR,11,15
04AA LDI,4 -4
04AB LDI,5 #20
04AC LDI,5 4 SPACES
04AD LDI,5 #0D
04AE LDI,5 #0D
04AF LDI,5 #0D

```


MODCOMP ASSEMBLY

SUBROUTINES.....

909	04B0	A	0020	
910	04B1		AF5A	SBX,5,10
911	04B2		60BF	ABR,11,15
	04B3		704F	ABRB,4,15
	04B4	R	04B1	
912	04B5		FF0E	BRX,14

\$-2

MESSAGES.....

MODCOMP ASSEMBLY

```

914
915 *****
      MSG01 DFC
      #0D0A,#0A0A,|123 123456 |
      *****

916      I REPORTABLE SIGNAL DESCRIPTION, I
      DFC
      *****
      M01S1 DFC
      | DET 5151,#0D0A
      *****
      M01S2 DFC
      |001 |,#0D0A,|CHAN A;FREQ |
      *****
      M01S3 DFC
      |17.8 NAA;TR7;GN23;BL47;CARD7|
      *****

```


MESSAGES.....

MODCOMP ASSEMBLY

925	0519 A 3030	M02S1 DFC	10003.62 00.00
	051A A 3033		
	051B A 2E36		
	051C A 3220		
	051D A 2030		
	051E A 302E		
	051F A 3030		
926	0520 A 2020	M02S2 DFC	+000.0 ,0
	0521 A 202B		
	0522 A 3030		
	0523 A 302E		
	0524 A 3020		
	0525 A 0000		
927	0526 A 000A	MSG03 DFC	#000A, OFFSET:
	0527 A 4F46		
	0528 A 4653		
	0529 A 4554		
	052A A 3A20		
928	052B A 3031	M03S1 DFC	0137.62 , #000A, ONSET RATE:
	052C A 3337		
	052D A 2E36		
	052E A 3220		
	052F A 000A		
	0530 A 4F4E		
	0531 A 5345		
	0532 A 5420		
	0533 A 5241		
	0534 A 5445		
	0535 A 3A20		
929	0536 A 2030	M03S2 DFC	000.0 ,0
	0537 A 3030		
	0538 A 2E30		
	0539 A 0000		
930	053A A 000A	MSG04 DFC	#000A, SIGNAL TERMINATED:
	053B A 5349		
	053C A 474E		
	053D A 414C		
	053E A 2054		
	053F A 4552		
	0540 A 4049		
	0541 A 4E41		
	0542 A 5445		
	0543 A 443A		
931	0544 A 2053	M04S1 DFC	SIGNAL LOST , #000A, #000A, 0
	0545 A 4947		
	0546 A 4E41		
	0547 A 4C20		
	0548 A 4C4F		
	0549 A 5354		
	054A A 000A		

MODCOMP ASSEMBLY MESSAGES.....

932	054B	A	0A0A						
	054C	A	0000						
	054D	A	0D0A	LEADS	DFC			#0D0A, #0A0A, #0A0A	
	054E	A	0A0A						
	054F	A	0A0A						
933	0550	A	0101	LEADR	RES			25, #0101	
934	0569	A	1F1F		DFC			#1F1F, 0	
	056A	A	0000						
935	056B	A	0D0A	MSG05	DFC			#0D0A, #0A0A, RSD	
	056C	A	0A0A						
	056D	A	2052						
	056E	A	5344						
936	056F	A	0000	M05S1	RES			19, 0	
937	0582	A	0D0A	MSG06	DFC			#0D0A	
938	0583	A	0000	M06S1	RES			80, 0	

TABLES AND PARAMETERS.....

* TABLE TO ASSOCIATE TRANSMITTER CALL LETTERS WITH * FREQUENCY.

MODCOMP	ASSEMBLY	CAL TR	DFC	FREQUENCY	TRANSMITTER CALL LETTERS
940	05D3 A	3132			
941	05D4 A	2E30			
942	05D5 A	204D		12.0 MG4	
	05D6 A	4734			
943	05D7 A	3132			
	05D8 A	2E32		12.2 MG1	
	05D9 A	204D			
944	05DA A	4731			
	05DB A	3133		13.1 MG3	
	05DC A	2E31			
	05DD A	204D			
945	05DE A	4733			
	05DF A	3136		16.0 GBR	
	05E0 A	2E30			
	05E1 A	2047			
	05E2 A	4252			
946	05E3 A	3136			
	05E4 A	2E34		16.4 JXZ	
	05E5 A	204A			
	05E6 A	585A			
947	05E7 A	3137			
	05E8 A	2E34		17.4 NDT	
	05E9 A	204E			
948	05FA A	4454			
	05ER A	3137		17.8 NAA	
	05EC A	2E38			
	05ED A	204E			
949	05EE A	4141			
	05FF A	3138		18.6 NPG	
	05F0 A	2E36			
	05F1 A	204E			
950	05F2 A	5047			
	05F3 A	3139		19.6 GBZ	
	05F4 A	2E36			
	05F5 A	2047			
	05F6 A	425A			
951	05F7 A	3230			
	05F8 A	2E30		20.0 HWV	
	05F9 A	2057			
	05FA A	5756			
952	05FB A	3231			
	05FC A	2E34		21.4 NSS	
	05FD A	204E			
	05FE A	5353			
953	05FF A	3232			
	0600 A	2E33		22.3 NWC	
	0601 A	204E			
	0602 A	5743			

MODCOMP ASSEMBLY TABLES AND PARAMETERS.....

954	0603	A	3233	DFC	123.4	NPM
	0604	A	2E34			
	0605	A	204E			
	0606	A	504D			
955	0607	A	3234	DFC	124.0	NBA
	0608	A	2E30			
	0609	A	204E			
	060A	A	4241			
956	060R	A	4F4E	* LINE LABELS.....		
957	060C	A	5031	LLABE DFC		IONP1P2PKHRFR
	060D	A	5032			
	060E	A	504B			
	060F	A	4852			
	0610	A	4652			
958	0611	A	0000	* RSD NUMBERS FOR SIGNALS IN PROGRESS...		
959	061F	A	0001	UDRNO RES	14.0	
960	0620	A	0000	U-UDR DFC	001	MASTER NUMBER
961	0621	A	2041	LINEN DFC	0	OUTPUT LINE ID
962	0622	A	4C50	TYPEA DFC		ALPHA
	0623	A	4841			
963	0624	A	2042	TYPEB DFC		BRAVO
	0625	A	5241			
	0626	A	564F			
964	0627	A	2043	TYPEC DFC		COCO
	0628	A	4F43			
	0629	A	4F20			
965	062A	A	2044	TYPED DFC		DELTA
	062B	A	454C			
	062C	A	5441			
966	062D	A	0000	TEMP RES	5.0	
967	0632	A	0000	TPHAS RES	1.0	
968	0633	A	0000	TAMP RES	1.0	
969	0634	A	0000	TDIUR RES	1.0	
970	0635	A	0000	RPSV1 RES	7.0	
971	063C	A	0000	RPSV2 RES	6.0	
972	0642	A	204E	END1 DFC		NORMALLY
	0643	A	4F52			
	0644	A	4D41			
	0645	A	4C4C			
	0646	A	5920			
	0647	A	2020			
973	0648	A	2054	END2 DFC		TIME LAPSE
	0649	A	494D			
	064A	A	4520			
	064B	A	4C41			
	064C	A	5053			
	064D	A	4520			
974	064E	A	2053	END3 DFC		SIGNAL LOST

MODCOMP ASSEMBLY TABLES AND PARAMETERS.....

1006	0680	A	4F55						
	0681	A	5200						
	0682	A	2046	N05	DFC			I FIV , #4500	
	0683	A	4956						
1007	0684	A	4500						
	0685	A	2053	N06	DFC			I SIX , 0	
	0686	A	4958						
	0687	A	0000						
1008	0688	A	2053	N07	DFC			I SEVEN , 0	
	0689	A	4556						
	068A	A	454E						
	068B	A	0000						
1009	068C	A	2045	N08	DFC			I EIGHT , 0	
	068D	A	4947						
	068E	A	4854						
	068F	A	0000						
1010	0690	A	204E	N09	DFC			I NIN , #4500	
	0691	A	494E						
	0692	A	4500						
1011	0693	A	2046	N010	DFC			I FRONT , 0	
	0694	A	524F						
	0695	A	4E54						
	0696	A	0000						
1012	0697	A	2042	N011	DFC			I BAC , #4800	
	0698	A	4143						
	0699	A	4800						
1013	069A	A	2050	N012	DFC			I PEAL , #4800	
	069B	A	4541						
	069C	A	4800						
1014	069D	A	2048	N013	DFC			I HAL , #4600	
	069E	A	414C						
	069F	A	4600						
1015	06A0	A	2046	N014	DFC			I FUL , #4C00	
	06A1	A	554C						
	06A2	A	4C00						
1016	06A3	A	2048	N015	DFC			I HDU , #5200	
	06A4	A	4F55						
	06A5	A	5200						
1017	06A6	A	204C	N016	DFC			I LOS , #5400	
	06A7	A	4F53						
	06A8	A	5400						
1018	06A9	A	204D	N017	DFC			I MICROSECONDI , #5300	
	06AA	A	4943						
	06AB	A	524F						
	06AC	A	5345						
	06AD	A	434F						
	06AE	A	4E44						
	06AF	A	5300						
1019	0680	A	2041	N018	DFC			I ADVANCE , 0	
	06B1	A	4456						

MODCOMP ASSEMBLY TABLES AND PARAMETERS.....

0682	A	414E				
0683	A	4345				
0684	A	0000				
0685	A	2052	N019	DFC		RETAR1, #4400
0686	A	4554				
0687	A	4152				
0688	A	4400				
0689	A	2050	N020	DFC		PLUI, #5300
068A	A	4C55				
068B	A	5300				
068C	A	204D	N021	DFC		MINUS1, 0
068D	A	494E				
068E	A	5553				
068F	A	0000				
06C0	A	2044	N022	DFC		D1, #4200
06C1	A	4200				
06C2	A	2053	N023	DFC		SECOND ONSE1, #5400
06C3	A	4543				
06C4	A	4F4E				
06C5	A	4420				
06C6	A	4F4E				
06C7	A	5345				
06C8	A	5400				

MODCOMP ASSEMBLY		VALUES FOR U:RP3 REPORTS.....										U.CLS
1026	06C9	A	0000	ZCLS	RES	14,0	POINTS TO TYPE					
1027	06D7	A	0000	ZTYPE	RES	14,0	U.FRQ					
1028	06E5	A	0000	ZFRQ	RES	28,0	COMPUTES					
1029	0701	A	0000	CALLT	RES	14,0	M.STA					
1030	071D	A	0000	ZSTA	RES	28,0	U.OTM					
1031	072B	A	0000	ZOTM	RES	28,0	U.OTB					
1032	0747	A	0000	ZOTB	RES	14,0	U.OAM					
1033	0763	A	0000	ZOAM	RES	14,0	W.OST					
1034	0771	A	0000	ZOST	RES	14,0	U.ODR					
1035	077F	A	0000	ZODR	RES	14,0	W.ONR					
1036	078D	A	0000	ZONR	RES	28,0	W.PTM					
1037	079B	A	0000	ZPTM	RES	14,0	W.PAM					
1038	07B7	A	0000	ZPAM	RES	28,0	W.RAM					
1039	07C5	A	0000	ZRTM	RES	14,0	W.RAM					
1040	07E1	A	0000	ZRAM	RES	14,0	W.RFZ					
1041	07EF	A	0000	ZRFZ	RES	14,0	U.TRK					
1042	07FD	A	0000	ZTR	RES	14,0	U.OSG					
1043	0808	A	0000	ZOSG	RES	14,0	U.OFZ					
1044	0819	A	0000	ZOFZ	RES	14,0	U.AMP					
1045	0827	A	0000	ZFAM	RES	14,0	U.LFZ					
1046	0835	A	0000	ZFFZ	RES	28,0	U.LTM					
1047	0843	A	0000	ZFTM	RES	14,0						
1048	085F	A	0000	INPRG	RES	14,0						
1049	086D	A	0000	ZEND	RES	14,0						
1050	087R			END								

TOTAL ASSEMBLY ERRORS = 0 SIZE = 2171

```

1          PGM          U$RED
2          INT          U:RED
3          EXT          U.CST,U.AST,U.TRK,U.DIU,E:SKD
4          EXT          E:028,E.28F,E:LOG,U.FRQ,U.IHB
5          EXT          E:FRT,M.STA
6          CAT NO.     400100-007
7          * U$RED CHECKS FOR CHANNELS ON THE SAME FREQUENCY.
8          * THOSE WHICH ARE, ARE COMPARED FOR CRITICAL
9          * PROCESSING PARAMETER IDENTITY. THE LEVELS OF
10         * CHECKING ARE:
11         * 1. CHANNEL STATUS (U.CST)
12         * 2. ANALYSIS STATUS (U.AST)
13         * 3. TRACKING RATES (U.TRK)
14         * 4. DIURNAL SLOPE (U.DIU)
15         U:RED ZRR,6  6C66
16         STM,6       FLAG
17         RED02 TBMB,0,6  U.IHB,RED01  SKIP CHAN IF INHIBITED
18         TRR,7,6
19         ABR,7,15
20         RED04 TBMB,0,7  U.IHB,RED03  START 2ND CHAR AT 1ST + 1
21         * WE HAVE 2 UNHIBITED CHANS, LET'S SEE IF THEY'RE
22         * ON THE SAME FREQUENCY.
23         TRR,1,6
24         LLS,1,1
25         TRR,2,7
26         LLS,2,1
27         LDM,3,1      U.FRQ
28         SUM,3,2      U.FRQ
29         TRPB,3,3     RED03          BRU IF NOT SAME
30         ABR,1,15
31         ABR,2,15
32         LDM,3,1      U.FRQ
33         SUM,3,2      U.FRQ
34         TRRB,3,3     RED03          TENTHS NOT SAME
35         * IF EITHER CHANNEL HAS NO CARDIOIC, IT WILL MATCH
36         * THE OTHER; IF BOTH DO, THEY MUST BE ON THE SAME
37         * PATH.
38         LDM,3,6      M.STA
          E536
          001E X 000C

```

39	001F	EA30	ETI,3	#FFC7	
40	0020	FFC7	XOI,3	#0038	
41	0021	EC30	TRRB,3,3	\$+4	
42	0022	A 0038	BRU	RED05	CHAN IN 6 NONE
43	0023	R 7033	LDM,3,7	M. STA	
44	0024	R 0027	ETI,3	#FFC7	
45	0025	E700	XOI,3	#0038	
46	0026	R 0036	TRRB,3,3	\$+3	
47	0027	E537	HOP,RED05		CHAN IN 7 NONE
48	0028	X 000C	* NOW, MAKE SURE BOTH OF THESE CHANNELS ARE MONITORING		
49	0029	EA30	* THE SAME PATH.		
50	002A	FFC7	LDM,3,6	M. STA	
51	002B	EC30	XOM,3,7	M. STA	
52	002C	A 0038	TBRB,3,10	RED03	
53	002D	R 7033			
54	002E	R 0030			
55	002F	F707			
56	0030	E536			
57	0031	X 000C			
58	0032	E437			
59	0033	X 000C			
60	0034	763A			
61	0035	R 006C			
62	0036	8210			
63	0037	R 00C9	LDM,3,6	U. CST	
64	0038	E536	SUM,3,7	U. CST	
65	0039	X 0001	TRRB,3,3	FLEV1	BRU ON FAILURE
66	003A	E137	LDM,3,6	U. AST	
67	003B	X 0001	SUM,3,7	U. AST	
68	003C	R 7033	TRRB,3,3	FLEV2	BRU ON FAILURE
69	003D	R 0084			
70	003E	E536			
71	003F	X 0002			
72	0040	E137			
73	0041	X 0002			
74	0042	R 7033			
75	0043	R 0088			
76	0044	E536			
77	0045	X 0003			
78	0046	E137			
79	0047	X 0003			

* WE HAVE NOW ASCERTAINED THAT THE CHANS IN R6 AND R7 ARE ON THE SAME FREQUENCY. LET'S CHECK AND SEE IF THEY ARE AT THE SAME LEVEL OF PROCESSING.
 * THE CHAN STATUS SHOULD BE THE SAME (U. CST)
 RED05 0BMM,1 FLAG

* THE ANALYSIS STATUS SHOULD BE THE SAME (U. AST)
 LDM,3,6 U. AST
 SUM,3,7 U. AST

* THE DIFFERENCE IN TRACKING RATES SHOULD BE SMALL.
 LDM,3,6 U. TRK
 SUM,3,7 U. TRK

69	0048	7630	TBRB,3,0	\$+3	
	0049	004R			
70	004A	F702	HOP,\$+2		
71	004B	6F33	TTR,3,3		
72	004C	E930	SUI,3	#3	
	004D	0003			
73	004E	7630	TBRB,3,0	\$+4	
	004F	0052			
74	0050	E700	BRU	FLEV3	
	0051	008C			
75					
76					
77					
78					
79					
80	0052	E536	LDM,3,6	U. TRK	
	0053	0003			
81	0054	E930	SUI,3	5	(MUST BE > 4)
	0055	0005			
82	0056	7630	TBRB,3,0	RED03	TOO LOW
	0057	006C			
83	0058	E537	LOM,3,7	U. TRK	
	0059	0003			
84	005A	E930	SUI,3	5	
	005B	0005			
85	005C	7630	TBRB,3,0	RED03	TOO LOW
	005D	006C			
86	005E	E536	LOM,3,6	U. DIU	
	005F	0004			
87	0060	E137	SUM,3,7	U. DIU	
	0061	0004			
88	0062	7630	TBRB,3,0	\$+3	
	0063	0065			
89	0064	F702	HOP,\$+2		
90	0065	6F33	TTR,3,3		
91	0066	E930	SUI,3	#0	
	0067	0000			
92	0068	7630	TBRB,3,0	RED03	ALL LEVELS PASS
	0069	006C			
93	006A	E700	BRU	FLEV4	
	006B	0090			
94	006C	607F	RE003 ABR,7,15		NEXT 2ND CHANNEL
95	006D	ED10	LDI,1	14	
	006E	000E			
96	006F	7917	SURB,1,7	RED04	
	0070	0008			
97	0071	606F	RED01 ABR,6,15		NEXT 1ST CHANNEL
98	0072	ED10	LDI,1	13	
	0073	000D			
99	0074	7916	SURB,1,6	RED02	

* THE DIFFERENCE IN DIURNAL ESTIMATES SHOULD

* LIKEWISE BE SMALL.

* IF THE NOISE LEVEL IS SUFFICIENT TO REQUIRE LOW

* TRACK RATES, THE VALUE OF COMPARING DIURNALS IS

* QUESTIONABLE, SO WE WON'T.

MODCOMP ASSEMBLY

126	00A6	6C11	ZRR,1	
127	00A7	E610	STM,1	E:28F
	00A8	0007		
128	00A9	E7E0	BLM,14	E:028
	00AA	X 0006		
129	00AB	R 00AE	DFC	FMSG
130	00AC	E700	BRU	RED03
	00AD	R 006C		
131	00AE	A 000A	DFC	#000A,1123 123456 RED FAILURE,1
	00AF	A 3132		
	00B0	A 3320		
	00B1	A 3132		
	00B2	A 3334		
	00B3	A 3536		
	00B4	A 2020		
	00B5	A 5245		
	00B6	A 4420		
	00B7	A 4641		
	00B8	A 494C		
	00B9	A 5552		
	00BA	A 452C		
132	00BB	A 2043	DFC	I CHANS A AND B ON LEVEL 1 I,0
	00BC	A 4841		
	00BD	A 4E53		
	00BE	A 2041		
	00BF	A 2041		
	00C0	A 4E44		
	00C1	A 2042		
	00C2	A 204F		
	00C3	A 4E20		
	00C4	A 4C45		
	00C5	A 5645		
	00C6	A 4C20		
	00C7	A 3120		
	00C8	A 0000		
133	00C9	A 0000	FLAG	0
134	00CA		DFC	
			END	

TOTAL ASSEMBLY ERRORS = 0 SIZE = 202

```

1      PGM          U$PST
2      INT          U:PSST
3      EXT          U:GAN,U:TRK,U:BLI,E:35I,E:IOF
4      EXT          E:035,U:CNI,E:SKD,E:MIT,E:LOG
5      CAT NO.     400100-004
6      * THIS PROGRAM ALLOWS THE OPERATOR TO MAKE NEEDED ADJUSTMENTS TO
7      * THE TRACK RATE, GAIN, AND BLANKING LEVEL BY TYPING IN THE
8      * DESIRED VALUE FOR EACH ON THE ASR 35 TELETYPEWRITER. THE
9      * MACHINE RESPONDS BY EITHER CARRYING OUT THE DESIRED FUNCTION
10     * OR EXPLAINING WHY IT COULDN'T.
11     * THE OPERATOR CALLING SEQUENCE IS..... OPC: (HE THEN TYPES)
12     * A THRU N FOLLOWED BY A COMMA FOR CHANNEL DESIGNATION,
13     * A SINGLE LETTER FOR FUNCTION; B = BLANKING CHANGE
14     *                                     G = GAIN CHANGE
15     *                                     T = TRACKING RATE CHANGE
16     * THIS FUNCTION SHOULD BE FOLLOWED BY A COMMA, AND A
17     * TWO DIGIT VALUE. (SHOULD BE POSITIVE AND WITHIN THE
18     * FOLLOWING LIMITS) R = 00 THRU 63
19     *                                     G = 00 THRU 24
20     *                                     T = 00 THRU 09
21     * THE TWO DIGIT VALUE SHOULD BE FOLLOWED BY A CR.
22     * *****
23     * *****
24     * *****
25     * ***** EXAMPLE:  OPC:B,G,08 *****
26     * *****
27     * *****
28     * *****
29     * ***** U:PST LDI,1 ***** GET CHANNEL
30     * *****
31     * ***** ABR,1,13 *****
32     * ***** LDX,6,1 *****
33     * ***** SUI,6 ***** #41 CONVERT LETTER TO NUMBER
34     * ***** TBRB,6,0 ***** GOOF1
35     * ***** TRR,5,6 ***** AND HOLD IN R6
36     * ***** SUI,5 ***** IS CHANNEL NUMBER OK?
37     * ***** TBRB,5,0 ***** MAY1 IF YES CHECK FOR SIGNAL IN PROGRESS
38     * ***** GOOF1 ZRR,2 *****
39     * ***** STM,2 ***** E:IOF
40     * ***** BLM,14 ***** E:035 IF NO SAY SO...
41     * ***** DFC ***** ILLCH
42     * ***** BRU ***** E:SKD

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MODCOMP ASSEMBLY

71	0045	E700	BRU	E:SKD	
	0046	0008			
72	0047	6C22	ZRR,2		
73	0048	E620	STM,2	E.IOF	
	0049	0005			
74	004A	E7E0	BLM,14	E:035	OUTPUT MESSAGE TO OPER.
	004B	0006			
75	004C	R 00C8	DFC	NOPARSIG IN PROGRESS.....
76	004D	E700	BRU	E:SKD	TOO BAD, MAYBE NEXT TIME
	004E	X 0008			
77	004F	6C22	ZRR,2		
78	0050	E620	STM,2	E.IOF	
	0051	X 0005			
79	0052	E7E0	BLM,14	E:035	ILLEGAL FUNCTION MESSAGE
	0053	X 0006			
80	0054	R 00DF	DFC	ILFUN	
81	0055	E700	BRU	E:SKD	TRY AGAIN SOME OTHER TIME
	0056	X 0008			
82	0057	FDB1	LDX,11,1		GET NEXT CHAR
83	0058	E9B0	SUI,11	#2C	IS IT A COMMA
	0059	A 002C			
84	005A	7DB8	TRRB,11,11	\$+4	
	005B	R 005E			
85	005C	601F	ABR,1,15		YES.....
86	005D	FF0E	BRX,14		
87	005E	6C22	ZRR,2		NO, LOG INVALID FORMAT
88	005F	E620	STM,2	E.IOF	
	0060	X 0005			
89	0061	E7E0	BLM,14	E:035	
	0062	X 0006			
90	0063	R 0107	DFC	INFMT	
91	0064	E700	BRU	E:SKD	RETURN
	0065	X 0008			
92	0066	7640	NUMCK TBRB,4,0	00PS	NUMBER TOO LOW, SAY SO
	0067	R 003F			
93	0068	E940	SUI,4	10	
	0069	A 000A			
94	006A	7640	TBRB,4,0	\$+4	NUMBER TOO HIGH, SAY SO
	006B	R 006E			
95	006C	E700	BRU	00PS	
	006D	R 003F			
96	006E	FF0E	BRX,14		

149 * CLEVER LITTLE MESSAGES TO LET THE OPERATOR IN ON WHAT'S HAPPENING
150 ILLCH DFC #000A, IINVALID CHANNEL I, #000A, #0000

00BD A 000A
00BE A 494E
00BF A 5641
00C0 A 4C49
00C1 A 4420
00C2 A 4348
00C3 A 414E
00C4 A 4E45
00C5 A 4C20
00C6 A 000A
00C7 A 0000

151 NOPAR DFC #000A, IIND PARM. CHANGE PERMITTED, SIG. IN PROG. I

00C8 A 000A
00C9 A 4E4F
00CA A 2050
00CB A 4152
00CC A 4D2E
00CD A 2043
00CE A 4841
00CF A 4E47
00D0 A 4520
00D1 A 5045
00D2 A 524D
00D3 A 4954
00D4 A 5445
00D5 A 442C
00D6 A 2053
00D7 A 4947
00D8 A 2E20
00D9 A 494E
00DA A 2050
00DB A 524F
00DC A 472E

DFC #000A, #0000

152 00DD A 000A
153 00DE A 0000
00DF A 000A
00E0 A 494E
00E1 A 5641
00E2 A 4C49
00E3 A 4420
00E4 A 4655
00E5 A 4E43
00E6 A 5449
00E7 A 4F4E
00E8 A 000A
00E9 A 0000

ILFUN DFC #000A, IINVALID FUNCTIONI, #000A, #0000

ILVAL DFC #000A, ICANNOT USE NEG. VALUES I, #000A, #0000

154 00EA A 000A
00EB A 4341
00EC A 4E4E
00FD A 4F54

OUTPUT GOODIES

MODCOMP ASSEMBLY

```

00EE A 2055
00EF A 5345
00F0 A 204E
00F1 A 4547
00F2 A 2E20
00F3 A 5641
00F4 A 4C55
00F5 A 4553
00F6 A 000A
00F7 A 0000
00F8 A 0D0A
00F9 A 5641
00FA A 4C55
00FB A 4520
00FC A 4E4F
00FD A 5420
00FE A 5749
00FF A 5448
0100 A 494E
0101 A 204C
0102 A 494D
0103 A 4954
0104 A 5320
0105 A 0D0A
0106 A 0000
0107 A 0D0A
0108 A 494E
0109 A 5641
010A A 4C49
010B A 4420
010C A 464F
010D A 524D
010E A 4154
010F A 0D0A
0110 A 0000
0111

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#0D0A, I VALUE NOT WITHIN LIMITS I, #0D0A, #0000

ILLVL DFC

155

#0D0A, I INVALID ID FORMAT I, #0D0A, #0000

INFMT DFC

156

END

157

TOTAL ASSEMBLY ERRORS = 0 SIZE = 273

```

1  M:ESC
2  M:INT,M.TM1,M:ESC,M.STA
3  E:SKD,E:LOG,U.IHB,U.CST,U.RTY
4  U.END,U.CLS,U.TRF,U.OTM
5  U.CCN,U.TRL
6  * M:INT IS THE INTERRUPT ROUTINE ENTERED WHEN THE OPERATOR CHANGES
7  * A RECEIVER FREQUENCY OR CARDIOID CONFIGURATION, OR CHANGES EITHER
8  * THE MAINTENANCE OR AUTO/MANUAL SWITCHES, OR THE SENSORS DISCOVER
9  * AN INE CONDITION. THE ROUTINE SETS A FIVE SECOND TIMER IN THE
10 * SYSTEM CLOCK ROUTINE. WHEN THE TIMER RUNS OUT, M:ESC IS SCHEDULED.~
11 M:INT STM,2 $+7 SAVE R2
12 LDI,2 -5
13 STM,2 M.TM1 SET M.TM1=5
14 LDI,2 0 RESTORE R2
15 CIR CLEAR INTERRUPT AND RETURN
16 * M:ESC IS THE EQUIPMENT STATUS CHANGE MONITOR ROUTINE. THIS ROUTINE
17 * CHECKS THE STATUS OF FREQUENCY, CARDIOID, INE, MAINTENANCE, AND
18 * AUTO/MANUAL FOR EACH CHANNEL AND LOGS THE NEW VALUE ON THE ASR 28.
19 * ANY PROCESSING STATUS CHANGES WHICH MAY RESULT FROM THE EQUIPMENT
20 * STATUS ARE ALSO MADE.
21 M:ESC LDI,2 -14 SET UP TO READ DATA
22 LDI,5 #4350 INIT COMMANDS
23 STM,5 READ1 OCD COMMANDS
24 LDI,5 #4F30
25 STM,5 READ2 IDD COMMAND
26 LDI,5 #4000 CONTROL WORD
27 READ1 OCD,5,0
28 READ2 IDD,3,0
29 XOI,3 #FFFC
30 STM,3,2 INWRD+14 STORE DATA IN BUFFER
31 ABMM,15 READ1 UPDATE OCD
32 ABMM,15 READ2 UPDATE IDD
33 ABR,5,15 UPDATE CONTROL WORD
34 ABR,2,15 LOOP THRU 14 CHANNELS
35 LDI,2 -14 CHANNEL POINTER

```


69	004B	A	0E0A	004B	4,5	GET CHANNEL POINTER
70	004C		0845	TBRB,3,14		GOING IN OR COMING OUT OF MAINT.
	004D	R	763E		ESC10-2	
	004E	R	0055			
71	004F		605F	ABR,5,15		MAKE MSG. NO. 11 IF NOW OUT OF MAINT.
72	0050	R	763D	TBRB,3,13	ESC10	DO NOT RESET IF INE STILL ON
73	0051	R	0057	BLM,7	RESET	RESET PARAMETERS TO ALLOW CHAN UP
	0052	R	E770			
	0053	R	00A2			
74	0054		F703	HOP,\$,3	REST2	SET INHIBIT FLAG FOR NEW MAINT COND
75	0055	R	E770	BLM,7	E:LOG	LOG MAINT. CHANGE
76	0056	R	008B	ESC10 BLM,4		
	0057	X	E740			
	0058	X	0002	ZBR,6,14	ESC5	RESET MAINT. DETECTION BIT
77	0059		626E	BRU		RETURN
78	005A	R	E700			
	005B	R	0034			
79						
80	005C		0952	* CHANGE IN INE STATUS FOUND		
81	005D		E850	CHINE MBL,5,2		
	005E	A	0E0C	ADI,5	#0E0C	SET UP CHAN/MSG NO. FOR E:LOG
82	005F		0845	MBR,4,5		SET CHANNEL POINTER
83	0060	R	763D	TBRB,3,13	ESC11-2	GOING IN OR COMING OUT OF INE
	0061	R	0068			
84	0062		605F	ABR,5,15		MAKE MSG. NO. 13 IF NOW OUT OF INE
85	0063	R	763E	TBRB,3,14	ESC11	DO NOT RESET IF STILL IN MAINT.
	0064	R	006A			
86	0065	R	E770	BLM,7	RESET	RESET PARMS AND ALLOW CHAN TO COME UP
	0066	R	00A2			
87	0067		F703	HOP,\$,3	REST2	SET INHIBIT FLAG FOR NEW INE
88	0068	R	E770	BLM,7	E:LOG	LOG INE CHANGE
	0069	R	008B			
89	006A	X	E740	ESC11 BLM,4		
	006B	X	0002	ZBR,6,13	ESC6	RESET THE DETECTION BIT
90	006C		626D	BRU		RETURN
91	006D	R	E700			
	006E	R	0036			
92						
93	006F		6D53	* CHANGE IN CARDIOIC CONFIGURATION FOUND		
94	0070		2953	CHCAR TRP,5,3		GET NEW VALUE
95	0071	A	FF8	RLS,5,3	#FFF8	
	0072	A	FFF8	ETI,5		
96	0073	R	E650	STM,5	ESC9	
	0074	R	007A			
97	0075		0952	MBL,5,2	#0E02	
98	0076	A	E850	ADI,5		
	0077	A	0E02			
99	0078	X	E740	BLM,4	E:LOG	
	0079	X	0002			
100	007A	A	0000	ESC9 DFC	0	

101	007B	E700	BRU	ESC7	ESC7
102	007C	R 003A			
103	007D	6D53	* FREQUENCY SETTING CHANGE FOUND		
104	007E	E650	CHFRQ TRR,5,3		
105	007F	R 0088	STM,5	ESC8	
106	0080	0952	MBL,5,2		
107	0081	E850	ADI,5	#0E01	
108	0082	A 0E01			
109	0083	0845	MBR,4,5		RESET PARMS TO ALLOW NEW CHAN UP
110	0084	E770	RLM,7	REST1	
111	0085	R 00A4			
112	0086	E740	BLM,4	E:LOG	LOG NEW FREQ SETTING
113	0087	X 0002			
114	0088	A 0000	ESC8	0	
115	0089	E700	BRU	ESC3	GO TO NEXT CHAN
116	008A	R 002A			
117	008B	8204	REST2 ORMM,0,4	U.IHB	SET INHIBIT BIT
118	008C	X 0003			
119	008D	6CAA	ZRR,10		
120	008E	E6A4	STM,10,4	U.END	RESET PROCESSING PARAMETERS
121	008F	X 0006			
122	0090	E6A4	STM,10,4	U.CLS	
123	0091	X 0007			
124	0092	E6A4	STM,10,4	U.TRF	
125	0093	X 0008			
126	0094	E6A4	STM,10,4	U.CCN	
127	0095	X 000A			
128	0096	6D1A	TRP,1,10		
129	0097	2D11	LLS,1,1		
130	0098	E6A1	STM,10,1	U.OTM	
131	0099	X 0009			
132	009A	601F	ABR,1,15		
133	009B	E6A1	STM,10,1	U.OTM	
134	009C	X 0009			
135	009D	F707	HOP,REST1		
136	009E	EDA0	LDI,10	5	
137	009F	A 0005			
138	00A0	E6A4	STM,10,4	U.TRL	
139	00A1	X 000B			
140	00A2	8104	RESET ZRMM,0,4	U.IHB	RESET CHAN PROCESSING INHIBIT FLAG
141	00A3	X 0003			
142	00A4	ED10	REST1 LDI,1	4	
143	00A5	A 0004			
144	00A6	E614	STM,1,4	U.CST	SET CHANNEL STATUS DOWN (4)
145	00A7	X 0004			
146	00A8	ED10	LDI,1	-18	
147	00A9	A FFE4			
148	00AA	E614	STM,1,4	U.RTY	RESET RECOVERY TALLY TO -10
149	00AB	X 0005			

131	00AC		FF07	BRX,7						
132	00AD	A	0000	M,TMI	DFC					RETURN
133		R	00AE	M,STA	EQU					DELAY TIMER
134	00AE	A	0000	SVWRD	RES					CURRENT STATUS VALUES
135	00BC	A	0000	INWRD	RES					INPUT STATUS VALUES
136	00CA				END					

0
\$ 14,0
14,0

TOTAL ASSEMBLY EPRORS = 0 SIZE = 202


```

1      0000      E7E0
2      0001      X 0001
3      0002      R 00C2
4      0003      ED20
5      0004      A 400E
6      0005      432E
7      0006      4F4E
8      0007      0824
9      0008      E820
10     0009      A 2030
11     000A      E620
12     000B      R 00D3
13     000C      0C44
14     000D      2844
15     000E      2D44
16     000F      2C44
17     0010      E840
18     0011      A 3030
19     0012      E640
20     0013      R 00D4
21     0014      6C22
22     0015      E620
23     0016      X 000A
24     0017      E7E0
25     0018      X 0002
26     0019      R 00BF
27     001A      ED20
28     001B      A 5550
29     001C      E620
30     001D      R 0108
31     001E      6C22
32     001F      6C33
33     0020      E540
34     0021      X 0005
35     0022      E630
36     0023      X 0005
37     0024      ED50
38     0025      A 0064

```

M\$LEV
M:LEV
E:FRT,E:028,E:BAD,E:SKD,E:TM3
M:UPT,M:TRT,M:GNT,E:BDF,E:28F
CAT NO. 40000-010
M\$LEV EXAMINES THE DATA COLLECTED BY E\$MIN AND COMPUTES STATISTICAL INFORMATION FOR EACH CHANNEL WHICH THE OPERATOR WILL USE TO DETERMINE PATH RELIABILITY.
* OUTPUT INCLUDES A CHANNEL BY CHANNEL LISTING OF:
* 1. PERCENT OF TIME CHANNEL WAS UP
* 2. PERCENT OF UP TIME CHANNEL WAS AT EACH TRACK RATE
* 3. PERCENT OF UP TIME CHANNEL WAS AT EACH GAIN SETTING
M:LEV BLM,14 GET CURRENT DOY/TIME GROUP
DFC LINE1+1
LDI,2 #400E
OCD,2,#E
IDD,4,#E
MBR,2,4
ADI,2 #2030
STM,2 LINE1+18 UPPER HALF
MLR,4,4
RLD,4,4
LLS,4,4
LLD,4,4
ADI,4 #3030
STM,4 LINE1+19
ZRR,2 E.28F
STM,2
BLM,14 OUTPUT FIRST LINE
DFC LINE0 LABEL UP TIME LINE
LDI,2 #5550
STM,2 BUFFER+1
ZRR,2 R2 FOR CHAN POINTER (0 - 13)
ZRR,3 R3 FOR OUTPUT BUFFER POINTER
LDM,4 DIVISOR TO DETERMINE PRCNT UP TIME
STM,3 E.TM3
LDI,5 E.TM3 RESET SYSTEM TIMER
100 R5 SCALE TO PERCENT (MPY*100)

38	0026	E5D2	LEV01	LDM,13,2	M.UPT	GET UP TIME VALUE FOR CHANNEL
39	0027	X 0006				PERCENT=(M.UPT*100/E.TM1)
40	0028	20C5	MPR,12,5			
41	0029	21C4	DVR,12,4			
42	002A	2FC1	LAS,12,1			
43	002B	69C4	SUR,12,4			
44	002C	76C0	TBPB,12,0	\$+3		
45	002D	R 002F				
46	002E	60DF	ABR,13,15			
47	002F	6DCD	TRR,12,13			
48	0030	E6C2	STM,12,2	UPFLG		STORE VALUE FOR LATER USE
49	0031	R 0126				
50	0032	E7E0	BLM,14	E:BAD		CONVERT VALUE TO BCD
51	0033	X 0003				
52	0034	E7E0	BLM,14	LPACK		STORE CHARS FOR OUTPUT
53	0035	R 0098				
54	0036	602F	ABR,2,15			MOVE POINTER TO NEXT CHANNEL
55	0037	EDB0	LDI,11	14		
56	0038	A 000E				
57	0039	79B2	SURB,11,2	LEV01		LOOP UNTIL LAST CHANNEL IS DONE
58	003A	R 0026				
59	003B	E7E0	BLM,14	E:028		LOG LINE OF PERCENT UP TIMES
60	003C	X 0002				
61	003D	R 0107	DFC	BUFFER		
62	003E	E7E0	* NOW ATTACK TRACK RATES AND GAINS			
63	003F	X 0002	BLM,14	E:028		OUTPUT TRACK RATE TO TTY
64	0040	R 00F6				
65	0041	ED70	DFC	LINE2		
66	0042	A 0064	LDI,7	100		R7 = 100
67	0043	ED10	LDI,1	10		R1 = INCREMENTAL VALUE BETWEEN CHANS~
68	0044	A 000A				
69	0045	6C44	ZRR,4			R4 VALUE POINTER
70	0046	8100	ZBMM,0	LFLAG		SET FLAG = 0 FOR TRACK RATE
71	0047	R 0134				
72	0048	ED50	LDI,5	#A		
73	0049	A 000A				
74	004A	E650	STM,5	ENDVL		SET END VALUE FOR TR SETTING = 10
75	004B	R 0135				
76	004C	ED50	LDI,5	M.TRT		BASE OF TRACK RATE TABLE
77	004D	X 0007				
78	004E	6C22	LEV02	ZRR,2		R2 IS CHANNEL POINTER
79	004F	6C33	ZRR,3			R3 IS OUTPUT BUFFER POINTER
80	0050	6DB5	TRP,11,5			TABLE POINTER FOR THIS SETTING
81	0051	6DC4	TRR,12,4			OUTPUT NEW LEVEL
82	0052	E8C0	ADI,12	#2030		
83	0053	A 2030				
84	0054	E6C0	STM,12	BUFR+1		STORE IN OUTPUT BUFFER
85	0055	R 0108				
86	0056	E5C2	LEV03	LDM,12,2	UPFLG	

71	0057	R	0126	TRR8,12,12	\$+3	WAS CHANNEL UP AT ALL
	0058	R	7DCC			
72	0059	R	F70C	HOP,LEV04		NO, OUTPUT '0'
73	005A		F00B	LDX,13,11		GET VALUE OF M.GNT FOR CHANNEL
74	005C		20C7	MPR,12,7		*100
75	005D		A1C2	DVM,12,2	M.UPT	/M.UPT
76	005E	X	0006			
77	005F		2FC1	LAS,12,1		
	0060		E1C0	SUM,12	M.UPT	
	0061	X	0006			
78	0062		76C0	T8R8,12,0	\$+3	
	0063	R	0065			
79	0064		60DF	ABR,13,15		
80	0065		60CD	TRR,12,13		
81	0066		E7E0	BLM,14	E:8AD	CONVERT TO ASCII DECIMAL
	0067	X	0003			
82	0068		E7E0	BLM,14	LPACK	PACK AND STORE FOR OUTPUT
	0069	R	0098			
83	006A		68B1	ADP,11,1		MOVE TABLE POINTER TO NEXT CHANNEL
84	006B		602F	ABR,2,15		MOVE POINTER TO NEXT CHANNEL
85	006C		EDAO	LDI,10	14	
	006D	A	000E			
86	006E		79A2	SUR8,10,2	LEV03	DO NEXT CHANNEL AT THIS LEVEL
	006F	R	0056			
87	0070		E7E0	BLM,14	E:028	
	0071	X	0002			
88	0072	R	0107	DFC	BUFR	
89	0073		605F	ABR,5,15		MOVE BASE POINTER FOR NEXT SETTING
90	0074		604F	ABR,4,15		INCREMENT SETTING
91	0075		6DA4	TRR,10,4		
92	0076		E1A0	SUM,10	ENDVL	LOOP TILL LAST SETTING DONE
	0077	R	0135			
93	0078		7DAA	TRR8,10,10	LEV02	
	0079	R	004E			
94	007A		8600	T8MR,0	LFLAG,LEV05	
	007B	R	0134			
	007C	R	008D			
95	007D		8200	08MM,0	LFLAG	SET FLAG BIT FOR GAIN
	007E	R	0134			
96	007F		E7E0	BLM,14	E:028	OUTPUT *GAIN* TO TTY
	0080	X	0002			
97	0081	R	00FE	DFC	LINE3	INCREMENTAL VALUE BETWEEN CHANNELS
98	0082		ED10	LDI,1	8	RESET VALUE
	0083	A	0008			
99	0084		6C44	ZRP,4		
100	0085		ED50	LDI,5	#8	
	0086	A	0008			
	0087		E650	STM,5	ENDVL	SET END VALUE FOR GAIN = 8
101	0088	R	0135			

MODCOMP ASSEMBLY

102	0089	ED50	LDI,5	M.GNT
103	008A X	0008	BRU	LFV02
104	008B R	004E	LEV05 LDI,2	265
105	008D A	0109	ZRP,3	
106	008F	6C33	STM,3,2	M.TRT
107	0090	E632	SRRB,2,15	LEV06
108	0091 X	0007	STM,3	M.TRT
109	0092 R	0090	BLM,14	E:028
110	0093 X	0007	DFC	TERMN
111	0094 R	0103	BRU	E:SKD
112	0095 X	0002		
113	0096	F7E0		
114	0097 X	0004		
115	0098 R	0103		
116	0099	E700		
117	009A X	0004		
118	009B	ED60		
119	009C A	0004		
120	009D	716F		
121	009E R	00A0		
122	009F	F70B		
123	00A0	EDC0		
124	00A1 A	FFD0		
125	00A2 X	E5D6		
126	00A3	0009		
127	00A4 R	78CD		
128	00A5	00AA		
129	00A6	81B6		
130	00A7 X	0009		
131	00A8 R	E700		
132	00A9	009D		
133	00AA A	ED60		
134	00AB X	0003		
135	00AC	E5C6		
136	00AD	0009		
137	00AE	09CC		
138	00AF	616F		
139	00B0	E0C6		
140	00B1 X	0009		
141	00B2 R	E6C3		
142	00B3	0109		
143	00B4	616F		
144	00B5	E5C6		
145	00B6 X	0009		
146	00B7	09CC		
147	00B8	616F		

* LPACK TAKES OUTPUT FROM E\$RAD AND PACKS IT IN ASCII CHARS FOR M\$LEV

* IN THE OUTPUT BUFFER.

LPACK LDI,6 4

R6 IS CHAR POINTER

LPCK SRRB,6,15 \$+3

HOP,LPCK1

LDI,12 -#30

LDM,13,6 E.BDF

ADR8,12,13 LPCK1

CHANGE LEADING 0 TO A SPACE CHAR

ZBMM,11,6 E.BDF

BRU LPCK

LPCK1 LDI,6 3 R6 IS CHAR POINTER AGAIN

GET FIRST CHAR

TO LEFT BYTE

MBL,12,12

SBR,6,15

ADM,12,6

GET SECOND CHAR TO RIGHT BYTE

STORE FOR OUTPUT

BUFFER+2

SBR,6,15

LDM,12,6

GET 3RD CHAR.

MBL,12,12

SBR,6,15

TO LEFT BYTE

132	00B9	E0C6	ADM,12,6	E.BDF	GET LAST CHAR IN RIGHT BYTE
	00BA	0009			
133	00BB	E6C3	STM,12,3	BUFFR+3	STORE FOR OUTPUT
	00BC	010A			
134	00BD	603E	ABR,3,14		SET UP FOR NEXT OUTPUT
135	00BE	FF0E	BRX,14		RETURN
136	00BF	A 000A	LINE0 DFC	#000A,#0A0A	
	00C0	A 0A0A	LINE1 DFC	#0A0A,1123 123456	PARA1
137	00C1	A 0A0A			
	00C2	A 3132			
	00C3	A 3320			
	00C4	A 3132			
	00C5	A 3334			
	00C6	A 3536			
	00C7	A 2020			
	00C8	A 5041			
	00C9	A 5241			
138	00CA	A 4D45	DFC		METER HISTORY, DET 5151
	00CB	A 5445			
	00CC	A 5220			
	00CD	A 4849			
	00CE	A 5354			
	00CF	A 4F52			
	00D0	A 592C			
	00D1	A 2044			
	00D2	A 4554			
	00D3	A 2035			
	00D4	A 3135			
139	00D5	A 0D0A	DFC		#0D0A,#0A43,1HAN A B C D E F G I
	00D6	A 0A43			
	00D7	A 4841			
	00D8	A 4E20			
	00D9	A 4120			
	00DA	A 2020			
	00DB	A 4220			
	00DC	A 2020			
	00DD	A 4320			
	00DE	A 2020			
	00DF	A 4420			
	00E0	A 2020			
	00E1	A 4520			
	00E2	A 2020			
	00E3	A 4620			
	00E4	A 2020			
	00E5	A 4720			
140	00E6	A 2020	DFC		I H I J K L M N ,#0D0A,#0000
	00E7	A 4820			
	00E8	A 2020			
	00E9	A 4920			
	00EA	A 2020			

```

00EB A 4A20
00EC A 2020
00ED A 4B20
00EE A 2020
00EF A 4C20
00FO A 2020
00F1 A 4D20
00F2 A 2020
00F3 A 4E20
00F4 A 0D0A
00F5 A 0000
00F6 A 0D0A
00F7 A 0D0A
00F8 A 5452
00F9 A 4143
00FA A 4B20
00FB A 5241
00FC A 5445
00FD A 0000
00FE A 0D0A
00FF A 0D0A
0100 A 4741
0101 A 494E
0102 A 0000
0103 A 0D0A
0104 A 0A0A
0105 A 0A0A
0106 A 0000
0107 A 0D0A
0108 RES
0125 A 0000
0126 A 0000
0134 A 0000
0135 A 0000
0136

```

141 LINE2 DFC #0D0A,#0D0A,|TRACK RATE|,#0000

142 LINE3 DFC #0D0A,#0D0A,|GAIN|,#0000

143 TERMIN DFC #0D0A,#0A0A,#0A0A,0

```

BUFFR DFC #0D0A
RES 29
DFC #0000
UPFLG RES 14,0
LFLAG DFC 0
ENDVL DFC 0
END

```

BIT 0=0 FOR TR, =1 FOR GAIN

TOTAL ASSEMBLY ERRORS = 0 SIZE = 310

73	0059	2C41	LLD,4,1				
74	005A	6D14	TRR,1,4				COPY INE BIT
75	005B	E840	ADI,4	#2030			
	005C	A 2030					
76	005D	E640	STM,4	LINE2+7			INE BIT
	005E	R 0170					
77	005F	6C44	ZRR,4				
78	0060	2C41	LLD,4,1				
79	0061	6B14	ORR,1,4				OR IN MAINT. BIT
80	0062	E840	ADI,4	#2030			
	0063	A 2030					
81	0064	E640	STM,4	LINE2+8			MAINT BIT
	0065	R 0171					
82	0066	ED40	LDI,4	#4D20			AUTO/MAN. = M
	0067	A 4D20					
83	0068	7650	TBRB,5,0	\$+4			
	0069	R 006C					
84	006A	ED40	LDI,4	#4120			AUTO/MAN. = A
	006B	A 4120					
85	006C	E640	STM,4	LINE2+10			
	006D	R 0173					
86							
87							
88							
89							
90	006E	761F	TBRB,1,15	SHORT			
	006F	R 00E5					
91	0070	6C44	ZRR,4				
92	0071	E556	LDM,5,6	U.GAN			DO GAIN
	0072	X 0008					
93	0073	ED10	LDI,1	10			
	0074	A 000A					
94	0075	2141	DVR,4,1				
95	0076	F850	ADI,5	#2030			
	0077	A 2030					
96	0078	E650	STM,5	LINE2+11			
	0079	R 0174					
97	007A	0944	MBL,4,4				
98	007B	E840	ADI,4	#3020			
	007C	A 3020					
99	007D	E640	STM,4	LINE2+12			
	007E	R 0175					
100	007F	E556	LDM,5,6	U. TRK			DO TRACK RATE
	0080	X 0009					
101	0081	0955	MBL,5,5				
102	0082	F850	ADI,5	#3020			
	0083	A 3020					
103	0084	E650	STM,5	LINE2+14			
	0085	R 0177					
104	0086	6C44	ZRR,4				DO BLANKING LEVEL

* SO MUCH FOR THE INFO FROM THE RECEIVER.
 * CHECK, IF EITHER MAINT OR INE IS SET,
 * TERMINATE LINE HERE.
 * OTHERWISE, LINE UP PROCESSOR CONTROLLED PARAMETERS.

105	0087	E556	LDM,5,6	U.BLL	
	0088	000A			
106	0089	2141	DVR,4,1		
107	008A	E850	ADI,5	#2030	
	008B	A			
108	008C	E650	STM,5	LINE2+15	
	008D	R			
109	008E	0944	MBL,4,4		
110	008F	E840	ADI,4	#3020	
	0090	A			
111	0091	E640	STM,4	LINE2+16	
	0092	R			
112					
113					
114	0093	E556	LDM,5,6	U.DIU	
	0094	000C			
115	0095	2041	MPR,4,1	*10	
116	0096	2A46	RAD,4,6		
117	0097	605F	ABR,5,15		
118	0098	2851	RAS,5,1		
119	0099	6DC5	TRR,12,5		
120	009A	E7E0	BLM,14	E:BAD	CONVERT TO ASCII DECIMAL
	009B	X			
121	009C	E540	LDM,4	E.BSG	
	009D	X			
122	009E	E640	STM,4	LINE2+17	STORE SIGN
	009F	R			
123	00A0	ED20	LDI,2	E.BDF	
	00A1	X			
124	00A2	FD42	LDX,4,2		
125	00A3	0944	MBL,4,4		
126	00A4	E840	ADI,4	#20	
	00A5	A			
127	00A6	E640	STM,4	LINE2+20	
	00A7	R			
128	00A8	602F	ABR,2,15		
129	00A9	FD42	LDX,4,2		
130	00AA	0944	MBL,4,4		
131	00AB	E840	ADI,4	#2E	
	00AC	A			
132	00AD	E640	STM,4	LINE2+19	
	00AE	R			
133	00AF	602F	ABR,2,15		
134	00B0	FD42	LDX,4,2		
135	00B1	602F	ABR,2,15		
136	00B2	FD52	LDX,5,2		
137	00B3	0955	MBL,5,5		
138	00B4	6854	ORR,5,4		
139	00B5	E650	STM,5	LINE2+18	
	00B6	R			

* NOW SET UP CURRENT VALUE FOR THE DIURNAL,
 * STANDART DEVIATION, AND AMPLITUDE.

MODCOMP ASSEMBLY

Line No.	Code	Label	U. SIG	GET SIGMA
140	00R7	ESC6		
141	00R8	000R	LDM,12,6	
142	00R9	E7F0	BLM,15	
143	00RA	0007		
144	00RB	0196	DFC	CONVERT FOR OUTPUT
145	00RC	E530	LDM,3	
146	00RD	0198	STM,3	
147	00RE	E630		
148	00RF	017E	LDM,3	
149	00C0	E530	TEMP+3	
150	00C1	0199	STM,3	
151	00C2	E630	LINE2+22	
152	00C3	017F	LDM,3	
153	00C4	E530	TEMP+4	
154	00C5	019A	STM,3	
155	00C6	E630	LINE2+23	
156	00C7	0180	BLM,14	
157	00C8	E7E0	E:FAM	
158	00C9	00FD		
159	00CA	0181		
160	00CB	E546	DFC	
161	00CC	0012	LDM,4,6	
162	00CD	614F	SBR,4,15	
163	00CE	2D41	LLS,4,1	
164	00CF	E534	LDM,3,4	
165	00D0	0198	STM,3	
166	00D1	E630	L2S1+1	
167	00D2	0185	ABR,4,15	
168	00D3	604F	LDM,3,4	
169	00D4	E534	STAT	
170	00D5	0198	STM,3	
171	00D6	E630	L2S1+2	
172	00D7	0186	E:028	OUTPUT LINE.
173	00D8	E7E0	BLM,14	
174	00D9	0002	STD2	
175	00DA	0169	DFC	
176	00DB	606F	ABR,6,15	
177	00DC	ED50	LDI,5	
178	00DD	000E	SURB,5,6	LOOP THRU 14 CHANNELS
179	00DE	7956	BLM,14	
180	00DF	001B	E:028	
181	00E0	E7E0	DFC	
182	00E1	0002	BRU	TERMINATION MSG.
183	00E2	0193	E:SKD	RETURN
184	00E3	E700		
185	00E4	0011	SHORT ZRR,2	
186	00E5	6C22	STM,2	
187	00E6	E620	LINE2+11	
188	00E7	0174		

* ALSO, SHOW THE CHANNEL STATUS FROM U.CST


```

183 *****
184 * E:FAM WILL TAKE THE AMPLITUDE OF THE CHANNEL
185 * SPECIFIED IN R6, CONVERTS IT FOR OUTPUT AND
186 * STORES THE RESULT IN THREE WORDS STARTING AT
187 * THE LOC SPECIFIED BY THE PARAMETER IN THE CALLING
188 * SEQUENCE:
189 *   BLM,14          E:FAM   CHAN IN R6
190 *   (LOC OF FIRST WCRD OF OUTPUT)
191 *   FORMAT: 1+00.001 ( IN DR )
192 *   E:FAM SFM,1    FAMS    V
193
194   A510
195   R 00FE          LDY,7,14
196   00FF          ABR,14,15
197   0100          STM,14          FSV14
198   0101          ABR,7,14
199   0102          LDM,5,6          U.AMP
200   0103          LDI,1          1000
201   0104          MPR,4,1
202   0105          RAD,4,11
203   0106          SUI,5          500
204   0107          LAS,5,2
205   0108          TRP,12,5
206   0109          BLM,14          E:BAD
207   0110          LDI,2          E.BDF
208   0111          LDY,4,2
209   0112          ABR,2,15
210   0113          LDY,5,2
211   0114          MBL,5,5
212   0115          ORR,5,4
213   0116          STX,5,7
214   0117          SBR,7,15
215   0118          ABR,2,15
216   0119          LDY,4,2
217   0120          MBL,4,4
218   0121          ADI,4          #002E
219   0122          STX,4,7
220   0123          SBR,7,15
221   0124          ABR,2,15
222   0125          LDY,5,2
223   0126          LDM,4          E.BSG
224   0127          MRL,4,4
225   0128          ADR,5,4
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```

GET STORE ADDR.
MOVE RET ADDR.

AMP*1000
/2,048

CONVERT TO DECIMAL

1.....001

1..0.001

GET SIGN

MODCOMP ASSEMBLY AMPLITUDE REFORMAT ROUTINE.....

224	0126	FE57	STX,5,7				
225	0127	A410	LFM,1	FAMSV			1+00.001
	0128	R 012C					RESTORE REGISTERS
226	0129	EDE0	LDI,14	0			
	012A	A 0000					
227	012A	R 012A	FSV14 EQU	\$-1			RETURN
228	012B	FF0E	BRX,14				
229	012C	A 0000	FAMSV RES	7,0			

OUTPUT INFORMATION.....

MDDCOMP ASSEMBLY

LINE#	DFC	RECEIVER STATUS, DET 5151	DIURNAL SIGMA AMPLITUDE, 0
231	DFC		
232	DFC		
233	DFC		
234	DFC		
235	DFC		
236	DFC		
237	DFC		

 #0DOA
 #0A0A, 1123 123456 CURRENT I

LINE# DFC
 LINE# DFC
 #0DOA
 #0A0A, 1123 123456 CURRENT I

#0DOA, #0A20

ICHAN FREQ C I M AM GAIN TR BLLI

DIURNAL SIGMA AMPLITUDE, 0

0133 A 0DOA
 0134 A 0A0A
 0135 A 3132
 0136 A 3320
 0137 A 3132
 0138 A 3334
 0139 A 3536
 013A A 2020
 013B A 4355
 013C A 5252
 013D A 454E
 013E A 5420
 013F A 5245
 0140 A 4345
 0141 A 4956
 0142 A 4552
 0143 A 2053
 0144 A 5441
 0145 A 5455
 0146 A 532C
 0147 A 2044
 0148 A 4554
 0149 A 2035
 014A A 3135
 014B A 0DOA
 014C A 0A20
 014D A 4348
 014E A 414E
 014F A 2046
 0150 A 5245
 0151 A 5120
 0152 A 4320
 0153 A 4920
 0154 A 4020
 0155 A 414D
 0156 A 2047
 0157 A 4149
 0158 A 4E20
 0159 A 5452
 015A A 2042
 015B A 4C4C
 015C A 2044
 015D A 4955
 015E A 524E
 015F A 414C
 0160 A 2053
 0161 A 4947
 0162 A 4D41
 0163 A 2041

MODCOMP ASSEMBLY OUTPUT INFORMATION.....

244	0196	A	0000	TEMP	RES	5,0
245	0198	A	2055	STAT	DFC	I UP C,NRC,R DOWNI
	019C	A	5020			
	019D	A	432C			
	019E	A	4E52			
	019F	A	432C			
	01A0	A	5220			
	01A1	A	444F			
	01A2	A	574E			
246	01A3				END	

TOTAL ASSEMBLY ERRORS = 0 SIZE = 419

```

1      PGM          E$LOG
2      INT          E:LOG,U,FRQ
3      EXT          E:O28,E:FRT,E:BAD,U,TRK,U,GAN
4      EXT          E:BDF,U:BLL,E:28F
5      * THIS IS THE LOG MESSAGE GENERATOR.  CHANGES IN SYSTEM STATUS OR
6      * PARAMETER VALUES ARE LOGGED ON THE STATION LOGGING TELEPRINTER.
7      * THE OUTPUT FORMAT IS.....
8      * CHAR 1-3.....DAY OF YEAR
9      * CHAR 4.....BLANK
10     * CHAR 5-10.....ZULU TIME OF DAY
11     * CHAR 11-12.....BLANK
12     * CHAR 13.....CHANNEL DESIGNATOR
13     * CHAR 14.....BLANK
14     * CHAR 15-16..TWO CHAR CODED MESSAGE (SEE MESSAGE TABLE)
15     * NEW VALUE, ETC. FOLLOWS FOR APPROPRIATE MESSAGES.
16     * THE CALLING SEQUENCE DEPENDS ON THE TYPE OF MESSAGE.
17     * FOR ALL MESSAGES, ONE PARAMETER IS PASSED VIA R5, INTERPRETED AS...
18     * BITS 0-7.....CHANNEL DESIGNATOR, 0 FOR CHAN A, 1 FOR B, ETC.
19     * BITS 8-15...MESSAGE NUMBER POINTS TO ENTRY IN MSG. TABLE
20     * FOR MESSAGES WITH NO NEW VALUE, ETC. NEEDED....
21     * BLM,4
22     * WHILE FOR MESSAGES WITH FURTHER INFORMATION...
23     * BLM,4
24     * DFC
25     * E:LOG BLM,14  E:FRT
26     * E7E0
27     * 0001 X
28     * 0002 R
29     * 0003 A510
30     * 0004 R
31     * 0005
32     * 0006 E870
33     * 0007 A
34     * 0008 E670
35     * 0009 R
36     * 000A EA50
37     * 000B A
38     * 000C E525
39     * 000D R
40     * 000E E620
41     * 000F R
42     * 0010
43     * 0011 R
44     * 0012 E700
45     * 0013 R
46     * 0014 715F
47     * 0015 R
48     * 0016 E700
49     * 0017 R
50     * 0018
51     * 0019 A
52     * 0020
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39	001A	7650	TBRB,5,0	PRMSG	MSG NO IS 2, 3, 4, 5.
	001B	004B			
40	001C	6C66	ZRR,6		
41	001D	E660	STM,6	OBUF1+2	STORE MSG TERMINATOR (#00)
	001E	R			
42	001F	6C22	ZRR,2		
43	0020	E620	STM,2	E.28F	
	0021	X			
44	0022	E7E0	BLM,14	E:028	LOG MSG ON ASR 28
	0023	X			
45	0024	R	DFC	OBUFF	
46	0025	A410	LFM,1	SAVRS	RESTORE REGISTERS
	0026	R			
47	0027	FF04	BRX,4		RETURN
48			* FREQUENCY CHANGE MESSAGE...		
49	0028	E740	FRMSG BLM,4	GETCH	GET CHANNEL IN R5
	0029	R			
50	002A	2D51	LLS,5,1		
51	002B	E578	LDM*,7	SAVRS+3	GET PARAMETERS
	002C	R			
52	002D	80F0	ABMM,15	SAVRS+3	MOVE RETURN AROUND PARM.
	002E	R			
53	002F	6C66	ZRR,6		
54	0030	2C62	LLD,6,2		FREQ*10 INTO R6
55	0031	2D64	LLS,6,4		
56	0032	2C64	LLD,6,4		FREQ*1 INTO R6
57	0033	ED40	LDI,4	#3939	
	0034	A			
58	0035	6946	SUR,4,6		RECOVER FREQ FROM 9'S COMPLIMENT
59	0036	6245	ZBR,4,5		
60	0037	6244	ZBR,4,4		
61	0038	E645	STM,4,5	U.FRQ	SAVE FOR OTHER USES
	0039	R			
62	003A	605F	ABR,5,15		
63	003B	E640	STM,4	OBUF1+2	STORE IN OUTPUT BUFFER
	003C	R			
64	003D	6C66	ZRR,6		
65	003E	2C64	LLD,6,4		FREQ*1 INTO R6
66	003F	ED40	LDI,4	#2E39	
	0040	A			
67	0041	6946	SUR,4,6		RECOVER VALUE FROM 9'S COMPLIMENT
68	0042	E645	STM,4,5	U.FRQ	SAVE REST OF FREQ.
	0043	P			
69	0044	E640	STM,4	OBUF1+3	STORE IN OUTPUT BUFFER
	0045	R			
70	0046	6C66	ZRR,6		
71	0047	E660	STM,6	OBUF1+4	STORE MSG TERMINATOR
	0048	R			
72	0049	E700	BRU	LOG1	OUTPUT MSG.
	004A	R			

73		E6C0		* P PARAMETER CHANGE	MSG....		
74	004B	R	0054	PRMSG STM,12	HLD12	SAVE R12	
75	004D	R	E5C8	LDM*,12	SAVRS+3	GET PARAMETER VALUE	
76	004E	R	00CC	ABMM,15	SAVRS+3	MOVE RETURN AROUND PARM.	
77	004F	R	80F0	BLM,14	E:BAD	CONVERT VALUE TO ASCII DECIMAL	
78	0051	X	0003	LDI,12	0		
79	0053	A	0000	HLD12 EQU	\$-1		
80	0054	R	ED20	LDI,2	E.BDF		
81	0055	X	0006	LDX,5,2		GET LOW ORDER DIGIT	
82	0057		FD52	ABR,2,15		GET HIGH ORDER DIGIT	
83	0058		602F	LDX,4,2		PACK TO ONE WORD	
84	0059		FD42	MBL,4,4		STORE IN OUTPUT BUFFER	
85	005A		0944	ORR,4,5			
86	005B		6845	STM,4	OBUF1+2		
87	005C	R	E640	ZRR,4			
88	005D		00C1	STM,4	OBUF1+3	STORE MSG TERMINATOR	
89	005E		6C44	BRU	LOGI	OUTPUT	
90	005F	R	E640				
91	0060	R	00C2				
92	0061	R	E700				
93	0062	R	001F				
94	0063	R	E740	* UP MESSAGE TIME, GIVE FREQ, TRACK RATE, GAIN, BLANKING LEVEL...	GETCH	CHANNEL POINTER IN R5	
95	0064	R	009A	UPMSG BLM,4			
96	0065		6D45	TRR,4,5			
97	0066		2D41	LLS,4,1		U.FRQ POINTER IN R4	
98	0067	R	E534	LDM,3,4	U.FRQ		
99	0068	R	00D0	STM,3	OBUF1+2	STORE UPPER HALF OF FREQ IN BUFFER	
100	0069	R	E630	ABR,4,15			
101	006A	R	00C1	LDM,3,4	U.FRQ		
102	006B		604F	STM,3	OBUF1+3	STORE LOWER HALF OF FREQ IN BUFFER	
103	006C	R	E534	LDM,3,5	U.TRK	GET TRACK RATE	
104	006D	R	00D0	ADI,3	#2C30	CONV DIGIT TO ASCII AND ADD COMMA	
105	006E	R	E630	STM,3	OBUF1+4		
106	006F	R	00C2	LDM,12,5	U.GAN		
107	0070	X	E535	BLM,14	E:BAD	CONVERT GAIN TO DECIMAL ASCII	
108	0071	X	0004				
109	0072	A	E830				
110	0073	A	2C30				
111	0074	R	E630				
112	0075	R	00C3				
113	0076	X	E5C5				
114	0077	X	0005				
115	0078	X	E7E0				
116	0079	X	0003				

104	007A	ED60	LDI,6	E.RDF					
105	007B	X 0006	LDX,3,6		GET LOW ORDER DIGIT				
106	007C	FD36	M8L,3,3						
107	007D	0933	ADI,3	#002C	ADD COMMA				
108	007E	E830	STM,3	0BUI1+6	STORE LOW PART OF GAIN				
109	007F	A 002C	ARR,6,15						
110	0081	R 00C5	LDX,3,6	#2C00	GET HIGH ORDER DIGIT				
111	0082	606F	ADI,3		ADD COMMA				
112	0083	FD36	STM,3	0BUI1+5	STORE HIGH PART OF GAIN				
113	0084	E830	LDM,12,5	U.BLL	GO AFTER BLANKING LEVEL				
114	0085	A 2C00	BLM,14	E:BAD	CONV BLANK LEVEL TO DECIMAL ASCII				
115	0086	E630	LDI,6	E.BDF					
116	0087	R 00C4	LDX,3,6		GET LOW ORDER DIGIT				
117	0088	E5C5	ABR,6,15		GET HIGH ORDER DIGIT				
118	0089	X 0007	LDX,2,6		PACK TO ONE WORD				
119	008A	E7E0	M8L,2,2	0BUI1+7	STORE IN OUTPUT BUFFER				
120	008B	X 0003	ORR,2,3						
121	008C	ED60	STM,2		MESSAGE TERMINATOR				
122	008D	X 0006	ZRR,2	LOG1	OUTPUT				
123	008E	FD36	STM,2	0BUI1+8					
124	008F	606F	BRU						
125	0090	FD26	GETCH LDI,6	0BUI1					
126	0091	0922	LDI,7	0					
127	0092	6B23	LBX,5,6		GET CHAN.				
128	0093	E620	SUI,5	#41					
129	0094	R 00C6	BRX,4		RETURN WITH CHAN. IN R5.				
130	0095	6C22	DFC	IUI	00-CHANNEL UP				
131	0096	E620	MSGTB	IICI	01-FREQUENCY CHANGED				
132	0097	R 00C7	DFC	ICCI	02-CARDIOID CHANGED				
133	0098	E700	DFC	ITCI	03-TRACKING RATE CHANGED				
134	0099	R 001F	DFC	IGCI	04-GAIN CHANGED				
135	009A	ED60	DFC	IBCI	05-BLANKING LEVEL CHANGED				
136	009B	R 00BF	DFC	IRSI	06-REDUNDANCY CHECK SUCCESSFUL				
137	009C	ED70	DFC	ISDI	07-SYSTEM DEAD IN SCHEDULER				
138	009D	A 0000	DFC	IMNI	08-MANUAL/AUTO TO MANUAL				
139	009E	AE56	DFC	IAUI	09-MANUAL/AUTO TO AUTO				
140	009F	E950							
141	00A0	A 0041							
142	00A1	FF04							
143	00A2	A 5520							
144	00A3	A 4643							
145	00A4	A 4343							
146	00A5	A 5443							
147	00A6	A 4743							
148	00A7	A 4243							
149	00A8	A 5253							
150	00A9	A 5344							
151	00AA	A 4D4E							
152	00AB	A 4155							


```

1      PGM          E$BDC
2      INT          E:RDC,E:BAD,E:RDF,E:BSG
3      * THIS UTILITY PROGRAM CONVERTS A BINARY VALUE IN R12 TO ITS ASCII
4      * DECIMAL (E:BAD) OR BCD (E:BDC) EQUIVALENT, LEAVING THOSE CHARS,
5      * LEAST SIGNIFICANT DIGIT FIRST, IN A FIVE WORD BUFFER (E:BUF).
6      * THE ASCII REPRESENTATION OF THE NUMBER'S SIGN IS STORED IN E:BSG.
7      * CALLING SEQUENCES ARE.....
8      *   BLM,14      E:BDC      FOR BCD CHARS
9      *   BLM,14      E:BAD      FOR ASCII CHARS
10     *   WITH BINARY NUMBER IN R12
11     E:BDC OBMM,0  CONVR      SET CONVERSION FLAG
12     HOP,$+3      CONVR      RESET CONVERSION FLAG
13     E:BAD ZBMM,0 CONVR      SAVE WORKING REGISTERS
14     SFM,1        SAVRS      IS NUMBER NEGATIVE?
15     TBRB,12,0   NEGNO      NO..
16     LDI,1        #202B     SET E.BSG TO '+'.
17     STM,1        E.BSG     YES..
18     HOP,$+6     #202D     SET E.BSG TO '-'.
19     NEGNO LDI,1  E.BSG     GET ABS (NUMBER) FOR NO.
20     STM,1        E.BSG     CHECK FOR ASCII OR BCD OUTPUT
21     TTR,12,12   -5        CONVR,$+5
22     LDI,1        10        #30      CONV. TO ASCII.
23     LDI,2        10        E:RDF+5
24     TRR,5,12    DIVLP      LOOP BACK TILL FOUR CHARS.
25     ZRR,4        DVF,4,2   RESTORE REGISTERS
26     DVB,4,2     TBM,0     RETURN
27     TBM,0       BRX,14    CODE I.D. FLAG
28     ADI,4        CONVR DFC
29     STM,4,1     SAVRS RES
30     ABRB,1,15  0
31     LFM,1      7
32     BRX,14
33     CONVR DFC
34     SAVRS RES

```

35	002E	A	0000	E.BSG DFC	0	NUMBER SIGN
36	002F			E.BDF RES	5	WORKING AND OUTPUT BUFFER
37	0034			END		

TOTAL ASSEMBLY ERRORS = 0 SIZE = 52

```

1      1      PGM          E$FRT
2      2      INT          E:FRT,E:FCT,E.TM1,E.TM2
3      3      CAT NO.    400000-007
4      4      * THIS ROUTINE PREPARES THE DAY/TIME GROUP AND STORES THE ASCII
5      5      * REPRESENTATION IN MEMORY STARTING AT THE LOCATION SPECIFIED
6      6      * IN THE PARAMETER. IF ENTRY IS MADE AT E:FRT, THE CURRENT TIME
7      7      * IS READ FROM THE WORLD TIME CLOCK AND IS USED. IF ENTRY IS MADE
8      8      * AT E:FCT, THE DAY/TIME GROUP IN E.TM1,E.TM2 IS USED. THE CALLING
9      9      * SEQUENCE FOR EACH FOLLOWS.
10     10     * BLM,14      E:FRT
11     11     * DFC        (ADDR. TO STORE ASCII CHARS. IN)
12     12     * BLM,14      E:FCT      (TIME IS IN E.TM1,E.TM2)
13     13     * DFC        (ADDR. TO STORE ASCII CHARS. IN)
14     14     E:FCT SFM,10 SAVER      USE R10 - R15
15     15     BRU        FMTIM
16     16     E:FRT SFM,10 SAVER      USE R10 - R15
17     17     LDI,11     #4000      SET UP TO READ DAY/TIME GROUP OFF WTC
18     18     OGD,11,#F
19     19     IDD,13,#F
20     20     TOR,13,13
21     21     STM,13
22     22     R          E.TM1
23     23     ODD,13,#F
24     24     TOR,13,13
25     25     STM,13      E.TM2
26     26     FMTIM LDX,10,14
27     27     ABMM,15     SAVER*4
28     28     ZRR,12      E.TM1
29     29     LDM,13
30     30     LLD,12,2
31     31     LLS,12,4
32     32     LLD,12,4
33     33     ADI,12      #3030
34     34     STX,12,10
35     35     ABR,10,15
36     36     ZRR,12
37     37     LLD,12,4
38     38     MBL,12,12
39     39     ADI,12      #3020
40     40     STX,12,10
41     41     ABR,10,15
42     42     ZRR,12
43     43     LLD,12,4
44     44     MBL,12,12
45     45     ADI,12      #3030
46     46     STX,12,10
47     47     ABR,10,15
48     48     ZRR,12
49     49     LLD,12,4
50     50     MBL,12,12
51     51     ADI,12      #3020
52     52     STX,12,10
53     53     ABR,10,15
54     54     ZRR,12
55     55     LLD,12,4
56     56     MBL,12,12
57     57     ADI,12      #3030
58     58     STX,12,10
59     59     ABR,10,15
60     60     ZRR,12
61     61     LLD,12,4
62     62     MBL,12,12
63     63     ADI,12      #3020
64     64     STX,12,10
65     65     ABR,10,15
66     66     ZRR,12
67     67     LLD,12,4
68     68     MBL,12,12
69     69     ADI,12      #3030
70     70     STX,12,10
71     71     ABR,10,15
72     72     ZRR,12
73     73     LLD,12,4
74     74     MBL,12,12
75     75     ADI,12      #3020
76     76     STX,12,10
77     77     ABR,10,15
78     78     ZRR,12
79     79     LLD,12,4
80     80     MBL,12,12
81     81     ADI,12      #3030
82     82     STX,12,10
83     83     ABR,10,15
84     84     ZRR,12
85     85     LLD,12,4
86     86     MBL,12,12
87     87     ADI,12      #3020
88     88     STX,12,10
89     89     ABR,10,15
90     90     ZRR,12
91     91     LLD,12,4
92     92     MBL,12,12
93     93     ADI,12      #3030
94     94     STX,12,10
95     95     ABR,10,15
96     96     ZRR,12
97     97     LLD,12,4
98     98     MBL,12,12
99     99     ADI,12      #3020
100    100    STX,12,10
101    101    ABR,10,15
102    102    ZRR,12
103    103    LLD,12,4
104    104    MBL,12,12
105    105    ADI,12      #3030
106    106    STX,12,10
107    107    ABR,10,15
108    108    ZRR,12
109    109    LLD,12,4
110    110    MBL,12,12
111    111    ADI,12      #3020
112    112    STX,12,10
113    113    ABR,10,15
114    114    ZRR,12
115    115    LLD,12,4
116    116    MBL,12,12
117    117    ADI,12      #3030
118    118    STX,12,10
119    119    ABR,10,15
120    120    ZRR,12
121    121    LLD,12,4
122    122    MBL,12,12
123    123    ADI,12      #3020
124    124    STX,12,10
125    125    ABR,10,15
126    126    ZRR,12
127    127    LLD,12,4
128    128    MBL,12,12
129    129    ADI,12      #3030
130    130    STX,12,10
131    131    ABR,10,15
132    132    ZRR,12
133    133    LLD,12,4
134    134    MBL,12,12
135    135    ADI,12      #3020
136    136    STX,12,10
137    137    ABR,10,15
138    138    ZRR,12
139    139    LLD,12,4
140    140    MBL,12,12
141    141    ADI,12      #3030
142    142    STX,12,10
143    143    ABR,10,15
144    144    ZRR,12
145    145    LLD,12,4
146    146    MBL,12,12
147    147    ADI,12      #3020
148    148    STX,12,10
149    149    ABR,10,15
150    150    ZRR,12
151    151    LLD,12,4
152    152    MBL,12,12
153    153    ADI,12      #3030
154    154    STX,12,10
155    155    ABR,10,15
156    156    ZRR,12
157    157    LLD,12,4
158    158    MBL,12,12
159    159    ADI,12      #3020
160    160    STX,12,10
161    161    ABR,10,15
162    162    ZRR,12
163    163    LLD,12,4
164    164    MBL,12,12
165    165    ADI,12      #3030
166    166    STX,12,10
167    167    ABR,10,15
168    168    ZRR,12
169    169    LLD,12,4
170    170    MBL,12,12
171    171    ADI,12      #3020
172    172    STX,12,10
173    173    ABR,10,15
174    174    ZRR,12
175    175    LLD,12,4
176    176    MBL,12,12
177    177    ADI,12      #3030
178    178    STX,12,10
179    179    ABR,10,15
180    180    ZRR,12
181    181    LLD,12,4
182    182    MBL,12,12
183    183    ADI,12      #3020
184    184    STX,12,10
185    185    ABR,10,15
186    186    ZRR,12
187    187    LLD,12,4
188    188    MBL,12,12
189    189    ADI,12      #3030
190    190    STX,12,10
191    191    ABR,10,15
192    192    ZRR,12
193    193    LLD,12,4
194    194    MBL,12,12
195    195    ADI,12      #3020
196    196    STX,12,10
197    197    ABR,10,15
198    198    ZRR,12
199    199    LLD,12,4
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203    203    ABR,10,15
204    204    ZRR,12
205    205    LLD,12,4
206    206    MBL,12,12
207    207    ADI,12      #3020
208    208    STX,12,10
209    209    ABR,10,15
210    210    ZRR,12
211    211    LLD,12,4
212    212    MBL,12,12
213    213    ADI,12      #3030
214    214    STX,12,10
215    215    ABR,10,15
216    216    ZRR,12
217    217    LLD,12,4
218    218    MBL,12,12
219    219    ADI,12      #3020
220    220    STX,12,10
221    221    ABR,10,15
222    222    ZRR,12
223    223    LLD,12,4
224    224    MBL,12,12
225    225    ADI,12      #3030
226    226    STX,12,10
227    227    ABR,10,15
228    228    ZRR,12
229    229    LLD,12,4
230    230    MBL,12,12
231    231    ADI,12      #3020
232    232    STX,12,10
233    233    ABR,10,15
234    234    ZRR,12
235    235    LLD,12,4
236    236    MBL,12,12
237    237    ADI,12      #3030
238    238    STX,12,10
239    239    ABR,10,15
240    240    ZRR,12
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243    243    ADI,12      #3020
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249    249    ADI,12      #3030
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253    253    LLD,12,4
254    254    MBL,12,12
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264    264    ZRR,12
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267    267    ADI,12      #3020
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269    269    ABR,10,15
270    270    ZRR,12
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272    272    MBL,12,12
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274    274    STX,12,10
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277    277    LLD,12,4
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302    302    MBL,12,12
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686    686    MBL,12,12
687    687    ADI,12      #3020
688    688    STX,12,10
689    689    ABR,10,15
690    690    ZRR,12
691    691    LLD,12,4
692    692    MBL,12,12
693    693    ADI,12      #30
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1      PGM          E$DIF
2      INT          E:DIF,E:COT
3      EXT          U.OTM
4      CAT NO.     400000-012
5      * E$DIF CONTAINS TWO EXECUTIVE UTILITY PROGRAMS CONCERNED WITH THE
6      * CONVERSION OF BCD TIME, AS READ THRU THE IOIS, TO BINARY COUNT OF
7      * SECONDS.
8      * E:COT SIMPLY CONVERTS THE BCD TIME CHARACTERS AT THE LOCATION
9      * SPECIFIED IN THE PARAMETER WORD TO BINARY, RETURNS IN R12,R13
10     * E:DIF DOES NOTHING IF THERE IS NO SIGNAL IN PROGRESS, I.E. THE CHAN'S~
11     * ONSET TIME IS ZERO. IF A SIGNAL IS CURRENTLY IN PROGRESS, THE
12     * DIFFERENCE BETWEEN THE ONSET TIME AND THE TIME SPECIFIED BY THE
13     * CALLING PARAMETER IS RETURNED IN R12,R13. CHANNEL DESIGNATED IN R6.~
14     * CALLING SEQUENCE.....
15     * BLM,14      E:COT
16     * (ADDRESS OF FIRST WORD OF TIME TO BE CONVERTED)
17     * OR.....
18     * BLM,14      E:DIF
19     * (ADDRESS OF FIRST WORD OF TIME TO BE CONVERTED)
20     * (RETURN LOCATION IF ONSET TIME IS ZERO)
21     * (RETURN LOCATION WITH TIME DIFFERENCE FROM ONSET)
22     * COMPUTATION IS MADE THUSLY:
23     * IF D0=HR*10
24     * D1=HR*1
25     * D2=MIN*10
26     * D3=MIN*1
27     * D4=SEC*10
28     * D5=SEC*1
29     * THEN
30     * SECONDS COUNT = (((D0*10+D1)*6+D2)*6+D4):10+D5)
31     * SAVER       SAVE REGISTERS FOR RETURN
32     * OBMM,0     FLAG       SET ENTRY FLAG FOR E:COT
33     * BRU        COMCN
34     * E:DIF SFM,1 SAVER     WEIDER, SAVE THE REGISTERS
35     * ZRMM,0     FLAG       JA WOHL, UND RESET DAS ENTRY FLAG
36     * LLS,6,1   U.OTM      SET UP FOR CHAN D.P. POINTER
37     * LDM,1,6
38     * ABR,6,15  U.OTM      BRU IF U.OTM ISN'T ZERO
39     * ADM,1,6
40     * TRRB,1,1  COMON
41     * ABR,14,15 SAVER     RETURN IF NO SIGNAL IN PROGRESS
42     * RETRN LFM,1

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43	0014	R	0052	BRX,14			UND, RETURN
44	0015		FF0E	LDX,7,14			GET HI ORDER TIME WORD ADDRESS
45	0017		FD7E	LDX,5,7			NOW GET THE WORD
46	0018		60EF	ABR,14,15			MOVE RETURN ADDRESS OVER PARAMETER
47	0019		2D5A	LLS,5,10			MOVE HRX10 TO LEFT END OF P5
48	001A		6C44	ZRR,4			
49	001B		2C42	LLD,4,2			AND OVER INTO R4.
50	001C		6D04	TRR,13,4			R13=HRX10
51	001D		ED10	LDI,1	#A		R1=10
52	001E	A	000A				
53	0020		20C1	MPR,12,1			
54	0021		6C44	ZRR,4			
55	0022		2C44	LLD,4,4			MOVE HR X 1 DIGIT TO R4
56	0023		68D4	ADR,13,4			AND ADD TO R13
57	0024	A	ED20	LDI,2	#6		R2=6
58	0025		0006				
59	0026		20C2	MPR,12,2			? SET UP TO GET LOW ORDER TIME WORD THEN GET IT
60	0027		607F	ABR,7,15			
61	0028		FD57	LDX,5,7			
62	0029		6C44	ZRR,4			
63	002A		2C45	LLD,4,5			GET MIN*10
64	002B		68D4	ADR,13,4			AND ADD TO R13
65	002C		20C1	MPR,12,1			
66	002E		6C44	ZRR,4			
67	002F		2C44	LLD,4,4			GET MIN*1 AND ADD TO R13
68	0030		68D4	ADR,13,4			
69	0031		20C2	MPR,12,2			
70	0032		6C44	ZRR,4			
71	0033		2C43	LLD,4,3			
72	0034		68D4	ADR,13,4			
73	0035		20C1	MPR,12,1			
74	0036		6C44	ZRR,4			
75	0037		2C44	LLD,4,4			
76	0038		6D54	TRR,5,4			
77	0039		6C44	ZRR,4			
78	003A	R	8600	DAR,12,4			AND DOUBLE PRECISION ADD TO R12,R13 AND CONVERT TO BINARY.
79	003B	R	0059	TMBB,0			
80	003C	R	0013				
81	003D	X	60EE	ABR,14,14			USE 2ND RETURN
82	003E	X	E556	LDM,5,6	U.OTM		GET ONSET TIME IN R4,P5
83	003F	X	616F	SBF,6,15			
84	0040	X	E546	LDM,4,6	U.OTM		
85	0041		0001	TOR,4,4			AND NEGATE (NEGATE?????)
86	0042		0D44	TTR,5,5			
87	0043		6F55	TRRB,5,5	\$+3		
88	0044		7D55				

* READY TO EXIT IF ONLY TASK WAS TO CONVERT TO BINARY.
FLAG=RETRN


```

0045 R 0047
0046 604F
0047 22C4
      ABR,4,15
      DAR,12,4
* WE'VE GOT TO WATCH OUT FOR TIME SPREADS WHICH TRAVERSE MIDNIGHT ZULU
* IF THE DIFFERENCE IS NEGATIVE, ADD 86,400 SECONDS TO CORRECT.
* NOTE: 86,400=#15180
* TBRB,12,0 $+4
0048 76C0
0049 R 004C
004A E700
004B R 0013
004C 654F
004D ED50
004E A 5180
004F 22C4
0050 E700
0051 R 0013
      BRU
      RETRN
      LBR,4,15
      LDI,5 #5180
      DAR,12,4
      BRU
      RETRN
      ADD 86,400 TO DIFFERENCE
      AND NOW RETURN
      R4,R5=#15180
      CORRECT IF SIGN BIT IS SET
      OTHERWISE RETURN
      REGISTER HOLD
      ENTRY POINT FLAG
* STORAGE AREA.....
SAVER RES 7,0
FLAG DFC 0
END

```

TOTAL ASSEMBLY ERRORS = 0 SIZE = 90

```

1          E$FMF
2          INT
3          EXT
4          CAT NO. 400000-013
5          * E$FMF ASSUMES THE VALUE IN R12 IS A PHASE VALUE. THIS
6          * ROUTINE CONVERTS THE NUMBER TO ASCII DECIMAL AND STORES
7          * THE CHARACTERS AWAY IN THE LOCATION SPECIFIED FOR OUTPUT.
8          * CALLING SEQUENCE:
9          *   BLM,15      E:FMF      (VALUE IN R12)
10         *   (ADDR OF FIRST LOC. OUTPUT TO BE STORED IN)
11         *   OUTPUT FORMAT: | +0037.62 | (I.E. 5 WORDS)
12         E:FMF SFM,1      SAVER

          A510
0001      R
0002      FD7F
0003      60FF
0004      6C33
0005      6D2C
0006      2A24
0007      0833
0008      2934
0009      7620
000A      R 000D
000B      6D13
000C      F702
000D      6F13
000E      6DC2
000F      E6E0
0010      R 0038
0011      E7F0
0012      X 0001
0013      ED30
0014      X 0003
0015      E830
0016      A 0003
0017      E540
0018      X 0002
0019      FE47
001A      607F
001B      ED60
001C      A FFFE
001D      FD43
001E      0944
001F      613F
0020      F843
0021      FE47
0022      607F
0023      613F
0024      706F
0025      R 001D
0026      ED60

          LDX,7,15      GET STORE ADDR (R7)
          ABR,15,15     MOD RETURN ADDR
          ZRR,3
          TRR,2,12
          RAD,2,4
          MBR,3,3
          RLS,3,4
          TBRB,2,0     $+4

          TRR,1,3
          HOP,$+2
          TTR,1,3
          TRR,12,2
          STM,14
          HLD14
          BLM,14
          LDI,3
          ADI,3
          LDM,4
          STX,4,7
          ABR,7,15
          LDI,6
          FMF1
          LDX,4,3
          MBL,4,4
          SBR,3,15
          ADX,4,3
          STX,4,7
          ABR,7,15
          SBR,3,15
          ABRB,6,15
          LDI,6

          $+4
          HLD14
          E:BAD
          E:PDF
          3
          E:BSG
          STX,4,7
          ABR,7,15
          LDI,6
          FMF1
          LDX,4,3
          MBL,4,4
          SBR,3,15
          ADX,4,3
          STX,4,7
          ABR,7,15
          SBR,3,15
          ABRB,6,15
          LDI,6

          STORE SIGN
          NEXT STORE LOC
          HIGH-ORDERED DIGIT
          NEXT DIGIT
          STORE FIRST TWO DIGITS
          NEXT STORE LOC
          FMF1
          625

```

FRACTION MUST BE POS.

STORE SIGN
NEXT STORE LOC

HIGH-ORDERED DIGIT

NEXT DIGIT
STORE FIRST TWO DIGITS
NEXT STORE LOC

FMF1

625

1	PGM	E\$SRT		
2	INT	E:SRT		
3	CAT NO.	400000-016		
4	E\$SRT	IS A SQUARE ROOT COMPUTATION ROUTINE.		
5		THE CALCULATION USES NEWTON'S METHOD OF		
6		TANGENTS.		
7		ITERATION IS MADE AS FOLLOWS:		
8		$X(N+1) = (A/X(N) + X(N)) / 2$		
9		WHERE X(0) IS BASED ON THE VALUE OF A, THE		
10		ARGUMENT OF THE SQUARE ROOT FUNCTION.		
11		THE ROUTINE IS ENTERED WITH DOUBLE PRECISION		
12		A IN R12,R13.		
13		THE CALLING SEQUENCE IS:		
14		BLM,14	E:SRT	
15		* AND THE FUNCTION ROOT IS RETURNED IN R13.		
16	E:SRT	SFM,1	SAVER	
	A510			
	0001	R		
	0002			
17	0003		TRR,6,12	COPY ARG.
18	0004		TRR,7,13	
19	0005	R	TBRB,12,0	NEGAR
20	0006		TRRB,12,12	SRT1
21	0007	R		CHECK A=0
22	0008		TRRB,13,13	SRT2
23	0009	R		
24	000A		NEGAR ZRR,13	
25	000B	R	RETRN LFM,1	SAVER
26	000C		BRX,14	
27	000D		EDI,1	31
28	000E	A		
29	000F		SRT3	LLD,6,1
30	0010	R	TBRB,6,0	EXIT
31	0011		SBR,1,15	
32	0012	R	BRU	SRT3
33	0013		LDI,1	16
34	0014	A		
35	0015	R	TRR,6,7	SRT4
36	0016		BRU	
37	0017	R	RAS,1,1	
38	0018		ADI,1	#2A60
39	0019	R		
40	001A	A	STM,1	SRT6
41	001B			
42	001C	R	TRR,6,12	
43	001D	A	TRR,7,13	
44	001E		RAD,6,0	
45	001F	R		
46	0020			
47	0021			
48	0022			COMPUTE X(0)

DIVIDE COUNT BY 2
MAKE 'RAD,6,N' INSTR

```

39 0023 6D17 TRR,1,7
40 0024 6D6C TRR,6,12
41 0025 6D7D TRR,7,13
42 0026 2161 DVR,6,1
43 0027 6871 ADR,7,1
44 0028 2971 RLS,7,1
45 * NEW ESTIMATE IN R7, CHECK AGAINST OLD ESTIMATE
46 0029 6917 SUR,1,7
47 002A 7610 TBRB,1,0 $+3 ABS(X(N+1)-X(N))
002B R 002D
48 002C HOP,$+2
49 002D 6F11 TTR,1,1
50 002E 6E10 SUI,1 3 ACCURACY DETERMINATION
002F A 0003
51 0030 7610 TBRB,1,0 $+5
0031 R 0035
52 0032 6D17 TRR,1,7
53 0033 E700 BRU SRT5 SET NEW ESTIMATE
0034 R 0024 AND TRY AGAIN
54 0035 6DD7 TRR,13,7 DONE
55 0036 E700 BRU RETRN
0037 R 000B
56 ***** SAVE REGISTERS *****
57 SAVER RES 7,0
58 END

```

TOTAL ASSEMBLY ERRORS = 0 SIZE = 63

34	0029	7E56	TERB,5,6	F.BAD+3	COMPARE SECOND WORD
	002A	0055			
35	002B	602F	ABR,2,15		IF GOOD GO ON
36	002C	E552	LDM,5,2	FUDRF	
	002D	0141			
37	002E	7E56	TERB,5,6	F.BAD+3	COMPARE THIRD WORD
	002F	0055			
38	0030	602F	ABR,2,15		IF GOOD GO ON
39	0031	E552	LDM,5,2	FUDRF	
	0032	0141			
40	0033	7E53	TERB,5,3	F.BAD	COMPARE FOURTH WORD
41	0034	0052			
	0035	ED30	LDI,3	#FOFO	FORMAT CHECKER FOR LOOP
	0036	FOFO			
42	0037	ED60	LDI,6	-22	COUNTER FOR LOOP
	0038	FFEA			
43	0039	602F	FLOOP		
44	003A	E552	ABR,2,15		
	003B	0141	LDM,5,2	FUDRF	
45	003C	7E53	TERB,5,3	F.BAD+6	COMPARE
46	003D	0058			
	003E	706F	ABRB,6,15	FLOOP	INC COUNTER
	003F	0039			
47	0040	602F	ABR,2,15		OUT OF LOOP, GET NEXT WORD
48	0041	ED60	LDI,6	#3132	
	0042	3132			
49	0043	E552	LDM,5,2	FUDRF	
	0044	0141			
50	0045	6C56	XOR,5,6		
51	0046	7D55	TRRB,5,5	F.BAD+8	
	0047	005A			
52	0048	ED60	LDI,6	#3400	
	0049	3400			
53	004A	602F	ABR,2,15		
54	004B	E552	LDM,5,2	FUDRF	
	004C	0141			
55	004D	6C56	XOP,5,6		
56	004E	7D55	TRRB,5,5	F.BAD+8	
	004F	005A			
57	0050	604E	ABR,4,14		NORMAL RETURN
58	0051	FF04	BRX,4		
59	0052	FA50	F.BAD	ETI,5	#030F
	0053	A030F			
60	0054	F706	HOP,\$+6		
61	0055	EA50	ETI,5	#070F	
	0056	A070F			
62	0057	F703	HOP,\$+3		
63	0058	EA50	ETI,5	#0FOF	
	0059	A0FOF			
64	005A	0865	MBR,6,5		MOVE LEFT BYTE

65	005B	7D66	TRRB,6,6	\$+3	
	005C	005E			
66	005D	F70A	HOP,CKRTB		
67	005E	6C33	ZRR,3		
68	005F	2961	RLS,6,1		
69	0060	7D66	TRRB,6,6	\$+4	
	0061	0064			
70	0062	E700	BRU	FNDBT	NO THAN YOU HAVE FOUND IT
	0063	0070			
71	0064	603F	ABR,3,15		
72	0065	E700	BRU	\$-6	
	0066	005F			
73	0067	6C33	CKRTB		
74	0068	2951	RLS,5,1		
75	0069	7D55	TRRB,5,5	\$+4	SHIFT BIT OUT
	006A	006D			
76	006B	E700	BRU	FNDBT	
	006C	0070			
77	006D	603F	ABR,3,15		
78	006E	E700	BRU	\$-6	
	006F	0068			
79	0070	E553	FNDBT	TAB	
	0071	010A	LDM,5,3		
80	0072	E650	STM,5	X-VAL+9	
	0073	00EE			
81	0074	E7F0	BLM,15	P:IOR	
	0075	X 0008			
82	0076	A 9000	DFC	#9000	
83	0077	R 00E5	DFC	X-VAL	
84	0078	E7E0	BLM,14	F:DDD	
	0079	X 000A			
85	007A	7600	TBR,0,0	\$+4	
	007B	007E			
86	007C	E700	BRU	ABORT	
	007D	007F			
87	007E	FE04	BRX,4		
88	007F	E7F0	ABORT	B:IOR	ERROR RETURN
	0080	X 000B	BLM,15		
89	0081	A 9000	DFC	#9000	
90	0082	R 00FB	DFC	QUIT	
91	0083	F705	HOP,\$+5		
92	0084	E7F0	FINI	B:IOR	
	0085	X 000B	BLM,15		
93	0086	A 9000	DFC	#9000	
94	0087	R 00F1	DFC	FINSH	
95	0088	6C55	ZRR,5		
96	0089	E650	STM,5	P-5TT	
	008A	X 0007			
97	008B	E700	BRU	E:SKD	
	008C	X 0006			

MODCOMP ASSEMBLY

```

128      00BF R 00C5      BADNO
129      00C0      6C55      ZRR,5
130      00C1      E650      STM,5
      M 00C2 X 0007
      M 00C3 E700      BRU
131      00C4 X 0006
132      00C5 A 0D0A      #0D0A,|INVALID CHAR, TASK ABORTED|
      00C6 A 494E
      00C7 A 5641
      00C8 A 4C49
      00C9 A 4420
      00CA A 4348
      00CB A 4152
      00CC A 2C20
      00CD A 5441
      00CE A 534B
      00CF A 2041
      00D0 A 424F
      00D1 A 5254
      00D2 A 4544
      00D3 A 0D0A      #0D0A,#0000
      00D4 A 0000
133      00D5 A 0D0A      #0D0A,IND. OF BLOCKS TO BE CHECKED?,|,0
134      00D6 A 4E4F      DFC
      00D7 A 2E20      NUMBK DFC
      00D8 A 4F46
      00D9 A 2042
      00DA A 4C4F
      00DB A 434B
      00DC A 5320
      00DD A 544F
      00DE A 2042
      00DF A 4520
      00E0 A 4348
      00E1 A 4543
      00E2 A 4845
      00E3 A 443F
      00E4 A 0000
      00E5 A 0D0A      #0D0A,|FORMAT ERROR BIT 4|, #0D0A,0
      00E6 A 464F      X.VAL DFC
      00E7 A 524D
      00E8 A 4154
      00E9 A 2045
      00EA A 5252
      00EB A 4F52
      00EC A 2042
      00ED A 4954
      00EE A 2034
      00EF A 0D0A
      00F0 A 0000

```

MDDCOMP ASSEMBLY

Line	Code	Label	Address	Mode	Operation	Comments
136	00F1	A	0D0A		FINISH DFC	#0D0A, TASK COMPLETED , #0D0A, 0
	00F2	A	5441			
	00F3	A	5348			
	00F4	A	2043			
	00F5	A	4F4D			
	00F6	A	504C			
	00F7	A	4554			
	00F8	A	4544			
	00F9	A	0D0A			
	00FA	A	0000			
137	00FB	A	0D0A		QUIT DFC	#0D0A, FORMAT CHECK TERMINATED , #0D0A, 0
	00FC	A	464F			
	00FD	A	524D			
	00FE	A	4154			
	00FF	A	2043			
	0100	A	4845			
	0101	A	4348			
	0102	A	2054			
	0103	A	4552			
	0104	A	4D49			
	0105	A	4E41			
	0106	A	5445			
	0107	A	4420			
	0108	A	0D0A			
	0109	A	0000			
138	010A	A	2031		TAB	1
139	010B	A	2032		DFC	2
140	010C	A	2034		DFC	4
141	010D	A	2038		DFC	8
142	010E	A	2041		DFC	A
143	010F	A	2042		DFC	B
144						
145						
146						
	0110		E7F0			
	0111	X	0008			
147	0112	A	2000		DFC	#2000
148	0113	A	6176		DFC	MTBUF
149	0114	A	1880		DFC	7040
150	0115		6C33		ZRR, 3	
151	0116		E630		STM, 3	
	0117	R	0119			F.MTP
152	0118		FF07		BRX, 7	
153						
154		A	6176		* RESFT WORD POINTER	24950
155	0119	A	0000		MTBUF EQU	0
					F.MTP DFC	

* THIS ROUTINE FILLS THE BIG BAD BUFFER WITH FRESH FUD.

* F:TRC BLM,15 B:IOR

START LOCATION

MODCOMP ASSEMBLY

F:USE.....

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    A510
    011A R 015D
    011B R 015D
    011C E530
    011D R 0119
    011E ED20
    011F A 6176
    0120 EDD0
    0121 A 0000
    0122 EDC0
    0123 R 0141
    0124 6D63
    0125 E960
    0126 A 3700
    0127 7660
    0128 R 0128
    0129 E770
    012A R 0110
    012B AE72
    012C 6D57
    012D E950
    012E A 000A
    012F 7D55
    0130 R 0133
    0131 AF5C
    0132 F702
    0133 AF7C
    0134 603F
    0135 60DF
    0136 6D6D
    0137 E960
    0138 A 0037
    0139 7660
    013A R 0128
    013B E630
    013C R 0119
    013D AF6C
    013E A410
    013F R 015D
    0140 FF04
    0141 R 0141
    0141 A 0000
    0150 A 0000
    0164

    F:USE IS THE USER PROGRAM THAT TRANSFERS
    * 55 BYTE BLOCKS OUT OF THE 88B AND STORES
    * THEM IN THE FUDBF.
    F:USE SFM,1
    LDM,3 F:MTP
    LDI,2 MTRUF
    LDI,13 0
    LDI,12 FUDBF
    TRR,6,3 14080
    SUI,6 $+4
    TBRB,6,0 $+4
    BLM,7 F:TRD
    USE
    LAX,7,2
    TRR,5,7
    SUI,5 10
    TRP8,5,5 $+4
    SBX,5,12
    HOP,$+2
    SBX,7,12
    ABR,3,15
    ABR,13,15
    TRR,6,13
    SUI,6 55
    TBRB,6,0 USE
    STM,3 F:MTP
    SBX,6,12
    LFM,1 FRS
    BRX,4
    F:DRF EQU
    FUDBF RES 28,0
    FRS RES 7,0
    END

    MAG TAPE POINTER

    SAVE ORIGINAL VALUE
    CHECK FOR TEN
    STORE ORIGINAL NO.
    STORE THE CONVERSION
    INC MTPNT
    INC FUDBF POINT
    SAVE R13

    RETURN
    $
    28,0
    7,0
    
```

TOTAL ASSEMBLY ERRORS = 15 SIZE = 356

34	0028	ED10	LDI,1	3		
35	0029	A 0003	LDM,5,1	F.DBF	FIRST 2 DIGITS OF DOY	
36	002A	E551	ADI,5	#3030	CONVERT	
37	002B	X 0001	STM,5	HEADR+9		
38	002C	E850	LDI,10	F.DBF		
39	002D	A 3030	LBR,11,12		LOAD 3RD DIGIT OF DOY	
40	002E	E650	LBX,5,10		CONVERT	
41	002F	R 00A1	MBL,5,5	#3020	PUT 3RD DIGIT DOY IN HEADR	
42	0030	EDA0	ADI,5		SET UP FOR SIX DIGITS OF TIME	
43	0031	X 0001	STM,5	HEADR+10		
44	0032	65BC	LDI,2	-3		
45	0033	AE5A	LDI,5	F.DBF		
46	0034	0955	LDX,3,5		CONVERT	
47	0035	E850	ADI,3	#3030	PUT DIGITS IN HEADR.	
48	0036	A 3020	STM,3,2	HEADR+6		
49	0037	E650	ABR,5,15			
50	0038	R 00A2	ABRB,2,15	TIMI		
51	0039	ED20	LDI,11	9	START GETTING THE DET NC.	
52	003A	A FFFD	LDI,12	HEADR		
53	003B	ED50	LDI,13	29	READY TO GO.	
54	003C	X 0001	LBX,5,10			
55	003D	FD35	ADI,5	#30		
56	003E	E830	SBX,5,12			
57	003F	A 3030	LDI,1	5		
58	0040	E632	LDM,5,1	F.DBF	CONVERT	
59	0041	R 009E	ADI,5	#3030	STORE DET IN HEADR	
60	0042	605F	STM,5	HEADR+15	OUTPUT HEADR.	
61	0043	702F	BLM,15	B:IOR		
62	0044	R 003D	DFC	#9000		

63	005A	R	0098	DFC	HEADR		
64	005B	ED20		LDI,2	-32		
65	005C	A	FFEO				
	005D	ED80		LDI,11	12		
	005E	A	000C				
66	005F	AE5A		LBX,5,10		LOAD BYTE FROM F.DBF	
67	0060	0965		MBL,6,5		SHIFT BYTE TO LEFT	
68	0061	608F		ABR,11,15		MOVE TO NEXT BYTE	
69	0062	AE5A		LBX,5,10		LOAD NEXT BYTE	
70	0063	6865		ORF,6,5		OR THE TWO REGISTERS	
71	0064	E860		ADI,6	#3030	CONVERT	
	0065	A	3030				
72	0066	E662		STM,6,2	DATA+32	PUT IN PROPER PLACE IN DATA	
	0067	R	00CA				
73	0068	602F		ABP,2,15		MOVE DATA POINTER ONE	
74	0069	608F		ABR,11,15		MOVE TO NEXT BYTE	
75	006A	AE6A		LBX,6,10		LOAD IT	
76	006B	E860		ADI,6	#2E30	CONVERT	
	006C	A	2E30				
77	006D	E662		STM,6,2	DATA+32		
	006E	R	00CA				
78	006F	602F		ABR,2,15		MOVE DATA POINTER TWO WORDS	
79	0070	608F		ABR,11,15		MOVE TO NEXT BYTE IN F.DBF	
80	0071	AE5A		LBX,5,10			
81	0072	E850		ADI,5	#2030		
	0073	A	2030				
82	0074	E652		STM,5,2	DATA+32		
	0075	R	00CA				
83	0076	602F		ABR,2,15			
84	0077	608F		ABR,11,15			
85	0078	AE5A		LBX,5,10		LOAD NEXT BYTE	
86	0079	0955		MBL,5,5			
87	007A	E850		ADI,5	#3020		
	007B	A	3020				
88	007C	E652		STM,5,2	DATA+32	STORE IN DATA	
	007D	R	00CA				
89	007E	608F		ABR,11,15			
90	007F	702F		ABRB,2,15	LOOP	FINISHED YET	
91	0080	R	005F				
	0081	ED60		LDI,6	-3		
	0082	A	FFFF				
92	0083	AE4A		LBX,4,10			
93	0084	608F		ABR,11,15			
94	0085	2844		RLD,4,4			
95	0086	2D44		LLS,4,4			
96	0087	2C44		LLD,4,4			
97	0088	E840		ADI,4	#3030		
	0089	A	3030				
98	008A	E642		STM,4,2	DATA+32		
	008B	R	00CA				

MODCOMP ASSEMBLY

99	008C	602F							
100	008D	706F	ABR,2,15	DLOOP					
	008E	0083	ABPB,6,15						
101	008F	E7F0	BLM,15	B: IOR	OUTPUT DATA	INFORMATION			
102	0090	X 0003							
103	0091	A 9000	DFC	#9000					
104	0092	R 00AA	DFC	DATA					
	0093	A 410	LFM,1	SVDDD					
105	0094	R 00CF							
	0095	A 4A0	LFM,10	SVDCD+7					
	0096	R 00D6							
106	0097	FF0E	BRX,14						
107	0098	A 0D0A	HEADR DFC	#0D0A,ITOD 123456	DOY 123				
	0099	A 544F							
	009A	A 4420							
	009B	A 3132							
	009C	A 3334							
	009D	A 3536							
	009E	A 2020							
	009F	A 444F							
	00A0	A 5920							
	00A1	A 3132							
	00A2	A 3320							
108	00A3	A 2020	DFC	I DET 5151, #0D0A, #0000					
	00A4	A 2044							
	00A5	A 4554							
	00A6	A 2035							
	00A7	A 3135							
	00A8	A 0D0A							
	00A9	A 0000							
109	00AA	A 5050	DATA DFC	IPP.P AA PP.P AA PP.P AA					
	00AB	A 2E50							
	00AC	A 2041							
	00AD	A 4120							
	00AE	A 5050							
	00AF	A 2E50							
	00B0	A 2041							
	00B1	A 4120							
	00B2	A 5050							
	00B3	A 2E50							
	00B4	A 2041							
	00B5	A 4120							
110	00B6	A 5050	DFC	IPP.P AA PP.P AA PP.P AA					
	00B7	A 2E50							
	00B8	A 2041							
	00B9	A 4120							
	00BA	A 5050							
	00BB	A 2E50							
	00BC	A 2041							
	00BD	A 4120							

MODCOMP ASSEMBLY

008E A 5050
 008F A 2E50
 00C0 A 2041
 00C1 A 4120
 00C2 A 5050
 00C3 A 2E50
 00C4 A 2041
 00C5 A 4120
 00C6 A 5050
 00C7 A 2E50
 00C8 A 2041
 00C9 A 4120
 00CA A 3331
 00CB A 3332
 00CC A 3334
 00CD A 000A
 00CE A 0000
 00CF A 0000
 00DC A 0000

DFC |PP.P AA PP.P AA 313234|, #0D0A, 0

SVD0D RES 13,0
 BLKBF RES 1,0

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00DD X E7F0
00DE X 0003
00DF A 5000
00E0 R 0145
00E1 E510
00E2 X 0008
00E3 6D51
00E4 ED60
00E5 A 0033
00E6 7956
00E7 R 00F2
00E8 E740
00E9 X 0007
00EA E7F0
00EB X 0003
00EC A 5000
00ED R 0159
00FE E740
00FF X 0004
00F0 E700
00F1 R 0009
00F2 E7F0
00F3 X 0003
00F4 A 5000
00F5 R 0169
00F6 E7F0
00F7 R 0112
00F8 E740
00F9 R 0188
00FA ED60
00FB A 0031
00FC 7916
00FD R 00EA
00FE E7F0
00FF X 0003
0100 A 5000
0101 R 0179
0102 E7F0
0103 R 0112
0104 E740

E7F0
0003
5000
0145
E510
0008
6D51
ED60
0033
7956
00F2
E740
0007
E7F0
0003
5000
0159
E740
0004
E700
0009
E7F0
0003
5000
0169
E7F0
0112
E740
0188
ED60
0031
7916
00EA
E7F0
0003
5000
0179
E7F0
0112
E740

DFC
DFC
BLM,15
BLM,4
LDM,1
TRR,5,1
LDI,6
SURB,5,6
BLM,4
BLM,15
DFC
DFC
BLM,4
BRU
TIM
BLM,15
DFC
DFC
BLM,15
BLM,4
LDI,6
SURB,1,6
BLM,15
DFC
DFC
BLM,4
E.351
#33
TIM
F:USE
E:ICR
#5000
NOBLK
AND HOLD IN BLKBF.
F:CVT
F:DMP+9
B:IDF
#5000
TIME
RFC
F:TIM
#31
BLOCK+2
B:IDR
#5000
QUIT
PFC
PACK

ONE OF THE ABOVE OPTIONS.
GET HIS REQUEST
HE WANTS A START TIME HUH!
REQUEST NO. OF BLOCKS
REQUEST START TIME
REQUEST START TIME
FIND CORRECT TIME ON TAPE
STOP TIME OR NO. OF BLOCKS
REQUEST STOP TIME
CANNED MSG.
GO GET RESPONSE

```

```

*****
* THIS PROGRAM SETS THE DRFUD TAPE TO THE
* DESIRED START TIME AND GIVES THE OPERATOR
* THE OPTIONS OF USING:
* 1. START TIME AND STOP TIME.
* 2. START TIME AND NUMBER OF DATA BLOCKS.
* 3. START IMMEDIATE AND NUMBER OF DATA BLOCKS.
F:FST RLM,15 B:IDR
* RE-ENTRY HERE AFTER OPERATORS RESPONSE
LDM,1 E.351
TRR,5,1
LDI,6
SURB,5,6
BLM,4
BLM,15
DFC
DFC
BLM,4
BRU
TIM
BLM,15
DFC
DFC
BLM,15
BLM,4
LDI,6
SURB,1,6
BLM,15
DFC
DFC
BLM,4
E.351
#33
TIM
F:USE
E:ICR
#5000
NOBLK
AND HOLD IN BLKBF.
F:CVT
F:DMP+9
B:IDF
#5000
TIME
RFC
F:TIM
#31
BLOCK+2
B:IDR
#5000
QUIT
PFC
PACK

ONE OF THE ABOVE OPTIONS.
GET HIS REQUEST
HE WANTS A START TIME HUH!
REQUEST NO. OF BLOCKS
REQUEST START TIME
REQUEST START TIME
FIND CORRECT TIME ON TAPE
STOP TIME OR NO. OF BLOCKS
REQUEST STOP TIME
CANNED MSG.
GO GET RESPONSE

```

151	1515	R	01A7				F:BR7
	0106	R	E7F0		BLM,15		
	0107	R	026E		DFC		T.MBF
152	0108	R	019A		LDM,4		NGSTR
153	0109	R	E540		LDM,5		NGSTR+1
	010A	R	026C		DAR,4,12		
154	0108	R	E550		STM,5		BLKBF
	010C	R	026D		BRU		F:DMP+11
155	0100	R	224C				
156	010E	R	E650				
	010F	R	00DC				
157	0110	R	E700				
	0111	R	000R				
158	0112	A	A510				
159	0113	R	01A0				
160	0114	X	ED20				
	0115	X	0008				
161	0116	A	ED30				
	0117	A	FFFD				
162	0118	E	FD42				
163	0119	E	E940				
	011A	A	0030				
164	0118	R	7640				
	011C	R	013F				
165	011D	A	E940				
	011E	A	0010				
166	011F	R	7640				
	0120	R	0123				
167	0121	R	E700				
	0122	R	013F				
168	0123	R	602F				
169	0124	R	703F				
	0125	R	0118				
170	0126	R	FD42				
171	0127	A	E940				
	0128	A	0020				
172	0129	R	7044				
	012A	R	013F				
173	0128	R	602F				
174	012C	A	ED30				
	012D	A	FFFA				
175	012E	R	FD42				
176	012F	A	E940				
	0130	A	0030				
177	0131	R	7640				
	0132	R	013F				
178	0133	A	E940				
	0134	A	0010				
179	0135	R	7640				

* CANNED OUTPUT MESS, BUFFERS, AND FORMAT CHECKER.

RFC SFM,1 RFCRS

LDI,2 E.35I

-3

LDX,4,2 #30

SUI,4 BADTM

#10

\$\$4

BADTM

PFC1

#20

BADTM

--6

#30

BADTM

#10

\$\$4

	TIME	DFC	#000A, SPECIFY START TIME DESIRED. , 0
191	0168 A 0000		
	0169 A 000A		
	016A A 5350		
	016B A 4543		
	016C A 4946		
	016D A 5920		
	016E A 5354		
	016F A 4152		
	0170 A 5420		
	0171 A 5449		
	0172 A 4D45		
	0173 A 2044		
	0174 A 4553		
	0175 A 4952		
	0176 A 4544		
	0177 A 2E20		
	0178 A 0000		
192	0179 A 000A	QUIT	#000A, SPECIFY STOP TIME DESIRED. , 0
	017A A 5350		
	017B A 4543		
	017C A 4946		
	017D A 5920		
	017E A 5354		
	017F A 4F50		
	0180 A 2054		
	0181 A 494D		
	0182 A 4520		
	0183 A 4445		
	0184 A 5349		
	0185 A 5245		
	0186 A 442E		
	0187 A 0000		
193	0188 A 000A	FORM	#000A, INVALID FORMAT ON
	0189 A 494E		
	018A A 5641		
	018B A 4C49		
	018C A 4420		
	018D A 464F		
	018E A 524D		
	018F A 4154		
	0190 A 204F		
	0191 A 4E20		
194	0192 A 5449	DFC	TIME REQUEST , #000A, 0
	0193 A 4D45		
	0194 A 2052		
	0195 A 4551		
	0196 A 5545		
	0197 A 5354		
	0198 A 0D0A		
	0199 A 0000		

F:FST.....

MODCOMP ASSEMBLY

195	019A	A	0000	T.MBF RES	6,0	
196	01A0	A	0000	RFCRS RES	7,0	
197				* CHANGE OPERATOR RESPONSE TO BCD AND PUT INTO T.MBF IN A FORMAT		
198				* COMPATABLE WITH F.DBF.		
199				* I123 123456 IS REPACKED TO 123456 DROP DOYJ		
200	01A7		651D	PACK	LBR,1,13	
201	01A8		ED70		LDI,7	-3
	01A9	A	FFFD			
202	01AA	X	E531	LDM,3,1	E.35I	TENS OF HOURS
	01AB	X	0008			
203	01AC		0933	MBL,3,3		
204	01AD		601F	ABR,1,15		
205	01AE		E331	ORM,3,1	E.35I	UNITS OF HOURS
	01AF	X	0008			
206	01R0		E930	SUI,3	#3030	
	01B1	A	3030			
207	01B2		E637	STM,3,7	T.MBF+3	
	01B3	R	019D			
208	01B4		601F	ABR,1,15		
209	01B5		707F	ABRB,7,15	PACK+3	
	01B6	R	01AA			
210	01B7		FF04	BRX,4		RETURN
211	01B8		A510	SFM,1	SAVER	
	01B9	R	0265			
212	01BA		ED20	LDI,2	#A	
	01BB	A	000A			
213	01BC		E550	LDM,5	E.35I	GET MOD.
	01BD	X	0008			
214	01BE		F950	SUI,5	#30	
	01BF	A	0030			
215	01C0		2042	MPR,4,2		X 10
216	01C1		651F	LBR,1,15		
217	01C2		E561	LDM,6,1	E.35I	GET TOD
	01C3	X	0008			
218	01C4		E960	SUI,6	#30	
	01C5	A	0030			
219	01C6		6856	ADR,5,6		
220	01C7		601F	ABR,1,15		
221	01C8		E561	LDM,6,1	E.35I	GET UOD
	01C9	X	0008			
222	01CA		E960	SUI,6	#30	
	01CB	A	0030			
223	01CC		2042	MPR,4,2		RESULT IN R5
224	01CD		6856	ADR,5,6		SAVE RESULT
225	01CE		F650	STM,5	CPDOY	
	01CF	R	0264			
226	01D0		E740	BLM,4	PACK	REPACK OPERATOR RESPONSE
	01D1	R	01A7			
227	01D2		E7F0	BLM,15	F:BR7	CONVERT IT TO BIN
	01D3	R	026E			

F:FST.....

MODCOMP ASSEMBLY

228	01D4	R	019A	DFC	T.MBF
229	01D5		6FDD	TTR,13,13	
230	01D6		ODCC	TOR,12,12	
231	01D7		7DDD	TRRB,13,13	\$+3
232	01D8	R	01DA		
233	01D9		60CF	ABR,12,15	
233	01DA		E6C0	STM,12	NGSTR
234	01DB	R	026C		
234	01DC		E6D0	STM,13	NGSTR+1
234	01DD	R	026D		
235	01DE		E740	BLM,4	F:USE
236	01DF	X	0007	BLM,4	F:CHK
236	01E0		E740		
237	01E1	X	0002	BRU	NOGO
237	01E2		E700		
237	01E3	R	0292		BAD FORMAT ON FUD
238	01E4		ED20		
239	01E5	A	000A	* CONVERT F,DBF DOY TO BIN FOR COMPARISON R2=10	
240	01E6		ED10	LDI,2	FDOY
240	01E7	A	0003	LDI,1	3
241	01E8		E561	LDM,6,1	F.DRF
241	01E9	X	0001		GET DOY
242	01EA		0856	MBR,5,6	GET HOD IN R4
243	01EB		0C66	MLR,6,6	GET ONLY TOD IN R6
244	01EC		2042	MPR,4,2	MULT R4 X 10
245	01ED		6856	ADR,5,6	ADD RESULT TO R6
246	01EE		601F	ABR,1,15	
247	01EF		E561	LDM,6,1	F.DBF
247	01F0	X	0001		GET UOD AND PART OF DET
248	01F1		0866	MBR,6,6	GET RID OF DET
249	01F2		2042	MPR,4,2	
250	01F3		6856	ADR,5,6	
251	01F4		E560	LDM,6	CPDOY
251	01F5	R	0264		
252	01F6		7956	SURB,5,6	GOON3
252	01F7	R	0216		COMPARE TWO DOYS
253	01F8		E7F0	BLM,15	F:88T
253	01F9	R	026E	CRAP	
254	01FA	X	0001		
254	01FB		E540	DFC	F.DBF
255	01FC	R	026C	* BOTH TIMES ARE NOW IN BINARY, DO A DOUBLE PRECISION * SURTRACT AND SEE HOW THEY COMPARE.	
256	01FD		E550	LDM,4	NGSTR
257	01FE	R	026D	LDM,5	NGSTR+1
259	01FF		224C	DAR,4,12	
260	0200		7D44	TRRB,4,4	GOON
261	0201	R	0207		
261	0202		7D55	TRRB,5,5	GOON


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291 *****
292 * F:PBT CONVERTS BCD TO BINARY TIME
293 * CALLING SEQUENCE*
294 *      R1M,15      F:BBT
295 *      DFC        (ADDR OFFIRST BCD WORD)
296 * RETURN WITH BINARY EQUIVALENT IN R12 AND R13
297 F:RBT SFM,1      BRTSV
                A510
                R 026E
298 026F R 028B
299 0270 FD7F
300 0271 60FF
301 0272 ED10
302 0273 A 000A
303 0274 ED20
304 0275 A 0006
305 0276 6CDD
306 0277 ED30
307 0278 A FFFD
308 0279 FD57
309 027A 607F
310 027B 0845
311 027C 68D4
312 027D 20C1
313 027E 0C55
314 027F 703F
315 0280 R 0282
316 0281 F705
317 0282 68D5
318 0283 20C2
319 0284 E700
320 0285 R 0279
321 0286 6C44
322 0287 22C4
323 0288 A410
324 0289 R 028B
325 028A FFOF
326 028B A 0000
                A510
                R 0292
327 0293 X 0003
328 0294 A 9000
329 0295 R 0251
330 0296 E700
331 0297 R 0016
332 0298

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*****
* F:PBT CONVERTS BCD TO BINARY TIME
* CALLING SEQUENCE*
*      R1M,15      F:BBT
*      DFC        (ADDR OFFIRST BCD WORD)
* RETURN WITH BINARY EQUIVALENT IN R12 AND R13
F:RBT SFM,1      BRTSV
                LDX,7,15      PARM. LOC.
                ABR,15,15    MODIFY RET. ADDR.
                LDI,1      #A
                LDI,2      #6
                ZRR,13
                LDI,3      -3
                BBT01 LDX,5,7      NEXT WORD
                ABP,7,15
                MBR,4,5
                ADR,13,4
                MPR,12,1
                MLR,5,5
                ABRB,3,15    $+3
                HOP,BBT02
                ADR,13,5
                MPR,12,2
                BRU
                BBT01
                BBT02 ZRR,4
                DAR,12,4
                LFM,1      BRTSV
                BRX,15
                RBTSV RES      7,0
                * TIME SEARCH ABORT
                NOGO BLM,15    B:IOR
                DFC          #9000
                DFC          NOG01
                BRU          CLOP1
                END

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16. ABSTRACT (A 200-word or less factual summary of most significant information. If document includes a significant bibliography or literature survey, mention it here.)
 Volume II of the "Automated U System" describes the software listings and the operator's manual. This volume is intended to instruct the station technician in the procedure to load programs and operate, under computer control, the automated U system.

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