Comparators for Voltage Transformer Calibrations at NBS

Wilbur C. Sze

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An inductive comparator is described for measuring ratio-correction-factor and phase-angle error of voltage transformers by comparison with reference transformers. The simple circuit utilizes a resistance-capacitance network and an inductive voltage divider. The relative magnitude of the ratio-correction-factors is obtained from the dial readings of the inductive voltage divider, and the phase angle between the voltage phasors is equal to $\pm \omega CR$. The accuracy of these measurements at 60 and 400 hertz is within 2 parts-per-million for ratio and within 10 microradians for phase angle at the limits of the phase-angle range of ± 38 milliradians. Only a few ranges of reference transformers are required since fractional ratios from 1/1 to 1/4 may be measured as readily as ratios near unity, without diminution in accuracy. A general discussion of a resistive comparator, which has been in continuous operation at NBS since 1939, is included.

Key Words: Voltage divider, inductive voltage divider, resistive voltage divider, voltage transformer calibration, ratio-correction-factor, phase-angle errors, voltage transformer comparator, voltage-transformer-test-set, comparator, test-set, calibration.

1. Introduction

The method most widely used by public utilities and standardizing laboratories to determine the ratio and phase angle of voltage transformers is by comparison with calibrated reference transformers. However, it is a relative rather than an absolute method, in that the accuracy of the results obtained depends upon the knowledge of accuracy of the reference transformers [1].¹

At the NBS laboratories, a group of reference transformers is available by means of which test transformers having any ratio from 1:1 to 2000:1 can be calibrated. The accurate initial establishment of the voltage ratio and phase angle for these reference transformers has been discussed elsewhere and will not be repeated here [2, 3].

The purposes of this paper are (1) to describe the resistive comparator which has been in continuous operation since 1939; (2) to describe a new inductive comparator; and (3) to analyze various factors affecting the accuracy with which measurements can be made.

2. Resistive Comparator

This comparator was designed and constructed at NBS by F. K. Harris and has been in continuous operation since 1939, except for a very brief period during which new switches were installed to replace the worn out original ones [4, 5].

The schematic circuit of this comparator is shown in figure 1.² The in-phase voltage balance condition is achieved by adjusting the tap of the resistive divider, and the quadrature voltage, by varying the mutual inductor, M.



2.1. Description of Components

The R_s and R_m circuits are supplied from the secondary of transformer S. Each can be varied from 2000 to 4000 Ω in increments of 100 Ω . The exact values are determined by the ratios of the test and reference transformers. The 100- Ω and 1000- Ω unit resistors of R_s are of woven wire and mounted on ceramic forms. Thus the residual, L_s , of R_s is reduced to a minimum [6]. The R_m circuit, nominally equal to R_s , include the primary circuit of M which has a d-c resistance of 200 Ω . The capacitor, C, shunting the lower portion of the resistance, serves to compensate for the self-inductance introduced by M [5]. Multiple taps are brought out from R_m to supply the shield potential of corresponding sections of R_s and of the detector circuit [7]. The shielding is shown in figure 4.

As shown in figure 2, the portion of the R_s circuit between 1800 and 2000 Ω carries a continuously adjustable tap, R_x , which is connected through the detector circuit and M to the secondary of the test transformer.

¹ Figures in brackets indicate the literature references at the end of this paper.

² Simplified by not showing the shielding arrangements around each segment of R_s .



FIGURE 2.

The mutual inductor used in this circuit consists of a 10×20 mH per section unit (± 200 mH maximum) and a continuously adjustable inductometer (± 25 mH maximum) [8, 9].

Both the R_s/R_x ratio of the resistive divider and the mutual inductance, M, are periodically redetermined to insure that maximum accuracy is maintained for transformer testing. The residual reactances, L_s and L_x , were also accurately determined at the time the comparator was built.

The design is such that the resistors must dissipate considerable power. Therefore, the entire resistive divider, R_s and R_m , is immersed in oil to minimize the effect of temperature coefficient on the R_s/R_x ratio.

The detector circuit consists of a doubly shielded impedance-matching transformer (1000-4000 turns) and a wave-analyzer used as a tuned null detector.

2.2. Comparator Desk

Figure 3 shows the general layout of the desk. The resistive divider is located to the right with the R_s adjustment dials in the back and the R_x dials in front. The step mutual inductor and the inductometer are located in the left portion of the desk. The detector is in the rear right. Since this comparator is adaptable for calibrating voltage-transformer-test-sets, the panel shown at the lower left side of the desk is for arranging the connections for calibrating either transformers or test-sets. Figure 4 shows the internal wiring of the resistive divider and the individual shields around units of R_s .

2.3. Theoretical Relations

The equations for obtaining the ratio-correctionfactor and phase-angle error are developed as follows. By definition, the voltage ratio of a transformer is the ratio of the primary terminal voltage to the secondary



FIGURE 3.

terminal voltage so that

$$\frac{|V_{\rm Pri}|}{|V_{\rm Sec}|} = N' = Nf$$

where N' is the actual voltage ratio, N the nominal ratio, and f the ratio-correction-factor. If θ represents the angle between the primary and reversed secondary voltage phasors, then

$$V_s = \frac{|V_p|}{N'_s} \left(\cos \theta_s + j \sin \theta_s\right)$$

and ³

$$V_x = \frac{|V_p|}{N'_x} (\cos \theta_x + j \sin \theta_x)$$

By inspection of figure 1, the following mesh current equations are obtained

$$V_{s} = I_{1}Z_{s} + I_{2}Z_{x}$$
$$V_{x} = I_{1}Z_{x} + I_{2}(Z_{x} + Z_{Ms}) + I_{3}j\omega M$$
$$V_{s} = I_{2}j\omega M + I_{3}Z_{m}$$

³ Subscripts s and x denote that the symbols refer to the reference or test transformers, respectively.



FIGURE 4.

 $I_2 = 0$ when a null is indicated on the detector, then

$$V_s = I_1 Z_s = I_3 Z_m$$

and

$$V_x = V_s \left(\frac{Z_x}{Z_s} + \frac{j\omega M}{Z_m} \right)$$

Since $Z_m = R_m$ by the addition of *C*, and $R_m = R_s$ nominally, the balance equation becomes

$$\frac{(\cos \theta_x + j \sin \theta_x)}{N_x f_x} = \frac{(\cos \theta_s + j \sin \theta_s)}{N_s f_s} \left\{ \frac{R_x}{R_s} \left[1 + j\omega \left(\frac{L_x}{R_x} - \frac{L_s}{R_s} + \frac{M}{R_x} \right) \right] \right\}$$

and on separation

$$f_x = \frac{N_s R_s}{N_x R_x} \left(\frac{f_s \cos \theta_x}{\cos \theta_s - \sin \theta_s \omega \left(\frac{L_x}{R_x} - \frac{L_s}{R_s} + \frac{M}{R_x} \right)} \right)$$
$$\approx \frac{N_s R_s}{N_x R_x} + (f_s - 1) + \frac{1}{2} (\theta_s^2 - \theta_x^2)$$

and ⁴

$$heta_x = heta_s + \omega \left(rac{L_x}{R_x} - rac{L_s}{R_s} + rac{M}{R_x}
ight)$$
 radians

As previously indicated, R_s and R_m circuits constitute a burden on the reference transformer. Therefore, f_s and θ_s are determined with these burdens.

The design of the divider is such that the balance conditions require

$$\frac{N'_x}{N'_s} = \frac{R_s}{R_x} \ge 1,$$

this requirement is met by using reference transformers whose voltage ratios have been adjusted to values 2 percent less than the integral values usually encountered as nominal ratios in instrument transformers.

The minimum resolution of this setup is such that the precision for ratio measurements is 2 ppm and for phase angle, 15 μ rad at 25 and 60 Hz. However, susceptibility of the mutual inductor to the gradients of stray magnetic fields, large residuals and uncertainty in ratio calibration of the resistive divider limit the accuracy of these measurements to 10 ppm and 60 μ rad, respectively. These errors increase rapidly with frequency. Therefore, this comparator is not suitable for accurate calibrations beyond 60 Hz.

3. Inductive Comparator

This comparator avoids a number of the objectionable features of the resistive comparator. The shielding is simple and is easily accomplished; the method is capable of extension to cover the audio-frequency range; the apparatus is less bulky and requires very little power for operation; the components are inherently more accurate and stable and readily available commercially; the ratio difference is indicated directly on the decade dials; and the set (except the detector) is self-contained in one portable case.

The schematic diagram of the circuit is shown in figure 5, which indicates the positions of resistor, R, and detector when the voltage phasor of the test transformer, X, leads that of the reference transformer, S. For lagging angles, the positions of R and detector are

 4 The (f_s-1) and θ_s for the NBS reference transformers are very small. Therefore, the approximations made are justifiable.



simply interchanged. The in-phase voltage balance is obtained by adjusting the tap on the inductive voltage divider, and the quadrature voltage balance is obtained by adjusting either the resistor or the capacitor.

3.1. Theoretical Relations

As previously defined, the voltage ratio is

$$\frac{|V_{\text{Pri}}|}{|V_{\text{sec}}|} = N' = Nf,$$
$$= \frac{|V_p|}{N_s f_s} (\cos \theta_s + j \sin \theta_s),$$

and

 V_s

$$V_x = \frac{|V_p|}{N_x f_x} (\cos \theta_x + j \sin \theta_x) = \frac{V'_x}{m},$$

m being the actual ratio of the inductive voltage divider. Corrections to the ratios and the phase angles of the inductive voltage divider used are insignificant and can be neglected. Then from figure 5,

$$\frac{V_s}{V_x'} = \frac{1 - j\omega CR}{1 + \omega^2 C^2 R^2} \approx 1 - \omega^2 C^2 R^2 - j\omega CR$$

The balance equation of the circuit becomes

$$\frac{N_x f_x(\cos \theta_s + j \sin \theta_s)}{m N_s f_s(\cos \theta_x + j \sin \theta_x)} = 1 - \omega^2 C^2 R^2 - j \omega C R$$

and on separation and by neglecting second order terms

$$\theta_x = \frac{N_x f_x}{m N_s f_s} \left(\theta_s + \omega C R + \theta_s \omega^2 C^2 R^2 \right) \approx \theta_s + \omega C R$$

radians, and

$$\begin{split} f_x = & \frac{mN_s f_s}{N_x} \left(\frac{\cos \,\theta_x - \omega^2 \, C^2 R^2 + \omega \, CR \, \sin \,\theta_x}{\cos \,\theta_s} \right) \\ \approx & \frac{mN_s}{N_x} + (f_s - 1) - \frac{1}{2} \, (\omega \, CR)^2 \end{split}$$

For relative lagging phase angle, with the detector and resistor, R, interchanged,

$$\begin{split} \theta_x &\approx \theta_s - \omega \, CR \; \text{radians, and} \\ f_x &\approx \frac{mN_s}{N_x} + (f_s - 1) + \frac{1}{2} \; (\omega \, CR)^2 \end{split}$$

3.2. Loading Effects

Since the input impedance of the inductive voltage divider at 60 Hz is in excess of 0.8 M Ω (nearly 1.00

power factor), the effect of loading of the f_x and θ_x of the test transformer can be expressed as

= (Correction to f_x and θ_x).

A transformer with an equivalent resistance of 1 Ω (which is easily measurable) will result in a correction of approximately 1 ppm [5].

The effect of loading by the quadrature balancing network on either the inductive voltage divider or the reference transformer is usually insignificant. At the limits of phase-angle range of ± 37.8 mrad (130 min), the correction to ratio and phase angle of the inductive voltage divider is ~ 1 ppm and <4 μ rad, respectively; or, to the reference transformer, <<1 ppm and << 1 μ rad.

3.3. Description of Components

All the components in this circuit are readily available commercially or accessible in most testing laboratories. In figure 5, R is a 0–10 k Ω , 10-turn, continuously adjustable resistor with small residual reactance. C is a 10×1 nF decade capacitor with a dissipation factor of < 0.0003. Both R and C were calibrated before and after assembly. The deviations from nominal values were found to be within 5 Ω and 5 pF, respectively.

The inductive voltage divider is a six-decade unit designed for optimum performance at 60 Hz.⁵ For tests at higher frequencies (up to 1 kHz), it can easily be replaced by a unit of appropriate design, or by application of appropriate loading corrections to the ratio-correction-factor and phase angle. However, due to the simplicity of the circuitry and infrequent requests for calibrations at higher frequencies, a tem-

⁵ Since maximum voltage on this divider is limited to 150 V at 60 Hz, it must be connected across the test transformer rather than the reference transformer whose secondary voltage may exceed this value in some instances.



FIGURE 6.



FIGURE 7.

porary setup is generally used at NBS. This consists of a high frequency divider, commercially available air capacitors, and an a-c decade resistor.

The input impedance of the inductive voltage divider was measured in a capacitive voltage divider setup; see figure 7 for circuit diagram [10]. The voltage-ratio corrections and phase-angle errors were obtained by a tracking method [11]. However, for a well designed inductive voltage divider, these corrections are usually < 0.2 ppm of input in ratio linearity deviation and $< 8 \ \mu$ rad of input in phase deviation.

The detector circuit consists of a wave-analyzer used as a tuned null indicator and a specially constructed shielded transformer, shown in figure 6. This transformer is used to match impedances and to isolate the measuring circuit from the detector circuit electrostatically. It consists of a high permeability toroidal core, a 700-turn inner primary winding, two electrostatic shields, a 3500-turn outer secondary winding, and an overall Mu-metal case. The use of a toroid minimizes the magnetic pickup from stray fields in the laboratory.

The polarity checking voltmeter is a 0–150 V, 3-in. panel-type instrument of 3 percent accuracy. The 21 k Ω resistor matches its internal resistance. The voltmeter indicates the secondary voltage of the test transformer when the selector switch is in the "CALIBRATION" position. Figures 8 and 9 show the exterior and interior of

Figures 8 and 9 show the exterior and interior of the comparator. The cylindrical shield shown in the interior view encloses the 10-k Ω resistor.

3.4. Accuracy Determination

After the completion of the comparator, the calibration of each component was rechecked.

The accuracy of the completed comparator was verified by the following procedures: (1) connecting a single voltage source to both " V_s " and " V_x " terminals to obtain the zero corrections for C and R and the unity correction for dial settings of the inductive voltage divider; (2) calibrating several pairs of transformers of the same ratio to reference transformers of appropriate ratios, thus determining the relative ratio-correction-factors and phase-angle errors with several values of secondary burden for each pair of transform-



FIGURE 8.



FIGURE 9.

ers; (3) comparing each pair of these calibrated transformers to each other with the comparator alternately as reference and test units and with various combinations of previously used secondary burdens; and (4) connecting a single voltage source to " V_x " terminals and to the input of a calibrated inductive voltage divider, whose output is connected to " V_s " terminals in order to check the fractional ratio measurements.

3.5. Transformer Testing Procedure

A reference transformer is selected so that $N_s \ge N_x$. The selector switch is set to "CHE CK" position; and the ratio, *m*, is set to equal N_x/N_s . The secondaries of the test and reference transformers are connected to " V_x " and " V_s " terminals respectively. With the transformers energized and the voltmeter switch depressed, the voltmeter indicates $(V_s - V_x)/2$ if the polarities of V_s and V_x are correctly connected to the comparator. [The polarity of V_x must be reversed if voltmeter indicates $(V_s + V_x)/2$.] The test proceeds with the selector switch turned to "CALIBRATION." V_x is now indicated on the voltmeter.

If $N_x > N_s$ by more than 10 percent, the test can still be made, but the reference transformer must be connected to " V_x " terminal and the test transformer to " V_s ". At the balanced condition, the ratio, *m*, will indicate the reciprocal of the relative ratio-correctionfactors, and the relative phase-angle switch will show the negative of the markings as shown in figures 6 and 8.

3.6. Consideration of Errors

An examination of errors requires a careful appraisal of all components of the measuring circuit as well as a justification of estimations, even though in this case one would expect most of the associated errors to be negligible.

The first to be considered are those possible errors concealed through approximation in developing the expressions for obtaining the ratio-correction-factor and phase-angle relations, where the effects of the residual of the 10-k Ω resistor and the dissipation factor of the decade capacitor are ignored. Approximate magnitudes of these errors can be obtained by adding the estimated equivalent residual reactance across Rand estimated equivalent series resistance to C. These effects were found to be less than 0.5 ppm for ratio and 5 μ rad for phase angle at the limits of the phase angle range (± 38 mrad) at 60 Hz.

A second source of uncertainty is in the calibration of the inductive voltage dividers. However, past experience indicates that errors should not exceed 0.2 ppm of input in ratio linearity deviation and 2 μ rad of input in phase deviation [12].

Errors caused by the leads, switch contacts, and imperfect shields are estimated to be < 0.5 ppm and $< 2 \mu rad$.

The last important uncertainty that should be considered is in obtaining the transformer secondary load corrections. Such errors are estimated to be 0.5 ppm and 3μ rad for each transformer.

Finally, one should add 3 times the standard deviations (0.3 ppm of ratio and 1 μ rad) of the results obtained in repeated balances of the comparator to the combination of the systematic-error estimates.

Source of errors	Uncertainties to ratio-correction- factor		Uncertainties to phase angles	
	n	n^2	n	n^2
From accumptions mode in	ppm		μ rad	
equations made in	0.5	0.25	5	25
Calibration of the inductive voltage divider	.2	.04	2	4
rections (S)	.5	.25	3	9
Obtaining secondary load corrections (X) Leads and contacts	.5 .5	.25 .25	$\frac{3}{2}$	9 4
Σn^2		1.04		51
$(\Sigma n^2)^{1/2}$ 3 × standard deviations	$\begin{array}{c} 1.02\\ 0.90\end{array}$		$7.1 \\ 3.0$	
Maximum error	1.9 ppm		10.1 µrad	

4. Conclusion

A high accuracy inductive comparator has been developed for measuring the relative voltage ratio and phase angle of voltage transformers and testing sets at power and audio frequencies. An overall accuracy of < 2 ppm in ratio measurements and < 10 μ rad in phase angle at 60 and 400 Hz is achieved, with a slight reduction in the accuracy attainable at higher audio frequencies.

The simple circuit does not require any special equipment. Only a few ranges of reference transformers are required, since fractional ratios from 1/1 to 1/4 may be measured as readily as ratios near unity.

This inductive comparator could replace testing sets now in general use. It is compact and portable, has a broader useful frequency range, and exceeds the accuracy of many commercial sets presently available by one or two orders of magnitude.

The resistive comparator will continue to be used at NBS for calibration of voltage transformers at 25 Hz at which frequency the operating voltage is usually above the maximum operating voltage of the inductive comparator.

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